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Characterizing Impacts of Storage Faults on HPC Applications: A Methodology and Insights

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Abstract—In recent years, the increasing complexity in scientific simulations and emerging demands for training heavy artificial intelligence models require massive and fast data accesses, which urges high-performance computing (HPC) platforms to equip with more advanced storage infrastructures such as solidstate disks (SSDs). While SSDs offer high-performance I/O, the reliability challenges faced by the HPC applications under the SSD-related failures remains unclear, in particular for failures resulting in data corruptions. The goal of this paper is to understand the impact of SSD-related faults on the behaviors of complex HPC applications. To this end, we propose FFIS, a FUSE-based fault injection framework that systematically introduces storage faults into the application layer to model the errors originated from SSDs. FFIS is able to plant different I/O related faults into the data returned from underlying file systems, which enables the investigation on the error resilience characteristics of the scientific file format. We demonstrate the use of FFIS with three representative real HPC applications, showing how each application reacts to the data corruptions, and provide insights on the error resilience of the widely adopted HDF5 file format for the HPC applications.

I. INTRODUCTION

In recent years, the increasing complexity in scientific simulation and emerging demands for training heavy artificial intelligence (AI) models require massive and fast data accesses, which urges high-performance computing platforms to equip more advanced storage infrastructures. To this end, flashbased solid-state drives (SSDs) have been widely employed in HPC systems as a replacement of hard disk drives (HDDs) to achieve an order of magnitude speedup in data access. Prior studies [\[1\]](#page-11-0)–[\[3\]](#page-11-1) showed that this rapid adoption of SSDbased I/O components, however, raises a new challenge to the overall HPC reliability. As shown in [\[1\]](#page-11-0), uncorrectable bit error rate (UBER) of data center SSDs are between 10[−]¹¹ , 10[−]⁹ , which results in a collective high error rate on the large-scale HPC system and breaks the JEDEC 2016 requirement for enterprise class (10^{-16}) [\[4\]](#page-11-2). Such concern is expected to be continuously present due to the fact that the sources of SSD failures, i.e., cell wear-out, program/read disturb errors, retention errors, power faults, radiations, etc, would not dissolve shortly.

There are two classes of failures prominently experienced on SSDs: fail-stop and partial disk failures. Unlike the failstop failures that cause the SSDs to become inaccessible from the higher level of the stack, partial drive failures only affect a portion of the SSD operations and the device remains to work from the user's perspective [\[5\]](#page-11-3)–[\[12\]](#page-11-4). The consequence of the partial failures is severe: they may cause corrupted data on SSDs and trigger issues in the file system and application layers in the I/O stack. Therefore, HPC applications need to mitigate the impact of the partial disk failures and tolerate the data corruption propagation due to such failures from SSDs.

Mitigating data corruption in an HPC application has been a challenging task that requires efficient and effective solutions. Towards this goal, a wide spectrum of studies [\[13\]](#page-11-5), [\[14\]](#page-11-6) employ statistical fault injections to characterize the impact of hardware faults, and take the characteristics obtained on per application basis to guide the design of the fault tolerance strategy. However, since the common fault models investigated in those studies are bit-flip faults affecting computational units and memory, their insights are not indicative to reason about the application's error resilience characteristics against SSDrelated data corruption, which demands a new systematic characterization approach.

The goal of this paper is to provide methodology for characterizing how different types of SSD-related faults would affect the behaviors of the HPC applications. We mimic the impact of partial disk failures on applications by introducing faults via an application's I/O operations and observe the outcomes of the applications after the fault injection. We introduce a fault injection framework, FFIS^{[1](#page-1-0)} (FUSE-based Fault Injection for Storage) that leverages the FUSE [\[15\]](#page-11-7) interface to systematically inject faults into the applications' I/O path. FFIS supports multiple fault models, each of which represents a specific data corruption scenario observed from the partial disk failures. FFIS offers a uniform interface for users to apply fault injection campaigns on various real HPC applications, without any modification on the applications.

FFIS is built based on the following key assumptions: *(i)* we focus on the data corruption that manifests on the

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¹https://github.com/FabioGrosso/pFsysFI.

application level, such as bit flips, shorn writes, etc. We explain the details of these faults in Section [IV.](#page-4-0)*(ii)* those errors can further propagate beyond the file system layer [\[16\]](#page-11-8)–[\[18\]](#page-11-9), skip the verification mechanisms (e.g., fsck [\[19\]](#page-11-10)) and silently compromise the data integrity of the applications.

Enabled by FFIS, systematic fault injection studies can be performed to reveal error resilience characteristics of an application or the common components exercised across applications: *(i)* FFIS is able to evaluate different inherent error masking capabilities for different applications, or to measure such ability of each phase of an application. This suggests a potential trade-off space for HPC systems to lower the requirement of the SSDs' reliability for faster data access while maintaining the same level of the overall application's reliability; *(ii)* as the modern HPC applications tend to leverage the scientific file format for efficient data management, FFIS is able to investigate how the certain scientific file format library handles the storage errors affecting both the file metadata and application data, thus to obtain the possible common error resilience characteristics of the applications while operating on such file format. This paper makes the following contributions:

- We design and build a fault injection framework, called FFIS, to model SSD-related failures at software level and to inject such faults systematically into HPC applications.
- We apply FFIS on three real-world HPC applications through comprehensive, large-scale fault injection experiments. Our evaluation shows that applications exhibit distinct error resilience characteristics for different fault models, and we offer the detailed explanation for each application's unique resilience characteristics.
- We unveil application-specific behaviors operating on the most widely adopted scientific file format - HDF5, and show the fault-tolerance behaviors of the HDF5 library against errors affecting the HDF5 metadata. We identify certain fields in the metadata that may cause SDC outcomes when affected by faults, and provide the solutions for auto-correction. To the best of our knowledge, it is the first research effort to systematically characterize the detailed error resilience of the HDF5 file format.

II. BACKGROUND

In this section, we describe the key context for our study and its importance to motivate the FFIS framework development.

Fault, error, and failure: As described in [\[20\]](#page-11-11), a (hardware) fault, error, and failure chain is defined as the following events: "a service is a sequence of a system's external states, a service failure means that at least one (or more) external state of the system deviates from the correct service state. The deviation is called an error. The adjudged cause of an error is called a fault". Below we specify these terms in the different context:

• *Storage system*: an SSD's partial failures refer to the events where SSDs are not providing the expected behavior or outcomes such that the SSDs' internal states are left with flipped bits or shorn data, and what causes such failures are considered as hardware faults, including power faults or ratification faults, etc..

- *File system*: the file system failures refer to the unsuccessful file operations such as I/O errors returned to the application, which can result from the storage failures, or software bugs (not covered in this study).
- *Application*: a failure of an application refers to that scenario that the outcome of the application differs from the expected: the application either terminates before it finishes (i.e., *crash*), or it suffers from data corruption. If the application is able to identify the errors, this failure is categorized as *detected*, otherwise such data corruption becomes silent data corruption (SDC).

FUSE File system in user-space is the most widely used user-space file system framework [\[15\]](#page-11-7) on Unix/Linux systems. It exposes the file operations to the file system users and allows the users to implement their own file operations such as *open(), read(), write(),etc.* if needed. When a FUSE file system is in use, the programs are able to access the data using the standard file operation system calls (i.e., POSIX) through the implementation of those system calls in the FUSE.

Why choose FUSE We choose FUSE as the underline file system interface for the fault injection framework for two reasons: *(i)*, as the goal is to study the impact of the failures on applications, the fault injection framework focuses on mimicking the SSD-related faults occurring during the I/O operations in the application level. FUSE allows us to implement such faults with a relatively straightforward manner;*(ii)*, since a FUSE-based file system works as the callbacks for the file operations, it allows the applications to call the userimplemented I/O primitives without modification on either the application or the actual running file system. This releases the burden for HPC applications that usually consists of complex behaviors and conservative execution environment.

Manifestation of SSD failures Zheng et al. [\[21\]](#page-11-12) found that the power faults can cause a large number of SSDs to fail partially and produce the number of chip-level bit errors that exceed the correction capability of SSDs' error correcting code (ECC) and bypass the SMART (Self-Monitoring, Analysis and Reporting Technology) system [\[22\]](#page-11-13). For example, A recent study [\[1\]](#page-11-0) reports that on SSDs the occurrence of partial drive failures that lead to data corruption can be an order of magnitude higher than on HDDs [\[1\]](#page-11-0). As shown in [\[16\]](#page-11-8), this type of the SSD failures can manifest in the file system and affect the application's I/O behaviors. This leads to two classes of failures in general: *(a)* the file system throws the I/O errors and leaves the handling to the application and the typical failures include uncorrectable bit corruption, metadata corruption, incomplete I/O operations; *(b)* the file system does not detect such failures and the failures may cause data corruption in the application. These failures include silent bit corruption (bit flips in the data), shorn write (a write operation is partially done on the device), and dropped write (the file system issues the write but never gets executed on the device), which is the focus of this paper.

Why focusing on HDF5 file format Hierarchical Data Format version 5 (HDF5) provides an API for performing I/O, data management tools, and a portable file format [\[23\]](#page-11-14). In

Fig. 1: Overview of HDF5 file structure (top), layout of datatype message (middle), floating-point property (bottom).

[\[24\]](#page-11-15), the authors show that HDF5 is the most used I/O library on HPC systems at the National Energy Research Scientific Computing Center (NERSC) and at several US Department of Energy(DOE) supercomputing facilities, including the Leadership Computing Facilities (LCFs). HDF5 has a rich ecosystem and various third-party bindings are available to manage data [\[25\]](#page-11-16). For instance, Matlab uses HDF5 as the primary storage format [\[25\]](#page-11-16) to allow the operations on the numeric data. Considering this rich eco-system and popular usage, verifying the resilience of the file format with FFIS offers significant insights for both HDF5 and application developers.

HDF5 file structure Figure [1](#page-3-0) depicts a general structure of the HDF5 format [\[26\]](#page-11-17). The HDF5 format defines a cascading style metadata: it holds a super block that points to multiple groups, and each group represents an object header that may store other groups or datasets within it. A dataset represents the actual data contained within the HDF5 file. It contains an object header that describes the data space, the data type, the data layout, and other useful information. Internally, HDF5 uses B-tree nodes to index where a particular information is stored. For example, we show the detailed layout and relation of datatype message (middle), and floating-point property (lower) in Figure [1.](#page-3-0)

III. FFIS FRAMEWORK

In this section, we present the design and implementation of our fault injection framework - FFIS. We first describe the overall system design of FFIS with the focus on the general workflow interacting with the FUSE file system I/O path. Then, we describe the fault models currently FFIS supports, and finally, we explain each component of FFIS and highlight the key features for systematically conducting error resilience characterization on HPC applications.

A. Design Goal

The goal of FFIS is to mimic the SSD failures as softwareimplemented faults that corrupt the application data in a controlled manner. To this end, there are four requirements that FFIS needs to follow:

- *Transparency:* FFIS should be able to transparently plant a fault into an application at runtime without modifying the application code (R1). This requires that FFIS should not make any assumptions about what specific file systems that the application work with or what specific I/O operations the application needs, and ensures that the application's calls to the I/O system remains unaffected due to any fault injection framework's artifacts.
- *Convenience:* As HPC applications tend to be conservative and sensitive to the computation environment, the FFIS framework should be deployed without requiring modification the compilation/execution environment of the application (R2).
- *Comprehensiveness:* FFIS should support multiple fault models that represent different types of SSD-related failures. This extends the bit-flip based fault models (R3).
- *Repressiveness:* as the goal of FFIS is to mimic the SSDrelated failures at software level, it is important that FFIS can introduce the faults uniformly over the set of all corresponding file operations (R4).

In Figure [2,](#page-4-1) we illustrate the overview design of the FFIS framework. FFIS employs the FUSE interface to introduce a standard file system for the application to use. The application under the characterization runs on top of the *FFISFS* file system, which can work in parallel with the actual file systems exercised by the application such as lustre, GPFS, ext4, etc. *FFISFS* works similarly to what normal FUSE-based file system does: at the time the *FFISFS* file system is mounted, the file system handler is registered with the OS kernel. If an application issues, for example read/write/stat requests for the mounted *FFISFS*, the kernel forwards these IO-requests to the handler and then sends the handler's response back to the user.

That said, FFIS intercepts the I/O system calls via instrumenting the file system primitives inside the FUSE interface without any change on the application code, which addresses the R1. Since FUSE offers the uniform API that is agnostic to how the application invokes the I/O, FFIS is able to support different applications without worrying about the specific set of APIs that the application employs. To invoke the FFIS framework for fault injection, the application only needs to make sure that the requested file(s) reside in the mount point of *FFISFS*, which addresses the R2.

B. Fault Model

FFIS currently supports three types of faults, each of which corresponds to a SSD failures' manifestation: BIT FLIP, SHORN WRITE, and DROPPED WRITE. As discussed in Section [II,](#page-2-0) these fault models could cause different types of data corruption in

Fig. 2: Overview of FFIS framework based on FUSE.

applications. Table [I](#page-4-2) summarizes what file system operations can potentially be used to host the fault for each fault model and the implementation specification FFIS defines for such fault. These specifications are aligned with the observations from the prior studies [\[16\]](#page-11-8), [\[21\]](#page-11-12).

TABLE I: Fault models supported by FFIS. The table reports the examples of the FUSE primitives where the faults can manifest, and the key features FFIS implements for each fault model.

Fault model	Examples of Affected FUSE primitives	Features
Bitflip	FFIS_write, FFIS_mknod,FFIS_chmod	flip consecutive multiple bits ²
Shorn write	FFIS write, FFIS mknod, FFIS chmod	completely write the first 3/8th of 4KB block or first 7/8th of 4KB block to the device at the granularity of 512B
Dropped write	FFIS write, FFIS mknod, FFIS chmod	the write operation is ignored

FFIS instruments the primitives and modifies the file state either in the metadata structures or the data blocks. Figure [3](#page-4-3) shows two examples of the instrumentation strategy performed by FFIS. In particular, for the *FFIS write* primitive, FFIS modifies the parameters of the *FFIS write* depending on the fault model and pass the modified content to the underline file system interface - *pwrite* in this case (In Section [IV](#page-4-0) we explained the details of the instrumentation on *FFIS write*); For the *FFIS mknod* primitive, it conducts the similar operations on the parameters based on the fault model, and the modified data are sent to *mknod* and *mkfifo* system calls respectively.

C. FFIS Workflow

FFIS consists of three components: the fault generator, the I/O profiler and the fault injector. Below we explain details of each component, and depict the overall workflow of the FFIS framework in Figure [4.](#page-5-0)

Fault Generator The fault generator reads the configuration specified by the user to produce a fault signature, which includes the fault model, the file system primitive where the fault would be injected for that fault model, and the choice of the feature associated with the fault model. The fault signature then gets passed to both the I/O profiler and the fault injector.

I/O Profiler The goal of the I/O profiler is to count the number of times that the primitive (i.e. configured in the fault signature) gets executed during the execution. To this end, the I/O profiler instruments the primitive inside the FUSE and executes the application fault-free to obtain the total count. It then passes this dynamic count of the primitive to the fault injector.

(a) The FFIS instrumentation on *FFIS write* primitive. It modifies the content of the BUFFER, SIZE AND OFFSET passed to the *FFIS write*, and the modified content is fed to the system call *pwrite*.

(b) The FFIS instrumentation on *FFIS mknod* primitive. It modifies the content of the MODE, AND DEV passed to the *FFIS write*, and the modified content are fed to the system call *mknod* and *mkfifo*.

Fig. 3: Examples of how FFIS performs fault injection by modifying content of target primitives based on fault models.

Fault Injector The fault injector performs the actual fault injection operations with the fault signature, including the fault model, the feature and the primitive, and the dynamic count obtained from the profiler. For each fault injection run, it first generates a random number from 0 to count-1, and executes the application normally. When the execution count of the target primitive hits that random number, the fault injector applies the fault based on the fault signature. This process is repeated until the statistical significance is reached.

In summary, the whole workflow of the fault injection proceeds as follows: FFIS loads the user configuration of the application, the fault model, the target primitive and the feature of the fault model, and launches the application in the mount point of FFISFS for profiling. Once it obtains the total executed count for the target primitive, it enters the fault injection mode: for each fault injection run, it randomly chooses an instance of the execution of the primitive, and plant the fault based on the fault signature with the fault specification described in Section [III-B.](#page-3-1) Note that in each run, FFISFS would be mounted and unmounted to mimic the real scenario on the HPC system for the application.

IV. EVALUATION METHODOLOGY

In this section, we present our evaluation methodologies. We first introduce our experimental platform and then present our fault models and their specifications. Lastly, we describe our evaluation applications and our fault injection approaches.

A. Experimental Platform

We perform our fault injection experiments using a local server with 24-core AMD Ryzen Threadripper 3960X CPUs. We also use the Frontera system [\[27\]](#page-11-18) (equipped with two Intel Xeon Platinum 8280 CPUs each node) at the Texas Advanced Computing Center for simulations and post-analyses.

Fig. 4: FFIS workflow that illustrates the process of systematically introducing a fault into the application's I/O path.

B. Fault Model Specification

As discussed in Section [III,](#page-3-2) the fault injection experiments incorporate a fault signature to determine the fault model based on the FUSE primitive and the fault feature. In this study, we consider three fault models: BIT FLIP, SHORN WRITE, and DROPPED WRITE. To efficiently and directly study the impact of data corruption, we choose the *FFIS write* primitive to implement all the fault models, while each fault model has its own feature and the implementation strategy:

- BIT FLIP: FFIS flips 2 consecutive bits randomly chosen in the buffer that is used in *pwrite* system call inside the *FFIS write* for each fault injection run.^{[3](#page-5-1)}
- SHORN WRITE: FFIS modifies a write operation to lose the last $\frac{1}{8}$ th of the data by stripping down the buffer used in *pwrite* system call. While the buffer shrinks, the size is yet kept as the original value: this would introduce undefined data to write to the file system, which copes with the shorn write failure on SSDs.
- DROPPED WRITE: FFIS ignores the *pwrite* call for that instance inside the *FFIS write* and sets the return value of the *FFIS write* to the original size of the data buffer.

C. Evaluation Applications and Fault Injection Approaches

We conduct our experiments on three representative realworld HPC applications: Nyx [\[28\]](#page-11-19), QMCPACK [\[29\]](#page-11-20), and Montage [\[30\]](#page-11-21). The common feature of these three applications

³All the bit-flip results shown in the paper are based on the observation associated with this fault model. We also tested the 4-bit bit flip model and the SDC rate remains minimal for Nyx.

are that they all have a large number of I/O operations and their own post-analysis processes. A large number of I/O operations mean that there could be more I/O faults, and the post-analysis process defines the impacts of these faults on each application. We use Table [II](#page-6-0) to list some key information of our evaluation benchmark applications. For each application's dataset under each fault model, we conduct 1,000 fault injection runs to obtain a statistically significant estimate, which leaves a 1%∼2% error bar on average for 95% confidence interval.

1) Nyx: Nyx [\[28\]](#page-11-19) is an adaptive mesh, hydrodynamics algorithm designed to model astrophysical reacting flows. This code models dark matter as discrete particles moving under the influence of gravity. The fluid in gas-dynamics is modeled using a finite-volume methodology on an adaptive set of 3-D Eulerian mesh. The mesh structure is used to evolve both the fluid quantities and the particles via a particle-mesh method.

It is worth mentioning that Nyx has multiple post-analysis programs, such as power spectrum (statistically describing the amount of the Universe at each physical scale) and dark matter halos (over-densities in the dark matter distribution). In this work, we select the most popular post-analysis: HALO FINDER (based on the Friends-of-Friends algorithm [\[31\]](#page-11-22)), which aims to find the halos using the "baryon density" field in the dataset and output the positions, the number of cells, and mass for each halo it finds, respectively.

Outcome Classification Similarly, we compare the output of the halo finder (e.g., NVB_integral_512 for $512 \times 512 \times$ 512 Nyx datasets) of the fault injected case with the original output. If they are bit-wise identical, they are classified as benign. If they differ, and there is no halo found, the cases are detected and otherwise they are the SDC.

2) QMCPACK: QMCPACK [\[29\]](#page-11-20) is an open-source, highperformance electronic structure application that implements numerous Quantum Monte Carlo (QMC) algorithms for electronic structure calculations of molecular, periodic 2D/3D solid-state systems. Real-space Variational Monte Carlo (VMC), Diffusion Monte Carlo (DMC), and other advanced QMC algorithms are implemented in this package.

Fault Injection Preprocess We choose the Diffusion Monte Carlo (DMC) algorithm in QMCPACK. It first runs VMC to generate a set of walkers and then performs DMC. Finally, there will be two types of output files – 000 for the VMC algorithm and 001 for the DMC algorithm – both of which contain large numbers of floating-point data.

We then use the QMCA tool in QMCPACK to obtain the total energies and related quantities. Since there is only one electron of each spin, DMC is supposed to reproduce the exact non-relativistic ground state energy (-2.90372 Hartree) [\[32\]](#page-11-23). Thus, by analyzing the energy change after the fault injection, we can observe the impacts of the faults on QMCPACK.

Outcome Classification To define the class of an outcome, we first bit-wise compare the output file He.s001.scalar.dat of each fault injected case with the original (fault-free) output file. If the two files are the same, this case will be identified as benign. Next, if this case is not benign, we continue to

TABLE II: Description of tested HPC applications.

Benchmark	Domain	Package Size	LoC	Method
Nvx	Astrophysics	71.9MB	21K	Adaptive mesh refinement (AMR) based cosmological simulation
OMCPACK	Ouantum Chemistry	381MB	403K	Quantum Monte Carlo simulation for electronic structures of molecules
Montage	Astronomy	126MB	31 K	Astronomical image mosaic

perform QMCPACK's post-analysis to obtain an energy value. Since QMCPACK itself allows minor confidence intervals, we rely on such information to define the boundary between SDC and detected. After communicating with the QMCPACK development team, we decide that if the final energy value remains in [-2.91, -2.90], we will define this case as SDC; otherwise as detected.

3) Montage: Montage [\[30\]](#page-11-21) is a toolkit to assemble Flexible Image Transport System (FITS) images into custom mosaics. In this paper, we create a mosaic of 10 2MASS Atlas images in a 0.2-degree area around m101 in the J band, reprojecting them into the TAN projection. We generate both backgroundmatched and uncorrected versions of the mosaic. The final product includes two mosaics of m101 and their corresponding area images, which are used by Montage when co-adding images together to form a mosaic. Specifically, it takes Montage ten stages to generate the final image from the beginning, four in which involves a large amount of I/O reads and writes. So, we inject the faults in different stages and then analyze the final results to study the fault propagation in this program.

Outcome Classification To determine each category of the outcomes, we first bit-wise compare the output image m101 mosaic.jpg of each fault injected case with the original (fault-free) output image. If the two images are the same, the case is identified as benign. Then, to determine if a faulty case is SDC or detected, we analyze the output of the process that uses the *fits* file to generate image in the last step. As we (1) observe that the "min" value in the output greatly correlates with the correctness of the final image, and (2) consider the round of error during the computation, we accept a threshold of 10^{-2} as the difference between the fault injected one and the fault-free one. As a result, we will define this case as SDC, if the "min" value of the last step is in [82.82, 82.83]; otherwise as detected. Figure [9b](#page-10-0) is a typical example of the case where the min is beyond such range. For the cases where the target file cannot be created, they are defined as crash.

For each benchmark application, we first answer the question: "How do the HPC applications react to different SSDrelated failures that propagate to the application data?". Then, we conduct a detailed analysis to reason about cases for the different failure categories.

D. HDF5 Metadata Fault Injection

We describe the approach to investigate how a storagerelated error occurring in the HDF5 metadata affects the application's error resilience.

This approach is based on the following observation: when an HDF5 file is created, the HDF5 library first locks the file to prevent the concurrent writes from other processes, and then performs multiple writes to store the raw data; after that, it packs all metadata and write them to the file and unlocks the file for later access.

Based on this procedure, FFIS identifies the specific write operation for metadata (i.e., the penultimate *fwrite*) and then perform a fault injection starting from the offset value specified by the *fwrite* and till the end of the buffer byte-by-byte. Note that we refer to the HDF5 File Format Specification [\[33\]](#page-11-24) to capture the field information of each metadata byte and analyze the results accordingly.

V. EVALUATION RESULTS

In this section, we present our fault injection results on the target HPC applications, evaluate the fault robustness of the applications, and discuss some observations and insights.

A. Results for Faults Affecting HDF5 Metadata

In this section, we present the evaluation result of the cases where the fault affects the HDF5 file metadata for the Nyx cosmological simulation application.

We first show the overview of the results of fault injection in the HDF5 metadata of Nyx dataset in Table [III.](#page-8-0) As shown in the table, 0.2% of the total cases lead to SDC, which means that they cannot be detected by the post-analysis procedure; 85.7% of the total cases have the same as the original (faultfree) data (i.e., benign); and 14.1% of the total cases cause the Nyx application to crash (mainly due to the exceptions thrown by the HDF5 library, indicating the values in the fields become unjustified by the library).

Below, we analyze the benign and SDC cases and associate the results with the injected fields of the metadata in detail.

Analysis of the benign cases: there are two types of fields contributing to the benign cases that are the dominant types of the HDF5 file format related failures:

- 1) Based on the HDF5 format specification and the HDF5 library's default space allocation policy, most of bytes of the total metadata belong to reserved fields, alignment space between fields, and space reserved for future metadata. For example, the B-tree nodes that accounts for 72% of the total metadata space, can be partially full (i.e. 10%), which leads to the situation that much of the space in the file metadata remains unused. The faults affecting these bytes would not make any impact on either the integrity of the HDF5 files or the post-analysis procedure.
- 2) There are certain fields that exhibit resilient behaviors for the HDF5 files. Some examples are as follows:
	- BIT PRECISION: It represents the field of obj-Header.dataType.floatingPointProperty and it shows the number of bits of precision of the floating-point value within the datatype.
- BIT OFFSET: This maps to the obj-Header.dataType.floatingPointProperty field and represents the bit offset of the first significant bit of the floating-point value within the datatype. Therefore, the faults affecting this field might not cause much change to the floating-point value.
- SIZE: The size field defines the obj-Header.layout.contiguousStorageProperty. This field contains the size allocated to store the raw data, in bytes. We observe that if a fault modifies the size to a bigger value, the application would still produce the correct output, otherwise a crash would occur.

Analysis of the SDC-causing fields: It is also worth noting that there are 6 potential metadata fields that may cause SDC, including *Bit-5 of Mantissa Normalization*, *Exponent Location*, *Mantissa Location*, *Mantissa Size*, *Exponent Bias*, and *Address of Raw Data (ARD)*. We describe each possible SDC case based on their errors in terms of halo mass, halo locations, number of halos, and the average value of input data in post-analysis (as shown in Table [IV\)](#page-9-0), and select 3 typical cases to visualize.

When there is a faulty *Exponent Bias*, as shown in Figure [5b,](#page-7-0) the mass of all found halos change by the same multiple (i.e., scaled), while all locations are unchanged.

When a Faulty *ARD* occurred, the input data will be shifted, as shown in Figure [5c.](#page-7-0) This causes all found halos' locations to shift, while the mass stays unchanged.

When *Exponent Location*, *Mantissa Location*, or *Mantissa Size* is changed, some halos will be missing or some additional halos will be found. Figure [6](#page-7-1) shows two halos found by the golden run and SDC run caused by the fault in *Mantissa Size* field.

Fig. 5: Visualization of typical SDC cases. A faulty Exponent Bias (b) scales up the input data; a faulty ARD (c) shifts the input data.

Thus, we conduct an in-depth study on these metadata fields, and propose a novel and specific approach to potentially help HDF5 library detect and correct the faulty value residing in those fields.

1) *Detection approach to identify which field is incorrect:* We first introduce the detection mechanism to identify which field may be affected by the fault. For Nyx, we observe that the average value of original input data in Nyx should remain 1 due to the law of mass conservation. Therefore, if the average value of the input data is not 1, which indicates that there might be an error in one of

(a) Original (b) Faulty

Fig. 6: Visualization of a halo with a faulty Mantissa Size field (right). A box indicates a halo cell candidate that meets the threshold. In the faulty case, the number of halo cell candidates is reduced compared to the original case (left) thus there are not enough halo candidates to form a halo.

these fields. Next, we examine the actual average value, and classify the value to speculate where the fault resides:

- If the average value of the input data is the power of 2, the *Exponent Bias* field might be erroneous.
- If the average value is between 1 and 2, the "datatype massage" in metadata can be used to determine whether there is a fault within *Exponent Location*, *Mantissa Location*, or *Mantissa Size* fields.

2) *Correction methodology:*

- To correct the fault in *Exponent Bias* field, one can re-scale the value of the *Exponent Bias* field based on the average value observed. For example, our experiment shows that if the average value becomes 4096 after fault injection, the *Exponent Bias* changes from 0x0000007f (the *Exponent Bias* for IEEE singleprecision floats) to 0x00000073, and this value can be corrected via added by 12 (i.e. 2^{12})
- Due to the constraint of these three fields in floatingpoint representation (e.g., 8-bit exponent is saved next to 23-bit mantissa in single precision), users can fix the fault by making sure that (1) the value of *Exponent Location* is equal to the value of *Mantissa Size* (e.g., 23), and (2) the value of *Mantissa Size* plus the value of *Exponent Size* (e.g., 8) is equal to the precision number minus 1 (e.g., 31 , due to 1 sign bit).

However, for faulty *ARD*, unlike the above SDC cases, users cannot determine whether there is a fault based on the average value of input data (because it remains 1), causing a severe impact on the post-analysis result. Thus, we need to introduce a protection mechanism to prevent this harmful SDC case. We note that the metadata is saved followed by data in the HDF5 file format, the *ARD* is exactly equal to the size of metadata. As a result, we can efficiently detect and correct the faulty *ARD* by changing it back to the metadata size.

Insight on HDF5 Metadata We observe that due to the high fault tolerance of Nyx's post-analysis, the faults in HDF5 metadata will have a relatively small impact on the halo-finder analysis result in general. But there is still a low probability of

Fig. 7: Characterization result of I/O faults with Nyx, QM-CPACK, and Montage. Note that all SDC cases with Nyx will be changed to detected cases after using the average-value-based method. "NYX", "QMC", and "MT1/2/3/4" represent Nyx, QMC-PACK, and different stages in Montage, respectively. "BF", "SW", and "DW" represent BIT FLIP, SHORN WRITE, and DROPPED WRITE, respectively.

SDC case that will result in a very serious impact. Therefore, we propose an average-value-based method for users to detect SDC faults in Nyx's post-analysis and protect the key fields in HDF5 metadata, which further improves the fault tolerance of Nyx application against storage faults.

We also note that the baryon density field in Nyx can be easily compressed (i.e., compression ratio ranging from tens to hundreds) [\[34\]](#page-11-25), [\[35\]](#page-11-26), thus the importance of metadata would be greatly raised due to its increasing portion in the whole file. And since some metadata fields are related to each other, certain faults in the metadata can be detected and corrected as aforementioned; in other words, as the metadata of HDF5 file format itself has a certain degree of redundancy (correlation), we do not choose to replicate the metadata.

B. Results for Faults Affecting Application Data

In this section, we evaluate error resilience of Nyx, QMC-PACK, Montage when the errors affect the application data.

a) Nyx: Figure [7](#page-8-1) shows the result of halo-finder analysis on Nyx's baryon density variable with injected faults. For BIT FLIP, there are 91.1% cases that Nyx produces the exact results compared to the golden, and only 0.8% SDC cases occurred, which is the lowest SDC rate among the three applications. For SHORN WRITE, surprisingly, there is no affect on the halo-finder analysis (i.e., all the cases are Benigns). And for DROPPED WRITE, 1000 out of 1000 injected faults cause SDC outcomes.

We explain the reasons for the differences in the impact of different fault types on the post-analysis results. As aforementioned, the halo-finder algorithm searches for the halos from all the simulated data, with the following two criteria: (1) the mass of an object(s) must be greater than a threshold (e.g., 81.66 times the average mass of the whole dataset) to become a halo cell candidate [\[34\]](#page-11-25), [\[35\]](#page-11-26), and (2) there must be enough halo cell candidates in a certain area to form a halo. Below, for each fault type, we explain in details how each fault type potentially affects the halo-finder procedure.

- 1) Bit Flip Although the bit-flip error only affects one data point of Nyx, the halo finder may fail to find all the Halos. This is mainly because when the mass of a certain point in the dataset changes sharply, the average value of the input dataset changes accordingly, which causes the mass of all points in the dataset to be less than the threshold. The consequence is that no halo candidates can be found (i.e. detected). On the other hand, when the change to the average value is not significant, the original halo candidates would still satisfy the searching criteria, but a particular point affected by the fault may cause the outcome to be slightly different than the golden run (i.e. SDC). Since most of the points do not participate in halos, the chance for the halo finder to produce the correct halos dominates the outcomes (i.e. benign).
- 2) Shorn Write SHORN WRITE replaces the unsuccessfully written data with the data that is within an order of magnitude difference from the original data, which results in all faults being very small. We confirmed this by checking the values of the changed file. Thus, due to the characteristics of halo finder, these "moderate" faults are all mitigated.
- 3) Dropped Write Unlike shorn write, a DROPPED WRITE fault drops a large piece of data, leading to a change in the average mass of the dataset. Thus the halofinder algorithm would find a different number of cells in a halo compared to the golden run, and this can not be mitigated by the halo finder procedure. Figure [8](#page-9-1) shows the distribution of the mass for the identified halos on faulty and original data. Note that the SDC curve is different from the original curve, especially when the mass is relatively large, because halos with larger mass have more halo cells and are more susceptible to DROPPED WRITE.

Observation and Insight Although DROPPED WRITE has a 100% of SDC rate, all the SDC cases in our experiment can be detected by using the average value, because the average value is reduced by at least 0.1% (e.g., less than 0.9983) for all the SDC cases. Thus, we recommend Nyx users to keep using the average-value-based method to protect the data from storage faults with respect to halo-finder analysis. Moreover, as explained above, an important reason is that the halofinder process is sensitive to large deviations, while the small

Fig. 8: Comparison of halo-finder analysis on original and faulty baryon density data in Nyx. The x-axis represents the halo mass. the left y-axis represents the counts of halos.

deviation introduced by BIT FLIP would not compromise the outcomes. As a result, Nyx is highly resilient to storage faults after using the average-value-based method.

b) QMCPACK: Figure [7](#page-8-1) illustrates the result of fault injection with QMCPACK. Through the figure, we can observe that the fault injection results with different types of faults are similar. Specifically, in each fault model, there are about 50% of the faults to be SDC; in BIT FLIP, it even reaches 60%. Also, in BIT FLIP 37% of the faults are SDC and 0.8% of the faults are detected. In SHORN WRITE 54% of the faults are SDC with no case is detected. In DROPPED WRITE 8% of the faults are SDC, 43% of the faults are detected and 12% of the faults are crash. This is because floating-point numbers can mitigate fault to a certain extent.

Compared to BIT FLIP, SHORN WRITE. has a higher percentage of SDC and hence a higher impact on QMCPACK, which is reasonable. This is because unlike BIT FLIP only changes two bits, SHORN WRITE could affect 512 Bytes. It is worth noting that all the SHORN WRITE faults are SDC while some of BIT FLIP faults are detected. This is because, on one hand, BIT FLIP simply flip two bits in a floating-point number; if the fault happens to occur in the most significant bits, then BIT FLIP will greatly affect the floating-point value, thus greatly affecting the final result. On the other hand, the principle of SHORN WRITE is to discard the portion of data and replace them with random values. From the result, the difference between the replacement value and the original value is minimal.

Similar to SHORN WRITE, the impact of DROPPED WRITE is milder than that of BIT FLIP. But compared to SHORN WRITE, the number of bits changed by DROPPED WRITE is larger. This results in that the final output of DROPPED WRITE deviates more from the correct energy compared to SHORN WRITE. Although the difference is not very large, 43.6% of the cases exceed the threshold (i.e., 10%) that we set for SDC. Therefore, the detected cases in DROPPED WRITE are more than the other two types of faults.

Observation and Insight QMCPACK is not resilient to BIT FLIP and SHORN WRITE, as they have a high likelihood to cause a minor deviation in the outcomes. That said, to improve the error resilience of QMCPACK, more advanced techniques guided by more domain knowledge need to be considered.

c) Montage: We report the fault injection results of Montage in Figure [7.](#page-8-1) We select the most four I/O-intensive stages for our fault injection: (1) mProjExec for reprojecting each image, (2) mDiffExec for subtracting each pair of overlapping images and creating difference images, (3) mBgExec for applying background matching to each reprojected image, (4) mAdd for generating a mosaic from reprojected images.

We investigate the characteristics of potential fault propagation in Montage, that is, to study if the impact of the faults has time dependency. We execute BF1 (mProjExec), BF2 (mDiffExec), BF3 (mBgExec), and finally execute BF4 (mAdd) sequentially, and the results of fault injection experiments for different are presented in Figure [7.](#page-8-1) By observing the number of SDC and benign cases in each Montage stage for BIT FLIP and SHORN WRITE, we find that in the BIT FLIP cases all the numbers stay relatively stable across different stages (e.g., 12.8%, 8%, 9%, 6.8%). In the SHORN WRITE cases, the fluctuation of the SDC and benign cases vary slightly across different stages (e.g., 56.6%, 40%, 52.5%, 48.5%), while there is no indication on a statistically significant trend observed. As of DROPPED WRITE, the SDC and benign cases vary in the same way as SHORN WRITE, but more drastically (e.g., 83.5%, 37.3%, 98.3%, 50.4%).

Interestingly, in stage two (i.e., mDiffExec), the data (diffdir) generated is not directly applied to the subsequent steps, but to be used to calculate plane-fitting coefficients for each difference image through the second stage, which potentially be mitigated in the process of extracting coefficients. This explains why the SDC rate in the second step is the lowest among the three types of faults.

Figure [9](#page-10-0) shows a typical example of faculty image due to DROPPED WRITE. It can be seen that there is a black line in the middle of the vortex, which is caused by missing a large piece of data due to DROPPED WRITE.

(a) Original (b) Faulty (detected)

Fig. 9: A typical faulty image due to DROPPED WRITE.

Observation and Insight For Montage we observe the relative small fluctuation on the SDC rates for BIT FLIP and SHORN WRITE across the four stages. This indicates that instead of "fault masking" or "fault propagation" behaviors, different Montage stages seem to bound the faults and the error resilience on each stage decouples from each other.

VI. RELATED WORK

Characterization of HPC Application Error Resilience There have been a large body of studies focusing on estimating the error resilience of HPC applications via fault injection experiments. For example, Ashraf et al. [\[13\]](#page-11-5) propose a fault injection and propagation framework that tracks the transient hardware faults within a process and across MPI processes. Calhoun et al. used FlipIt [\[14\]](#page-11-6), an LLVM-based FI tool, to investigate how corrupted data propagate through a specific HPC computation kernel [\[36\]](#page-11-27). There is also another line of work [\[37\]](#page-11-28)–[\[40\]](#page-12-0) that investigates the error resilience of GPGPU applications targeting high-performance accelerators. However, non of these tools are capable of modeling SSDrelated failures with software-implemented faults.

SSD Failure Simulation Xu et al. [\[41\]](#page-12-1) model the raw bit error rate at the disk level to quantitatively analyze unique error behaviors in SSDs. It investigates data-level error tolerance for various applications via introducing different error rates at the SSD simulator. It found that for some applications under certain error rates, it is possible to disable ECC for those applications with acceptable level of error tolerance. Although their aim aligns similar to ours, the two studies differ as follows: *(i)* their method is not practical for HPC applications due to the performance of the SSD simulator, which is the core implementation interface of their work; *(ii)* they need further interpretation to understand the application's reliability on HPC systems that experience a collective error rate; *(iii)* they did not consider other SSD-related failure types.

SSD Failures Affecting File Systems PFault [\[17\]](#page-11-29) is a general framework for analyzing the failure handling of parallel file systems. PFault automatically captures I/O commands on all storage nodes of lustre file system, issues realistic failure states and examine if lustre can detect and recover from such failure states. They randomly trash data on the local file system to emulate the failure state, which differs from our dedicated failure modeling techniques. In addition, Jeffer et al. [\[16\]](#page-11-8) conduct an extensive study on characterizing the resilience of various file systems (namely *ext4*, *F2FS* and *btrfs*) running on flash-based SSDs under SSD failures and evaluating the effectiveness of the recovery mechanisms of file systems. They are able to inject many types of faults as the manifestation of partial SSD failures, and present a detailed analysis over the file system error resilience characteristics. However, both [\[16\]](#page-11-8), [\[17\]](#page-11-29) do not focus on the application implication of the SSDrelated errors, which is the main goal of this paper.

Application-level Fault Injection for Storage Ganesan et al. [\[18\]](#page-11-9) build a fault injection framework called CORDS to study how the modern storage systems such as Redis, ZooKeeper, etc. handle the local fail system faults. Although CORDS also leverages the FUSE interface to plant the file system faults, there are fundamental differences between the two studies: *(i)* CORDS models the faults without considering the source of the faults, which resulting significantly distinct fault models and fault injection methodology. For example, CORDS focuses on two types of faults: corruption and file system I/O errors. In the former case, they randomly modify the content of a read buffer, while in FFIS we delicately design the faults and injection methodology; for the latter case, applications receive signals to fail immediately, hence causing no data corruption; *(ii)* we conduct a comprehensive study to understand the impact of errors on the scientific file format, and correlate such impact with the application behaviors.

VII. CONCLUSION

This study focuses on the HPC applications affected by the SSD-related failures, and proposes the fault injection framework FFIS to study the impact of those failures on the applications. We conduct comprehensive fault injection experiments on three representative HPC applications from different domains, and our findings show that different applications exhibit dramatically error resilience behaviors against the same type of SSD-related faults (nearly no SDCs to more than 50% of SDCs), while inside the same application (i.e. Montage), different stages of the application may have a similar error resilience characteristics against the same type of fault. Moreover, we unveil application-specifc behaviors operating on the most widely used scientific file format HDF5 and show the fault tolerance behaviors of the HDF5 library against errors affecting the HDF5 metadata. Finally, we propose a detection approach to identify which metadata field is potentially incorrect and corresponding correction methodology.

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REFERENCES

- [1] J. Meza, Q. Wu, S. Kumar, and O. Mutlu, "A large-scale study of flash memory failures in the field," in *Proceedings of the 2015 ACM SIGMETRICS International Conference on Measurement and Modeling of Computer Systems*, ser. SIGMETRICS '15. New York, NY, USA: Association for Computing Machinery, 2015, p. 177–190. [Online]. Available:<https://doi.org/10.1145/2745844.2745848>
- [2] I. Narayanan, D. Wang, M. Jeon, B. Sharma, L. Caulfield, A. Sivasubramaniam, B. Cutler, J. Liu, B. Khessib, and K. Vaid, "Ssd failures in datacenters: What? when? and why?" in *Proceedings of the 9th ACM International on Systems and Storage Conference*, ser. SYSTOR '16. New York, NY, USA: Association for Computing Machinery, 2016. [Online]. Available: [https://doi.org/10.1145/2928275.](https://doi.org/10.1145/2928275.2928278) [2928278](https://doi.org/10.1145/2928275.2928278)
- [3] B. Schroeder, R. Lagisetty, and A. Merchant, "Flash reliability in production: The expected and the unexpected," in *14th USENIX Conference on File and Storage Technologies (FAST 16)*. Santa Clara, CA: USENIX Association, Feb. 2016, pp. 67–80. [Online]. Available: [https://www.usenix.org/conference/fast16/technical-sessions/](https://www.usenix.org/conference/fast16/technical-sessions/presentation/schroeder) [presentation/schroeder](https://www.usenix.org/conference/fast16/technical-sessions/presentation/schroeder)
- [4] (2016) Solid state drive (ssd) requirements and endurance test method. [Online]. Available:<jedec.org/sites/default/files/docs/JESD218.pdf>
- [5] H. P. Belgal, N. Righos, I. Kalastirsky, J. J. Peterson, R. Shiner, and N. Mielke, "A new reliability model for post-cycling charge retention of flash memories," in *2002 IEEE International Reliability Physics Symposium. Proceedings. 40th Annual (Cat. No.02CH37320)*, 2002, pp. 7–20.
- [6] S. Boboila and P. Desnoyers, "Write endurance in flash drives: Measurements and analysis," ser. FAST'10. USA: USENIX Association, 2010, p. 9.
- [7] Y. Cai, E. F. Haratsch, O. Mutlu, and K. Mai, "Error patterns in mlc nand flash memory: Measurement, characterization, and analysis," in *2012 Design, Automation Test in Europe Conference Exhibition (DATE)*, 2012, pp. 521–526.
- [8] Y. Cai, Y. Luo, E. F. Haratsch, K. Mai, and O. Mutlu, "Data retention in mlc nand flash memory: Characterization, optimization, and recovery," in *2015 IEEE 21st International Symposium on High Performance Computer Architecture (HPCA)*, 2015, pp. 551–563.
- [9] Y. Cai, O. Mutlu, E. F. Haratsch, and K. Mai, "Program interference in mlc nand flash memory: Characterization, modeling, and mitigation," in *2013 IEEE 31st International Conference on Computer Design (ICCD)*, 2013, pp. 123–130.
- [10] N. Mielke, H. P. Belgal, A. Fazio, Q. Meng, and N. Righos, "Recovery effects in the distributed cycling of flash memories," in *2006 IEEE International Reliability Physics Symposium Proceedings*, 2006, pp. 29– 35.
- [11] N. Mielke, T. Marquart, Ning Wu, J. Kessenich, H. Belgal, E. Schares, F. Trivedi, E. Goodness, and L. R. Nevill, "Bit error rate in nand flash memories," in *2008 IEEE International Reliability Physics Symposium*, 2008, pp. 9–19.
- [12] R.-S. Liu, C.-L. Yang, and W. Wu, "Optimizing nand flash-based ssds via retention relaxation," in *Proceedings of the 10th USENIX Conference on File and Storage Technologies*, ser. FAST'12. USA: USENIX Association, 2012, p. 11.
- [13] R. A. Ashraf, R. Gioiosa, G. Kestor, R. F. Demara, C. Y. Cher, and P. Bose, "Understanding the propagation of transient errors in HPC applications," *International Conference for High Performance Computing, Networking, Storage and Analysis, SC*, vol. 15-20-Nove, 2015.
- [14] J. Calhoun, L. Olson, and M. Snir, "Flipit: An llvm based fault injector for hpc," in *Revised Selected Papers, Part I, of the Euro-Par 2014 International Workshops on Parallel Processing - Volume 8805*. Berlin, Heidelberg: Springer-Verlag, 2014, p. 547–558. [Online]. Available: [https://doi.org/10.1007/978-3-319-14325-5](https://doi.org/10.1007/978-3-319-14325-5_47) 47
- [15] M. Szeredi. (2005) Filesystem in userspace. [Online]. Available: <https://github.com/libfuse/libfuse>
- [16] S. Jaffer, S. Maneas, A. Hwang, and B. Schroeder, "Evaluating file system reliability on solid state drives," in *2019 USENIX Annual Technical Conference (USENIX ATC 19)*. Renton, WA: USENIX Association, Jul. 2019, pp. 783–798. [Online]. Available: <https://www.usenix.org/conference/atc19/presentation/jaffer>
- [17] J. Cao, O. R. Gatla, M. Zheng, D. Dai, V. Eswarappa, Y. Mu, and Y. Chen, "Pfault: A general framework for analyzing the reliability of high-performance parallel file systems," in *Proceedings of the 2018*

International Conference on Supercomputing, ser. ICS '18. New York, NY, USA: Association for Computing Machinery, 2018, p. 1–11. [Online]. Available:<https://doi.org/10.1145/3205289.3205302>

- [18] A. Ganesan, R. Alagappan, A. C. Arpaci-Dusseau, and R. H. Arpaci-Dusseau, "Redundancy does not imply fault tolerance: Analysis of distributed storage reactions to single errors and corruptions," in *15th USENIX Conference on File and Storage Technologies (FAST 17)*. Santa Clara, CA: USENIX Association, Feb. 2017, pp. 149–166. [Online]. Available: [https://www.usenix.org/conference/](https://www.usenix.org/conference/fast17/technical-sessions/presentation/ganesan) [fast17/technical-sessions/presentation/ganesan](https://www.usenix.org/conference/fast17/technical-sessions/presentation/ganesan)
- [19] T. J. Kowalski, *Fsck—the UNIX File System Check Program*. USA: W. B. Saunders Company, 1990, p. 581–592.
- [20] A. Avizienis, J. . Laprie, B. Randell, and C. Landwehr, "Basic concepts and taxonomy of dependable and secure computing," *IEEE Transactions on Dependable and Secure Computing*, vol. 1, no. 1, pp. 11–33, 2004.
- [21] M. Zheng, J. Tucek, F. Qin, M. Lillibridge, B. W. Zhao, and E. S. Yang, "Reliability analysis of ssds under power fault," *ACM Trans. Comput. Syst.*, vol. 34, no. 4, Nov. 2016. [Online]. Available: <https://doi.org/10.1145/2992782>
- [22] A. N. S. Institute. (2008) At attachment 8 - ata/atapi command set (ata8-acs). [Online]. Available: [http://smartlinux.sourceforge.net/smart/](http://smartlinux.sourceforge.net/smart/attributes.php) [attributes.php](http://smartlinux.sourceforge.net/smart/attributes.php)
- [23] The HDF Group. (2000-2021) Hierarchical data format version 5. [Online]. Available:<http://www.hdfgroup.org/HDF5>
- [24] S. Byna, M. S. Breitenfeld, B. Dong, Q. Koziol, E. Pourmal, D. Robinson, J. Soumagne, H. Tang, V. Vishwanath, and R. Warren, ""exahdf5: Delivering efficient parallel i/o on exascale computing systems"," in *Journal of Computer Science and Technology*, 2020, pp. 145 – 160.
- [25] (2021) Hierarchical data format. [Online]. Available: [https://en.](https://en.wikipedia.org/wiki/Hierarchical_Data_Format) [wikipedia.org/wiki/Hierarchical](https://en.wikipedia.org/wiki/Hierarchical_Data_Format) Data Format
- [26] The HDF Group. HDF5 File Format Specification Version 3.0. [Online]. Available:<https://support.hdfgroup.org/HDF5/doc/H5.format.html>
- [27] Frontera system, [https://www.tacc.utexas.edu/systems/frontera,](https://www.tacc.utexas.edu/systems/frontera) 2020.
- [28] A. S. Almgren, J. B. Bell, M. J. Lijewski, Z. Lukić, and E. Van Andel, "Nyx: A massively parallel amr code for computational cosmology," *The Astrophysical Journal*, vol. 765, no. 1, p. 39, 2013.
- [29] J. Kim, A. D. Baczewski, T. D. Beaudet, A. Benali, M. C. Bennett, M. A. Berrill, N. S. Blunt, E. J. L. Borda, M. Casula, D. M. Ceperley *et al.*, "Qmcpack: an open source ab initio quantum monte carlo package for the electronic structure of atoms, molecules and solids," *Journal of Physics: Condensed Matter*, vol. 30, no. 19, p. 195901, 2018.
- [30] J. C. Jacob, D. S. Katz, G. B. Berriman, J. C. Good, A. Laity, E. Deelman, C. Kesselman, G. Singh, M.-H. Su, T. Prince *et al.*, "Montage: a grid portal and software toolkit for science-grade astronomical image mosaicking," *International Journal of Computational Science and Engineering*, vol. 4, no. 2, pp. 73–87, 2009.
- [31] M. Davis, G. Efstathiou, C. S. Frenk, and S. D. White, "The evolution of large-scale structure in a universe dominated by cold dark matter," *The Astrophysical Journal*, vol. 292, pp. 371–394, 1985.
- [32] Input files for a single helium atom., [https://github.com/QMCPACK/](https://github.com/QMCPACK/qmcpack/blob/develop/examples/molecules/He/README) [qmcpack/blob/develop/examples/molecules/He/README,](https://github.com/QMCPACK/qmcpack/blob/develop/examples/molecules/He/README) 2020.
- [33] T. H. Group. (2016) Hdf5 file format specification version 3.0. [Online]. Available:<https://support.hdfgroup.org/HDF5/doc/H5.format.html>
- [34] S. Jin, P. Grosset, C. M. Biwer, J. Pulido, J. Tian, D. Tao, and J. Ahrens, "Understanding gpu-based lossy compression for extremescale cosmological simulations," in *2020 IEEE International Parallel and Distributed Processing Symposium (IPDPS)*. IEEE, 2020, pp. 105– 115.
- [35] S. Jin, J. Pulido, P. Grosset, J. Tian, D. Tao, and J. Ahrens, "Adaptive configuration of in situ lossy compression for cosmology simulations via fine-grained rate-quality modeling," in *The 30th ACM International Symposium on High-Performance Parallel and Distributed Computing (HPDC 2021)*, 2021.
- [36] J. Calhoun, M. Snir, L. Olson, and M. Garzaran, "Understanding the propagation of error due to a silent data corruption in a sparse matrix vector multiply," in *2015 IEEE International Conference on Cluster Computing*, 2015, pp. 541–542.
- [37] B. Fang, K. Pattabiraman, M. Ripeanu, and S. Gurumurthi, "Gpu-qin: A methodology for evaluating the error resilience of gpgpu applications," in *2014 IEEE International Symposium on Performance Analysis of Systems and Software (ISPASS)*, 2014, pp. 221–230.
- [38] S. K. S. Hari, T. Tsai, M. Stephenson, S. W. Keckler, and J. Emer, "Sassifi: An architecture-level fault injection tool for gpu application resilience evaluation," in *2017 IEEE International Symposium on Per-*

formance Analysis of Systems and Software (ISPASS), 2017, pp. 249– 258.

- [39] NVIDIA. (2020) Nvbitfi: Architecture-level fault injection tool for gpu application resilience evaluation. [Online]. Available: [https:](https://github.com/NVlabs/nvbitfi) [//github.com/NVlabs/nvbitfi](https://github.com/NVlabs/nvbitfi)
- [40] G. Li, K. Pattabiraman, C. Cher, and P. Bose, "Understanding error propagation in gpgpu applications," in *SC '16: Proceedings of the International Conference for High Performance Computing, Networking, Storage and Analysis*, 2016, pp. 240–251.
- [41] X. Xu and H. H. Huang, "Exploring data-level error tolerance in high-performance solid-state drives," *IEEE Transactions on Reliability*, vol. 64, no. 1, pp. 15–30, 2015.