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***Ab initio* investigation of charge trapping across crystalline-Si/amorphous-SiO₂ interface**

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Abstract

Accurate microscopic description of the charge trapping process from semiconductor to defects in dielectric oxide layer is of paramount importance to understanding many microelectronic devices like the complementary metal-oxide-semiconductor (CMOS) transistors, as well as electrochemical reactions. Unfortunately, most current microscopic descriptions of such processes are based on empirical models with parameters fitted to experimental device performance results or simplified approximations like the Wentzel-Kramers-Brillouin (WKB) method. Some critical questions are still unanswered, including: What controls the charge hopping rate, the coupling strength between the defect level to semiconductor level, or the energy difference? How does the hopping rate decay with defect-semiconductor distance? What is the fluctuation of the defect level, especially in amorphous dielectrics? Many of these questions can be answered by *ab initio* calculations. However, till date, there are scarce *ab initio* studies for this problem mainly due to technical challenges from atomic structure construction to large system calculations. Here, using the latest advance in calculation methods and codes, we have studied the carrier trapping problem using density functional theory (DFT) based on the Heyd-Scuseria-Ernzerhof (HSE) exchange correlation functional. The valence bond random switching method is used to construct the crystalline-Si/amorphous-SiO₂ (c-Si/a-SiO₂) interfacial atomic structure, the HSE yields band offset agrees well with experiments. The hopping rate is calculated with the Marcus theory, and the hopping rate dependences on the gate potential and defect distances are revealed, as well as the range of fluctuation results from amorphous structural variation. We have also analyzed the result with the simple WKB model and found major difference in the description of the coupling constant decay with the defect-semiconductor distance. Our results provide the *ab initio* simulation insights for this important carrier trapping process for device operation.

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I. Introduction

Charge trapping at the gate dielectric layer along with the dangling bonds generation at the oxide/channel interface are known to be the main origins of electronic device reliability issues such as bias temperature instability (BTI) [1-10]. Such charge trapping and transport process is also important to oxide protection layer in electrochemical cells. Till date, the charge transport processes and the possible related ionic movements are often described by continuous diffusion equation (reaction-diffusion model) with physical pictures based on simple effective mass like phenomenological model and parameters fitted to the experimental device performance. However, it is recently reported that the reaction-diffusion model [11-13], can fail to explain the BTI degradation recovery [14-17], suggesting that charge trapping plays an important role in bias temperature instability, and more realistic microscopic model might be needed [2,18-23]. Since charge trapping processes are usually facilitated by the defect states in the gate oxide, great efforts have been made to understand the properties of these defects as well as the intrinsic electron trapping in SiO_2 and HfO_2 by using atomistic *ab initio* calculations [24-30]. In Ref. [24], Anderson et.al studied the precursors of low energy E' centers in an ensemble of generated amorphous silica. In Ref. [25], Kuo et al. investigated the structure and stability of surface oxygen vacancies in SiO_2 , and found that structural interconversion is very likely to happen with thermal activation. El-Sayed et al. showed that the hydrogen-induced defects playing a role in amorphous SiO_2 in Ref. [26] and [27], alongside with the intrinsic electron traps in amorphous SiO_2 in Ref. [28]. F. Cerbu et al. studied the intrinsic electron traps in amorphous HfO_2 in Ref. [29]. In Ref. [30], Mehes and Patterson constructed c-Si(001)/a-SiO₂ structures by using classical molecular dynamics simulation, and studied the interfacial defects. In one of our previous works [31], we have constructed the c-Si(001)/a-SiO₂ interface using valence bond random switching model, and calculated the band alignment of such interface using the HSE exchange-correlation functional. The HSE allows us to yield accurate band gaps for both c-Si and a-SiO₂, and the calculated band offset for the interface agrees well with the experiments.

So far, the *ab initio* calculations have been used to study the single defect level in a-SiO₂ and their variations, and have been used to construct the c-Si(001)/a-SiO₂ interface. But there is no complete DFT calculation yet for the charge trapping between the c-Si and the defects state in a-SiO₂. Several challenges might have prevented such calculations so far. First, in order to calculate the charge transfer, a large (e.g., 500 atoms) supercell need to be used. That might be beyond the size regime one can calculate using *ab initio* method. Second, in order to get the correct band gap and band alignment, computationally expensive methods, like the HSE functional need to be used. That makes the large supercell calculation even more difficult. Third, the charge trapping rate is not so

straight forward to be calculated, especially to evaluate the electronic state coupling in such a disordered system. Lastly, given the disorder nature of the amorphous structure, a systematic study (e.g., the trapping rate distance dependence) might be difficult. However, the charge trapping process calculation is critical. It will help us to provide the parameters in the phenomenological models, and to understand the important trapping step in microscopic details. Since it is unlikely such microscopic processes can be precisely probed experimentally in the near future, realistic ab initio calculations of such processes become even more critical.

The charge trapping process in a metal-oxide-semiconductor field-effect transistor (MOSFET) manifests itself as a transfer of electrons/holes from the silicon channel to the oxide dielectric, which is a state-to-state transition from silicon conduction/valence band state to the oxide trap state. As far as we know, the research on charge trapping process in MOSFETs have been largely contributed by Grassler et al. in Si/SiO₂ systems, by partially using DFT calculation, WKB approximation, and nonradiative multi-phonon (NMP) theory [32]. The DFT calculation is used to obtain the eigen-energies of bulk Si and bulk SiO₂ separately instead of Si/SiO₂ interface structures given the computational capability and cost, and the WKB approximation is used to estimate the coupling constant between the initial and final state [20,32,33]:

$$\theta_{ij} = \tilde{k} \exp\left(-\frac{\sqrt{2m_t\Delta V}}{\hbar}d\right) \quad (1)$$

where m_t is the tunneling mass, d is the distance from the a-Si/a-SiO₂ interface to the defect location, and ΔV is the tunneling barrier, which is usually taken as the conduction band offset between the c-Si and a-SiO₂. \tilde{k} is a pre-factor fit to experiment. Then the charge trapping rate is calculated by the equation based on NMP theory:

$$\tau_{i \rightarrow j}^{-1} = \frac{2\pi}{\hbar} |\theta_{ij}|^2 \sqrt{\frac{1}{4\pi k_B T}} \frac{1}{\sqrt{S\hbar\omega}} \exp\left[-\frac{(E_j - E_i + S\hbar\omega)^2}{4S\hbar\omega k_B T}\right] \quad (2)$$

where E_i and E_j is the total energy of the system before and after charge trapping, and $S\hbar\omega$ is the reorganization energy.

While these works are very pioneering and illuminating, it can be seen that the simulation framework is compromised by overlooking the effect of Si/SiO₂ interface, and estimating the coupling constant by WKB approximation instead of by accurate first principle calculation. Furthermore, some critical parameters like the \tilde{k} is not calculated. It will be better to consider the Si/SiO₂ interface explicitly because it can have a gradual change of band offset within a certain thickness near the interface, and there could be an issue of coupling between the c-Si electron state and a-SiO₂ electron state. We also expect to improve the WKB description because there are several uncertainties about this formalism. First, what one should use for m_t and ΔV ? If one considers this tunneling is from the c-Si CBM state, then perhaps the effective mass of c-Si should be used for m_t and CBM band offset should be used for ΔV . On the other hand, if one considers this as the oxygen vacancy (V_O) defect state tunneling, perhaps effective mass within a-SiO₂ need to be used, and a different ΔV need to be used. It is also not

clear whether a simple effective tunneling model can be used to describe the a-SiO₂ wave function behavior. Lastly, the \tilde{k} has to be fitted to experiment, which significantly decrease the predictive power of the theory.

All the above compromises can be overcome by direct ab initio calculations if the following tasks can be accomplished. First, a good atomic structure model of the c-Si/a-SiO₂ interface needs to be built. Second, the computational code should be fast enough to enable the calculation of large systems especially using the HSE functional. The use of HSE functional is critical to provide the correct band gap and band alignment between the c-Si and a-SiO₂ band edges, as well as the correct silicon conduction band minimum energy E_{CBM} and the oxide defect level E_{defect} levels. Third, a reliable procedure needs to be developed to calculate the electronic coupling constant and the related reorganization energy.

The first task can be accomplished by at two different approaches: the molecular dynamics (MD) simulated annealing or covalent bond switching Monte Carlo simulation [31,34]. However, the time step for MD simulation could be rather long, and the ab initio simulation of that process can be costly if no good classical force field can be used. We will thus use the covalent bond random switching method. The second task is difficult because the c-Si/a-SiO₂ atomic structure must consist of a large number (~500) of atoms to realistically represent the amorphous nature of the SiO₂. Fortunately, recent developments in computational algorithm and code have made the direct calculations of systems with about 500 atoms feasible with HSE functional. More specifically, using the plane-wave pseudopotential PWmat, which is implemented in GPU [35,36], a 550 atom system can be calculated on a 8 GPU Mcluster within 6 hours for a converged SCF calculation using the HSE hybrid functional. The third task can be achieved by applying an electric field in the a-SiO₂ region, directly mimic the situation in the operation of the microelectronic device. When the electric field is large enough, the a-SiO₂ V_o defect level will cross the c-Si CBM level. Their energy anti-crossing is directly related to their coupling constant. We have also developed a technique, which inserting more a-SiO₂ layer between the defect and the c-Si, without changing the local atomic environment of the V_o. This can significantly reduce the uncertainty due to V_o local environment fluctuation, while let us focus on the distance dependence of the coupling constant.

After removing the above hurdles, all essential quantities needed to calculate the charge trapping can be obtained through ab initio DFT method, and the charge trapping rate can be calculated by the well-known Marcus charge transfer theory [37,38], which describes the electron transfer rate from an initial state to a final state as

$$v_{\text{trapping}} = \frac{2\pi}{\hbar} |V_c|^2 \sqrt{\frac{1}{4\pi\lambda k_B T}} \exp\left[-\frac{(\Delta G + \lambda)^2}{4\lambda k_B T}\right] \quad (3)$$

where V_c is the electronic coupling between the initial and final states, and ΔG is the total Gibbs free energy difference between the initial (electron occupation configuration) atomic relaxed ground state and the final (electron occupation configuration) atomic relaxed ground state. λ is the reorganization energy defined as the atomic relaxation energy after the electron reconfiguration from the initial state to the final state. The above

formula is basically the same with Eq. (2), but with more succinct form. The reorganization energy is represented as λ instead of $S\hbar\omega$ because it can be directly obtained by structural relaxation after hole trapping, and it is not necessary to calculate the phonon spectrum of the system. The V_C will also be obtained directly by DFT instead of by WKB approximation. Eq. (3) has been successfully applied to semiconductor-molecule systems to study the electron and hole transfer dynamics [39,40]. Marcus theory is a classical limit formula for relatively high temperature for systems with large degrees of freedom. It describes the multi-phonon energy transition with a classical thermodynamic limit. Quantum mechanical process can also be used to describe this phonon energy conservation process, and it has been shown that the difference is rather small for large systems [41]. Finally, a more rigorous treatment based on Franck-Condon approximation, and static state coupling can also be used to describe such charge transport, in which all the electron-phonon coupling constants, and all the phonon modes of the system need to be calculated [42-44]. In this study however, we will restrict ourselves to the Marcus theory as it describes well the large system charge transfer, and it has a relatively simple computation procedure.

By constructing a set of c-Si/a-SiO₂ interfaces with ~500 atoms, and then using the plane wave pseudopotential DFT code PWmat to conduct GPU accelerated HSE functional based DFT calculations, and finally studying the electron trapping rates with Marcus theory, we have the following discoveries: (1) The coupling constant between defect levels and Si CBM decays fast with the increase of defect-semiconductor distance, which is in accordance with the results of WKB approximation. However, the coupling of these two energy levels might be more complicated than the scenario described by a simple 1D WKB model. The coupling might be better described as the decays of the two wave functions towards each other, and meet in the middle. If the WKB formalism is to be used, more proper treatment is needed. First, the tunneling of both states must be taken into consideration, and the Si CBM wave function decays in a-SiO₂ might be more one dimensional, while the V_O wave function decays should be three dimensional. This can end up in a more complicated distance depend formula. (2) The structural variety of amorphous SiO₂ results in a variation of charge trapping energy barrier and coupling constants, and further results in the charge trapping rate variation for the same defect. (3) The role of energy difference between defect levels and CBM_{Si} is more important than their coupling strength in deciding the trapping rate when there is no bias. On the other hand, if a large electric field is applied, one asks what is the maximum possible charge transition rate during the scan of the electric field, then the coupling constant will be the determining factor. (4) The oxygen vacancies alone inside the SiO₂ layer are rarely able to trap electrons under small electric field, which is in agreement with previous works. (5) The DFT calculation is able to calibrate critical parameters for phenomenological models such as WKB approximation, so that to improve their accuracy and predictability.

This paper presents the ab initio calculations of the above-mentioned electron trapping process at crystalline silicon and amorphous silicon dioxide interface. It provides physical insights for the charge trapping related

reliability issues in CMOS devices as well as the critical parameters for the phenomenological models used in device simulations. The remaining of the paper is organized as follows. The construction of the c-Si/-SiO₂ structures and the DFT calculation details are described in Sec. II. The calculated band alignment, reorganization energy, and coupling constant are shown in Sec. III. A comparison between the coupling constant calculated by WKB approximation and DFT is discussed in Sec. IV. The calculated electron trapping rates are presented in Sec. V. Sec. VI concludes the paper.

II. Atomic structure construction and electronic structure calculation

A. Atomic structure construction

As mentioned above, amorphous SiO₂ structures can be obtained by either molecular dynamics (MD) simulations or bond switching Monte Carlo (BS-MC) simulations. It has been showed that the BS MC simulation reproduces the experimentally measured radial distribution function better than the MD simulation based on ReaxFF model, and it also produces more accurate O-Si-O angle distribution function [31]. The BS MC simulation starts with a c-Si/c-SiO₂ hetero-structure with several layers of c-Si and c-SiO₂ with a perfect bonding interface (although with large strain in the SiO₂ region). Then a random pair of nearby Si-O bonds is selected (e.g. A-B and C-D) and switched into a new pair of bonds: A-C and B-D. Using a valence force field model, which specifies which atom is bonded with which other atom, the new structure is relaxed, and its relaxed total energy compared with the energy before bond switching. The new structure is then accepted or rejected following the Metropolis MC scheme. However, to make the structure amorphous, the first $N/2$ steps are all accepted (N is the number of atoms in the simulation cell). After that, a stimulated annealing process is carried out using the bond switching process to cool the structure and to reduce the local strain. During this process, the c-Si side has been kept unchanged by the bond switching process.

The above process has been used in our previous work to generate the c-Si/a-SiO₂ interface which yield the correct band offset across the interface [31]. However, the original c-Si/a-SiO₂ structures generated in Ref [31] are periodic along the x-direction perpendicular to the interface. Such periodic structure is not suitable for investigating the electronic coupling between the c-Si CBM state and a-SiO₂ defect state since they can coupling through both interfaces (at the left and right of the defect location). The periodic boundary condition is not consistent with a real MOSFET setup either. For these reasons, we have built a new structure with only one c-Si/a-SiO₂ interface, and open ends in the other c-Si interface and a-SiO₂ interface, as shown in Fig.1. Such open ended structure can be obtained from the original periodic structure with a cut at one side, and using pseudo hydrogen atoms to passivate the cut-open surfaces. To generate the V_O defect, we simply remove one O from the a-SiO₂ side, and relax the system under DFT till the atomic forces becomes less than 0.02 eV/Å. PBE functional is used for all structural relaxation while HSE functional is used for energy level calculation. The Si-Si bond at the

oxygen vacancy is found to be 2.65 Å, which is much larger than that in bulk Si. Considering that such non-periodic system may arise people's concern on the finite-size effects, we have conducted a comparative study on the model shown in Fig. 1(a) with periodic and non-periodic boundary. Results show that the defect energy of these two systems relative to the c-Si CBM differ by only 0.089 eV, which is much less than the fluctuation due to the different structure in the amorphous system, indicating that the finite size effect is very small. Besides, all the models have a lattice of 16.3 Å * 16.3 Å along the cross section directions, and we have not seen strong coupling between the image defect states in the cross section of our model.

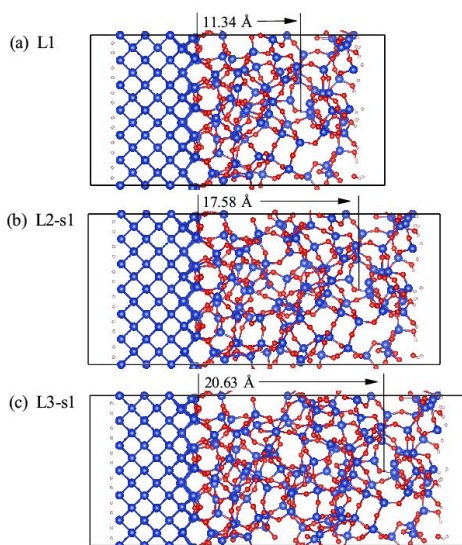


Figure 1. The c-Si/a-SiO₂ atomic structures studied in this work. The interface-defect distances are marked in each structure. Only two of the six elongated structures are presented here.

One of our goals is to investigate the effect of interface-defect distance (d_{I-D}) on the electron trapping. One can remove the O from different location, thus change the d_{I-D} . However, the V_Os constructed that way will be different from one another. This will increase the uncertainty due to the variation of that defect. We like to keep the defect (oxygen vacancy) and the nearby atomic structure the same, while changing the d_{I-D} . To realize that, we have inserted different layers of a-SiO₂ in a region between V_O and the interface. More specifically, we cut-off one cross section between V_O and interface, remove these two parts apart, and insert a slice of SiO₂ inside this region. This is followed by performing BS-MC for the bonds inside this inserted slice, while keeping the other atoms fixed. After all these are done, a whole system DFT relaxation is performed. Doing this way, we can keep the V_O local environment the same for different d_{I-D} distances as shown in Fig.1.

Besides the d_{I-D} , we have studied the effect of structure variation in between the defects and the semiconductor, i.e. by preparing three different inserting layers that varies in atomic structure but with the same thickness. Consequently, seven models are investigated in this study, including one model with $d_{I-D} = 11.34$ Å, three models with $d_{I-D} = 17.58$ Å, and another three models with $d_{I-D} = 20.63$ Å. These models are denoted as L1,

L2-s1, L2-s2, L2-s3, L3-s1, L3-s2, and L3-s3, respectively, where “L” denotes “length”, and “s” denotes “structure”.

B. electronic structure calculation

It is well known that the DFT calculation with LDA or GGA functional significantly underestimates the band gap of the semiconductors and does not yield correct energy levels. Such problem is critical for studying the charge trapping process because the trapping rate is closely related to the energy difference between silicon band edge and the defect level. The energy level also affects the tunneling decay of the wave function, thus a simple scissor operator will not help. Consequently, extra care must be taken to ensure the correctness of the electronic structure and band alignment. This is challenging since the smallest c-Si/a-SiO₂ structure studied in this work contains 448 atoms, and the largest one contains as many as 604 atoms. Here, we have used the PWmat package with HSE hybrid functional calculation. The PWmat package is implemented with the GPU, it can speed up the calculation for 20 times compared with the CPU. Taking the H passivated c-Si/a-SiO₂ structure with 550 atoms and 2550 electrons for example, the HSE functional based SCF calculation takes only 6 hours on 8 GPUs with SG15 norm-conserving pseudopotentials and an E_{cut} of 50 Ryd. We have confirmed the validity of 50 Ryd by repeating the SCF calculation on structure L1 with E_{cut}=60 Ryd, and found no notable differences. Only the Gamma point is sampled because the lattices and number of atoms in each model are so large. The validity of single k-point has been proved and shown in Sec. S1 of the supplemental material [45].

Another problem should be noticed is that the c-Si/a-SiO₂ structures contain two regions of materials, one for c-Si another for a-SiO₂. In order to yield accurate band gap, it is necessary to fine tune the HSE Fock exchange mixing parameter. Unfortunately, there is no single parameter which can yield accurate band gaps for both c-Si and a-SiO₂. Here, the PWmat package has implemented atom-weighted mixing parameters. More specifically, one can define a regional mask function as $f(r) = 1 + \sum_i a_i e^{-(r-R_i)^2/\sigma_i^2}$, where a_i and σ_i are atomic type dependent input parameters. With large σ_i , this function is smooth in space, but can have different values in different region.

The Fock exchange integral in the HSE total energy expression is then written

as $\sum_{i,j} 0.25 o(i) o(j) \iint \psi_i(\mathbf{r}) \psi_j^*(\mathbf{r}) f(r) \frac{\text{erfc}(\omega(\mathbf{r}-\mathbf{r}'))}{|\mathbf{r}-\mathbf{r}'|} f(r') \psi_i^*(\mathbf{r}') \psi_j(\mathbf{r}') d^3\mathbf{r} d^3\mathbf{r}'$, where 0.25 is the default mixing

parameter for HSE, and o(i), o(j) are the orbital occupation number. Using a_i and σ_i as -0.1 and 4.2 for Si, and 0.24 and 2.75 for O, we can yield c-Si band gap of 1.12 eV and a-SiO₂ of 8.5 eV, as measured from experiment. The calculated band offset between the CBM of c-Si and CBM of a-SiO₂ is 2.9 eV, in good agreement of the experimental results of 3.0 eV.

III. DFT calculation results

The silicon band edge and the oxygen vacancy defect levels in each structure are ascertained by checking the partial density of states (PDOS) of the crystalline Si part and the oxide defect part, respectively. Taking the shortest structure L1 for instance, the PDOS is shown in Fig. 2(a). It can be seen that two defect levels are induced by the oxygen vacancy, including one level lies above the Si conduction band minimum (CBM_{Si}) and the other one lies below the Si valence band maximum. Such phenomenon is in good agreement with previous works on a-SiO₂ [46]. Here we mean to study the electron transfer process, which is most likely to happen from the silicon conduction band to the unoccupied defect level, thus we will focus on the CBM_{Si} and the E_{defect} as marked in Fig. 2(a).

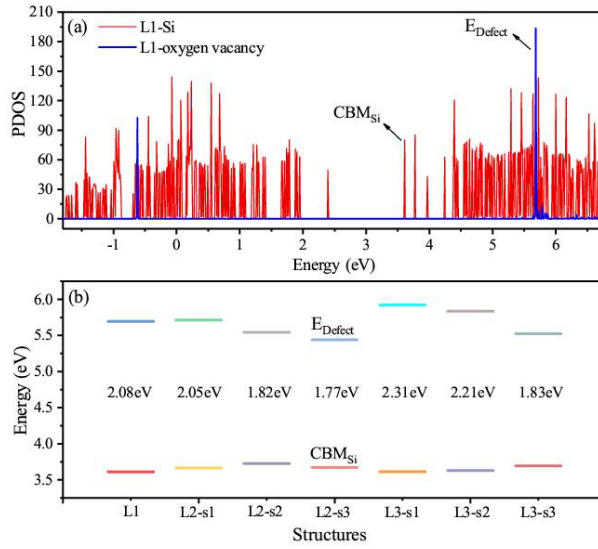


Figure 2. The band alignment calculated by HSE functional based DFT. (a) The partial density of states (PDOS) of the crystalline Si part and the oxygen defect part. The PDOS of defect is obtained by projecting the wave functions to the Si atoms close to the vacancy defect. The PDOS on c-Si is taken from the center of the c-Si slices. (b) The E_{CBM} and E_{defect} in different structures.

With the E_{CBM} and the E_{defect} known, it is attempting to use that to calculate the ΔG in the exponent of Eq. (3). However, it is worth to point out that ΔG does not equal to $E_{defect} - E_{CBM}$. This is because E_{CBM} and E_{defect} are all obtained from the neutral structure, which will change when E_{CBM} or E_{defect} is occupied by an additional electron. Such structural relaxation caused by electron occupation will further result in an energy change of the system, which is called reorganization energy. The reorganization energy is calculated as following: First we relax the atomic structure with defect at its neutral state (N electrons), and obtain an atomic structure R_0 . We then place an addition electron to the R_0 structure (N+1 electrons), and carry out an electronic structure self-consistent calculation to obtain the total energy $E(R_0, N+1)$. After that, we relax the structure with N+1 electrons to obtain its minimum energy $E(R_1, N+1)$. The energy differences between these two atomic configuration (both with N+1

electron) is the reorganization energy:

$$\lambda = E(R_0, N + 1) - E(R_1, N + 1) \quad (4)$$

Note, since both energies have N+1 electron, they both have electrostatic image energies, and thus should cancel each other out. The uncertainty caused by this image electrostatic interaction should be much smaller than the typical defect calculation where E(N+1) and E(N) are subtracted. All these calculations for reorganization energy are done using PBE.

Consider the original case when there is no electrons occupy the Si conduction band and the upper defect level, the energy of the system can be denoted as E_0 . Then we can write the Gibbs free energy before and after electron transfer as:

$$G_i = E_0 + E_{CBM} - \lambda_{CBM-Si} \quad (5)$$

$$G_f = E_0 + E_{Defect} - \lambda_{defect} \quad (6)$$

Consequently,

$$\Delta G = E_{Defect} - E_{CBM} - \lambda_{defect} + \lambda_{CBM-Si}. \quad (7)$$

It can be seen that the E_{CBM} and E_{defect} are essential quantities that must be calculated first. Also, by analyzing the PDOS, the E_{CBM} and E_{defect} in other structures are obtained and shown in Fig. 2(b). In Eq. (3), the total reorganization energy λ equals to the sum of λ_{defect} and λ_{CBM-Si} .

Due to the delocalization nature of the E_{CBM} state, the reorganization energy of Si CBM state should be very small, thus can be ignored. In contrast, the defect state is very localized, and the defect structure will respond strongly to the occupying electron. As seen in Fig. 3, in a V_O defect, the two Si atoms that are originally connected by the removed O atoms separate more from each other after the occupation of an electron, and the nearby atoms also move away slightly. The corresponding reorganization energy due to the occupation of the electron is calculated to be 0.68 eV.

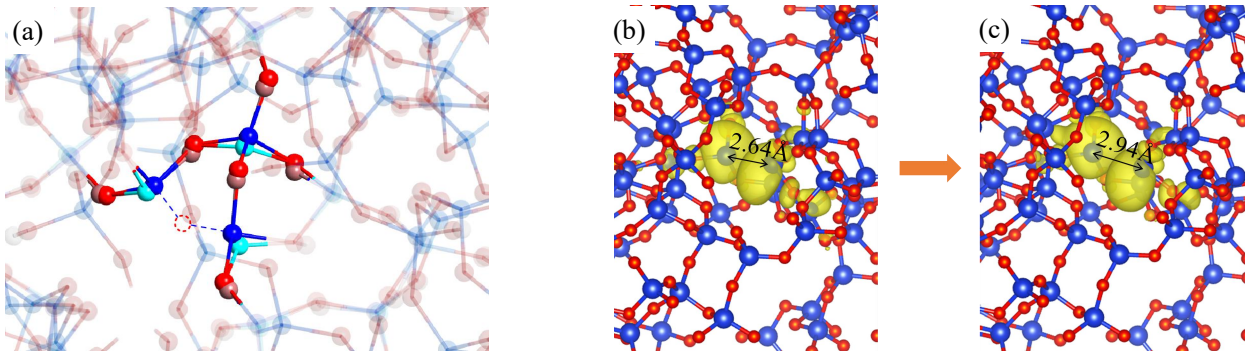


Figure 3. The structure reorganization of the oxygen vacancy defect after trapping an electron. (a) The original Si and O atoms are denoted by dark blue and red, respectively. And the Si and O atoms after reorganization are denoted by light blue and pink, respectively. The oxygen vacancy is denoted by a red dashed circle. (b) and (c)

The wave function of the defect level before and after reorganization.

The most important and difficult task is to calculate the coupling constant between the initial state (E_{CBM}) and the final state (E_{defect}). Our strategy is to apply an electric field to drive the two energy levels approaching each other till the “anti-crossing” happens. Anti-crossing, which is sometimes called “avoided crossing”, is the phenomenon where two eigenvalues of a Hermitian matrix cannot become equal in value due to their electronic coupling. The energy difference minimum of the two eigenvalues is called the “anti-crossing gap”, which equals to two times of their coupling constant (V_C) [47,48]. Another approach to get coupling constant is using a linear combination of two relevant adiabatic states, and then figure out the coupling constant by using the 2x2 Hamiltonian. The so obtained coupling constant V_C is usually the same as obtained from the avoided crossing calculations [39,40]. See Sec. S2 in the supplemental material [45] for more discussion on this topic. The electric field is applied by changing the potential of each place along the Z direction. Denoting the potential of the system obtained by HSE self-consistent calculation as $V_0(z)$, and the electric field strength as $-F$. Then by setting the center of the electric field at the Si/SiO₂ interface (z_0), we can create the new potential of the system as

$$V_{\text{new}}(z) = V_0(z) - F \cdot (z - z_0) \quad (8)$$

Consequently, the energy levels in the Si part will be risen up, and the defect level in the SiO₂ part will be pulled down. Taking structure L1 for example, the transition of E_{CBM} and E_{defect} , and the corresponding wave functions are shown in Fig. 4. It can be seen that the wave function of E_{defect} is mainly localized at the oxygen vacancy site before the coupling, and the wave function of E_{CBM} is delocalized at the Si atoms. When the two energy levels approach each other, their wave functions begin to mix together with significant charge densities at both the defect and the crystalline Si sides for both wave functions. With further increase of the electric field, the E_{defect} state continue to move downwards while the E_{CBM} state move upwards, and they become decoupled.

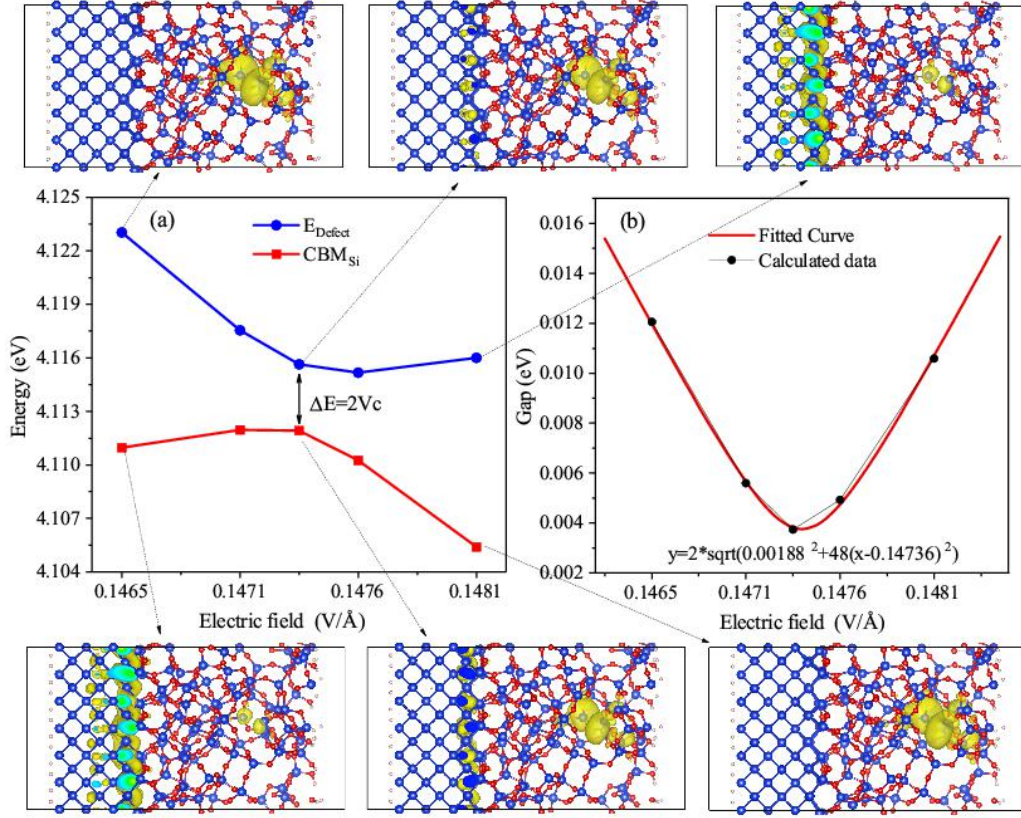


Figure 4. The coupling process of E_{CBM} and E_{defect} driven by electric field. (a) The changing of E_{CBM} , E_{defect} and their wavefunctions with the applied electric field. (b) The fitting of the anti-crossing energy gap.

Although the coupling constant V_c is known to be half of the anti-crossing energy gap [47,48] (the minimum of $E_{\text{CBM}} - E_{\text{defect}}$), challenges remain in order to yield a continuous curve of $E_{\text{CBM}} - E_{\text{defect}}$. To reduce the computational cost, we have fitted the energy curves with the eigen energies of the following 2x2 model:

$$\det \begin{pmatrix} \eta(F - F_0) - E & V_c \\ V_c & -\eta(F - F_0) - E \end{pmatrix} = 0 \quad (9)$$

and thus

$$\Delta E = 2\sqrt{V_c^2 + \eta^2 (F - F_0)^2} \quad (10)$$

in which F is the applied electric field, $\Delta E = |E_{\text{defect}} - E_{\text{CBM}}|$, V_c , η , and F_0 are three unknown parameters, while η roughly represent the distance between the defect state and the Si CBM state, F_0 is the field amplitude of the crossing point. With this equation and five groups of calculated $(F, \Delta E)$ data, a fitted curve and V_c can be obtained. As is seen in Fig. 4(b), the fitted curve matches well with the calculated points, and we get $V_c = 0.00188$ eV in this case. Note that this V_c is much smaller than the typical accuracy of the DFT calculations, one might thus wonder whether the DFT can be used to obtain this value. The inaccuracy of the DFT comes from its exchange correlation functional, for example, it is represented in its absolute eigen energy values. This V_c is a result of the electron wave functions coupling, so as long as the wave function is described correctly, the calculated coupling constant

should be reliable. We must emphasize that the 2-level Hamiltonian in Eq. (9) is just a simple model to explain the connection between the anti-crossing gap and the coupling constant. It is actually not used at all in actual simulation.

The coupling constant V_C between E_{defect} and E_{CBM} in other six structures are obtained by the same way and are shown in Fig. 5. It can be seen that V_C decays dramatically with the increase of the interface-defect distance, and the calculated data can be fitted well by an exponential function. On the other hand, a large fluctuation is clearly observed for the three structures with same interface-defect distance but different inserted layer in between the defect and Si, indicating the important role of amorphous structure variation. We also provide a decaying curve obtained by WKB approximation in Fig. 5 to support the discussion in the following section.

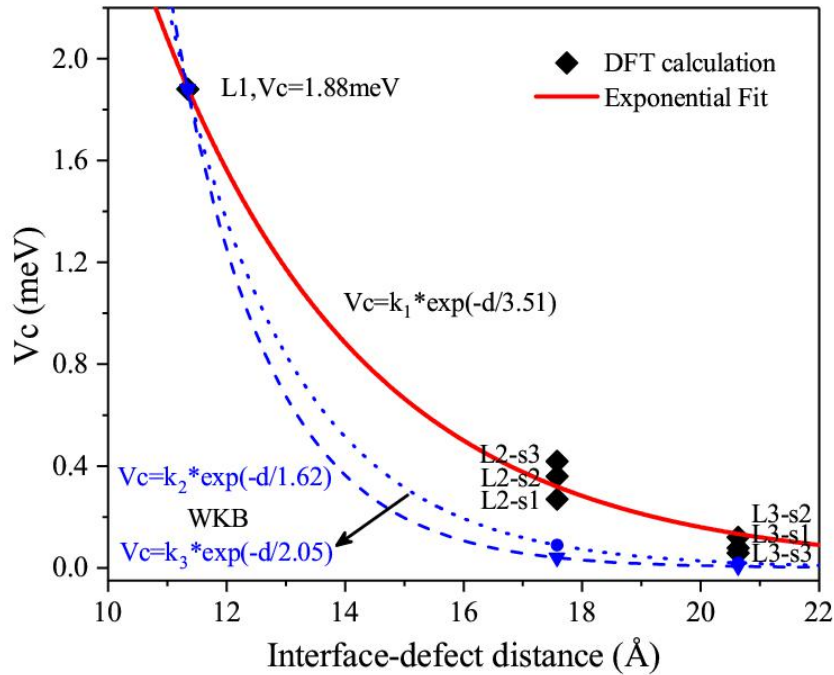


Figure 5. The dependence of V_C on the interface-defect distance and the structure variations. The DFT calculated data is fitted well by an exponential curve with a decay length of 3.51 Å and a pre-factor of $k_1=47.69$. The different DFT cases for the same c-Si/defect distance are due to different inserted a-SiO₂ layers between c-Si and the defect site. For comparison, the V_C decay predicted by WKB approximation with $m_t=0.5m_0$ is also provided, and the pre-factor k_2 and k_3 is tuned to cross the first DFT data. It is obvious that the WKB approximation overestimates the decay rate of V_C .

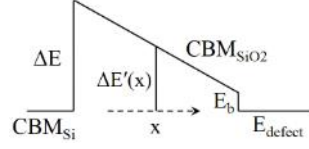
IV. Validity of the WKB approximation

Since the WKB approximation has been frequently used to estimate the state-to-state coupling in phenomenological models, it will be helpful to compare the WKB approximation with the DFT results. According to Eq. (1), the tunneling effective mass m_t and the tunneling barrier ΔE should be obtained first. When electric field is not considered, the tunneling barrier is just the conduction band offset between Si and SiO₂, which is

calculated to be 2.9eV. The tunneling mass is taken to be $0.5m_0$ according to Ref [2]. Consequently, Eq. (1) can be rewritten as:

$$V_c = \tilde{k}e^{-\frac{d}{1.62}} \quad (d \text{ in unit } \text{\AA}) \quad (11)$$

In a more sophisticated model, the electric field is used to adjust the band edge at different position:



As a result, the tunneling barrier declines along the distance away from the c-Si/a-SiO₂ interface. Consider the case when E_{defect} is pulled down to the level of $E_{\text{CBM-Si}}$ by the electric field, we then have:

$$V_c = \tilde{k}e^{-\frac{\sqrt{2m_t}}{h} \int_0^d \sqrt{\frac{E_b - \Delta E}{d} - x + \Delta E} dx} \quad (12)$$

where E_b is binding energy of the defect levels inside a-SiO₂, or say the difference between $E_{\text{CBM}(\text{SiO}_2)}$ and E_{defect} . E_b is calculated to be about 0.82eV. Plugging into $m_t = 0.5m_0$ and $\Delta E = 2.9$ eV, we have

$$V_c = \tilde{k}e^{-\frac{d}{2.05}} \quad (d \text{ in unit } \text{\AA}) \quad (13)$$

Both of Eq. (11) and Eq. (13) predict an exponential decay, and they are plotted in Fig.5. As can be see, they have much shorter decay distance (1.62 Å and 2.05 Å respectively) than the one produced by ab initio calculations (3.51 Å as shown in Fig.5). One obvious way to increase the decay length in the WKB model is to use a smaller m_t . For example, if $m_t = 0.17m_0$ is used, then the second WKB model can yield a 3.51 Å decay length. This decay is inside the a-SiO₂, thus it is difficult to judge what effective mass one should use given the amorphous nature. Nevertheless, if crystal SiO₂ is used instead, its effective mass is calculated to be 0.98 m_0 , which is obviously not able to reproduce the DFT result using the above WKB formula.

The wave function decays and their coupling inside the a-SiO₂ can also be observed directly from ab initio calculations. Here, we take the L2-s1 structure for example to check the coupling of the two wave functions at the case of $F=F_0$ (the exact coupling point). The reason to choose L2-s1 is that we can compare it with L2-s3 to reveal the effect of structure variation. To make the very small values visible, the wave function square (charge density) is transformed into its logarithm value and then shown in Fig. 6(a). We also manage to reduce the three dimensional charge density distribution into one dimensional by summing the values in the x-y plane, and then show it in Fig. 6(b). First, we can see from Fig. 6(a) that coupling of the two states is perhaps realized by the decays of both states from their original positions while meeting in the middle, instead of the tunneling of one state all the way to the center of the other state, which is usually the picture in previous WKB approximation [32]. The coupling might happen through a whole area of their overlaps. What makes it more complicated is that, while the CBM state might decay as a one-dimensional wave function, the defect state clearly decays as a three

dimensional spherical function. From Fig. 6(b), we can see that the decay of the defect state is at least two times slower than the decays of the CBM state. All these might contribute to the difference between our direct DFT calculated results and the simple WKB approximated results. One intriguing question is: whether the left part (from the interface) and right part (from the defect) of the wave function should be described by the same atomic characteristics. They are obviously different for the wave function inside c-Si and near the defect. There thus could be a coupling issue for the CBM state in c-Si to be transmitted to the defect like state in a-SiO₂ region. Indeed, we see a more abrupt drop of amplitude at the interface for the CBM state, while a more graduate decay for the defect state in a-SiO₂.

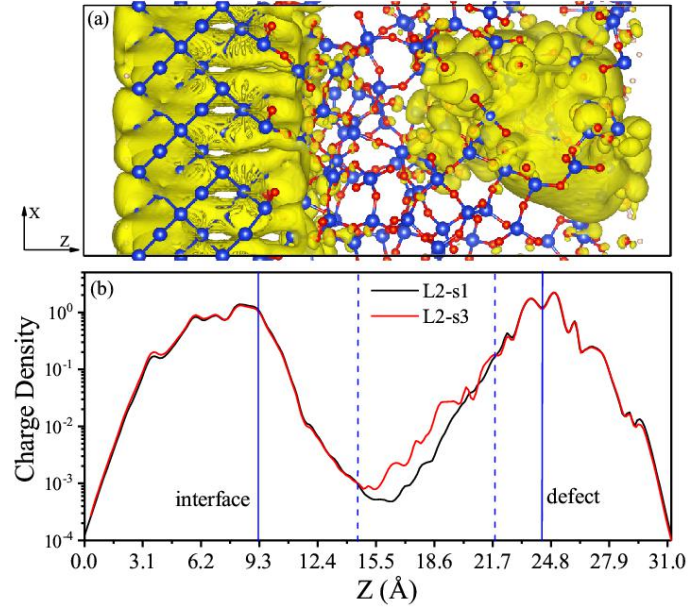


Figure 6. The $\log(\psi_{\text{CBM}}^2)$ when E_{CBM} and E_{defect} couples the strongest. (a) The wavefunction of E_{CBM} in L2-s1. (b) The projection of charge density in L2-s1 and L2-s3 on the Z direction. The area between the two dashed lines is the inserted SiO₂ layer.

To explain the V_C fluctuation in different structures, the charge density distribution in structure L2-s3 is also shown in Fig. 6(b). It can be seen that the charge density in the two structures varies significantly around the inserted SiO₂ layer, and the charge density in L2-s3 is larger than that in L2-s1. This is consistent with the fact that the V_C in L2-s3 is larger than that in L2-s1, as is seen in Fig. 5. In other words, the amorphous structure in structure L2-s3 is more favorable for the electron transfer, and thus induces larger coupling constant.

In summary, the conventional WKB approximation underestimates the electronic coupling between the E_{CBM} and E_{defect} states when only the tunneling of CBM_{Si} state is considered. It yields a too small decay length when conventional effective mass is used for the SiO₂ layer. On the other hand, if $m_r=0.17m_0$ is used, it can provide a decay curve similar to the DFT result. The DFT calculated results also yield the pre-factor \tilde{k} , which was unobtainable in WKB approximation, and must be calibrated by experiments [32].

V. Electron transfer rates

Since E_{CBM} , E_{defect} , reorganization energy λ , and the coupling constant V_C have all been obtained, the electron trapping rate can be easily calculated using Eq. (3). Fig. 7(a) shows the trapping rate at each structure when no electric field is applied. First, it can be seen that all the trapping rates are very small, with values less than 10^{-9} s^{-1} . This is because the energy difference between E_{CBM} and E_{defect} is very large, and the resulting exponential part in Eq. (3) is very small. Second, the trapping rate of the different structures with same interface-defect distance differs greatly with each other due to the fluctuation of coupling constant V_C and the $(E_{\text{defect}} - E_{CBM})$, as is seen in Fig. 5 and Fig. 2(b). More importantly, we have investigated the impact of this fluctuation due to the variation of V_C and $(E_{\text{defect}} - E_{CBM})$ separately. The variation of V_C (for the same distance defects) only changes the transition rate by a factor of three (as demonstrated by the maximum transition rate in Fig. 7(b)), but the change caused by the variation in $(E_{\text{defect}} - E_{CBM})$ can be as much as 10 order of magnitudes as shown in Fig.7(a).

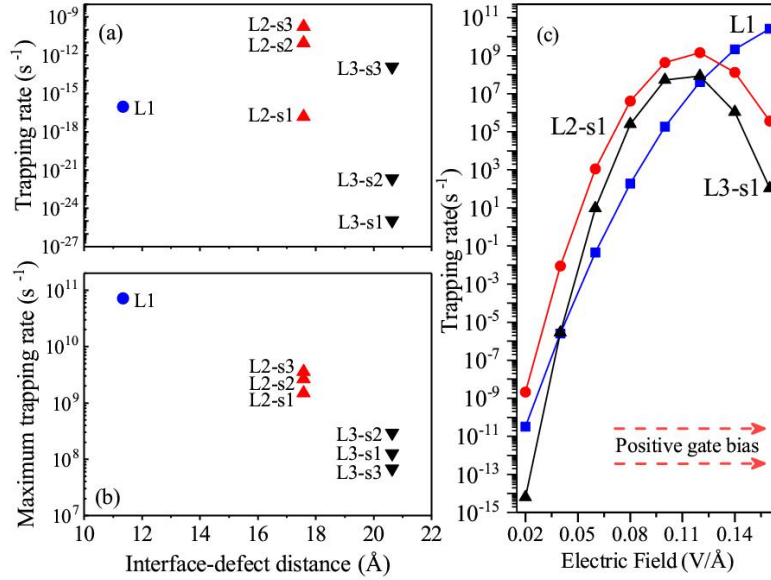


Figure 7. The electron trapping rate from E_{CBM} to E_{defect} . (a) The trapping rate in different structures when no electric field is applied. (b) The maximum trapping rate in different structures under the critical electric field F_0 . (c) The dependence of trapping rate on electric field.

However, the above zero electric field transition is of minimum consequences. What more important is the transition under an applied electric field (when the gate is turn on), especially when the defect level is in resonant with the Si CBM level due to the electric field. We have thus calculated the transition rate as a function of the applied electric field. Denoting the distance between defects and the Si/SiO₂ interface as d_{1-D} , and the electric field in the oxide induced by a positive gate voltage as F_{OX} , the Eq.(7) will be rewritten as

$$\Delta G = E_{\text{Defect}} - d_{1-D} \cdot F_{OX} - E_{CBM} - \lambda_{\text{defect}} + \lambda_{CBM-Si} \quad (14)$$

Combining Eq. (3) and Eq. (14), it can be seen that the charge trapping rate rises when E_{defect} approaches E_{CBM} (of silicon), and it falls when E_{defect} departs from E_{CBM} under very large electric field. Taking structure L1, L2-s1, and L3-s1 for example, we have calculated the oxide electric field dependent trapping rates. As is seen in Fig. 7(c), the trapping rate grows very fast with the enhancing of the electric field at the beginning, but it starts to decrease after reaching a critical electric field. The defect in structure L1 is closest to the Si/SiO₂ interface, and thus its trapping rate changes slowest with the electric field. The maximum trapping rate in all structures are shown in Fig. 7(b). Now, this transfer rate decays clearly with $d_{\text{I-D}}$ just as the V_C does. In other words, the maximum electron trapping rate from CBM to defect is controlled by V_C .

VI. Conclusion

In conclusion, we have investigated the electron trapping process in crystalline-Si/amorphous-SiO₂ interface structures by using HSE hybrid functional based DFT calculation and the Marcus electron transfer theory. The effect of interface-defect distance and amorphous structure variation are both systematically considered, and the validity and accuracy of the WKB approximation are evaluated. Results show that the coupling constant between silicon CBM state and oxygen vacancy defect state decays exponentially with the increase of the interface-defect distance. However, the conventional WKB model with the commonly used parameters significantly underestimates the decay length. There are fluctuations in the coupling constant due to both the different local environment at the defect and due to different amorphous structures in the region between the defect and the c-Si. This randomness caused fluctuation in the coupling constant can be up to 50% of its amplitude (for the same c-Si/defect distance). The calculation of coupling constant by DFT provides a way to calibrate the critical parameters used in phenomenological device simulation models. Lastly, the electron trapping rate from Si to oxygen vacancy defect is found to be mainly controlled by energy difference ($E_{\text{defect}} - E_{\text{CBM}}$) if no voltage bias is applied. However, it is controlled by the coupling constant when E_{defect} approaches E_{CBM} under positive gate voltages. The electron trapping rate from c-Si to the O_V defect level at such resonant gate voltage is 10^{11} s^{-1} , and 10^8 s^{-1} when the c-Si/defect distance is 10 and 20 Angstroms respectively.

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