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SOME RELATIONSHIPS BETWEEN ASYNCHRONOUS INTERPRETERS OF A DATAFLOW LANGUAGE*

by

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SOME RELATIONSHIPS BETWEEN ASYNCHRONOUS INTERPRETERS OF A DATAFLOW LANGUAGE*

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The theory of fixpoint semantics is applied to discover relationships between different interpreters of a dataflow programming language. A very concise model of the relative asynchrony of two interpreters is given and it is shown that one interpreter (described in the paper) is the most asynchronous interpreter possible for the given language. This same interpreter may also produce more results than other less asynchronous interpreters when executing the same program. Conditions are also given, which if followed by an actual machine architecture, will guarantee that the machine will always compute the least fixpoint of the program regardless of the order in which computation steps are carried out or the availability of processing resources.

1. INTRODUCTION

This paper applies the theory of fixpoint semantics to the design of computer architectures and operating systems. Our particular interest is in asynchrony (or concurrency, or parallelism) of execution, and our starting point is the adoption of a dataflow language [2,5] as the base machine language to be directly executed by a new computer system [1]. Dataflow languages are single-assignment languages [3,4,8,10,13] that offer many advantages over more conventional languages in areas such as modularity, inherent asynchronous execution, and more easily describable semantics. We have selected Dennis' Data Flow (DDF) language [5] as the base language for our architecture because of its relatively advanced state of development [6,13] over other dataflow languages.

We begin by specifying a formal semantic interpretation of DDF, where the semantics is best considered a model of a machine which would execute DDF. (The model is, in fact, a slight generalization of the machine proposed by Dennis [7] for executing DDF.) We call this first interpreter the Queued Interpreter (QI) since it treats the input and output lines of an operator as FIFO queues. We then give a second semantic interpretation of DDF corresponding to a second machine [1] called the Unfolding or Unraveling Interpreter (UI) which does not explicitly use FIFO queues. Using the relation "less defined than or equal to" on the history of lines (variables) as the basis of our measure of asynchrony, we show that for any program in DDF the UI machine gives results that are more defined than or equal to QI, and

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that UI is capable of more asynchrony (concurrency of execution) than QI. Furthermore, we extend the above comparison of UI and QI and show that no interpreter operating under the basic dataflow rules can produce greater concurrency of execution than UI, unless that interpreter is allowed to "guess" the result of computations. Lastly, we show that if a machine operates under three particularly simple conditions, then we can guarantee that regardless of how slowly it progresses (due, say, to a reduction in available resources), or the order in which it carries out its allowed work, the least fixpoint will be the natural result of its computation.

2. THE DATAFLOW LANGUAGE

Figure 1 shows the operation of the dataflow operators: function and

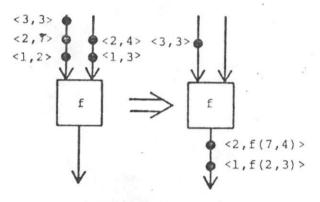


Figure la

The result of all firings of a function or predicate operator f under QI

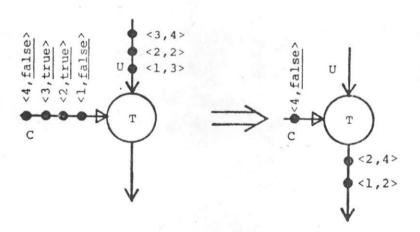


Figure 1b

The results of a gate-if-true operator

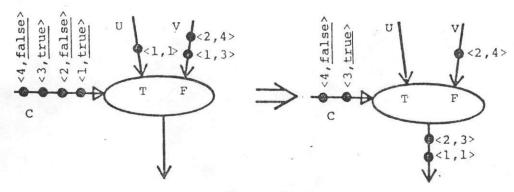


Figure 1c

The merge operator

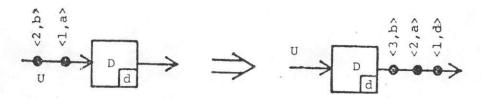


Figure 1d

The D-box operator (produces initial tokens)

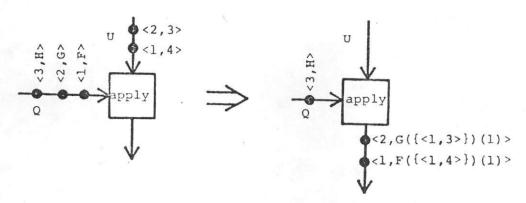


Figure le
The apply operator

predicate, gate-if-true (gate-if-false is not shown and is simply the complement of gate-if-true), merge, D, and apply. Dataflow is based upon two principles:

- An operator fires (produces an output) whenever the inputs required by that operator are present.
- 2. All operators are functional and produce no side-effects.

Data is carried by tokens that flow along directed arcs connecting the output of one operator to the input of another operator. Thus, in Figure la two complete sets of values have arrived at function f, and one value of a third set. A token is represented as an ordered pair <i,v> where i is the logical position of the token relative to the lst, 2nd, ..., i-lth, ith, i+lth, ... etc. tokens on that same line. The second component v of <i,v> is the data value carried by the token. As can be seen in Figure la, function f has fired two times, once for each set of input values. As soon as some token <3,v> arrives on the right-hand input to f, f may fire. Firing causes the input tokens to be destroyed, and if the operator produces an output then a new token is created to carry that output value.

Figure 1b shows the gate-if-true operator whose function is to gate the input data to the output only if the boolean (control) input carries the value true; otherwise no output is produced (but the inputs are still absorbed). Note that the position indicator i in the output tokens follows the order of the input tokens, and does not leave "holes" in the output line's history. Gates may be used to construct a switch operator which in turn may be used to build an if-then-else operator. For example, Figure 2 shows a switch and it

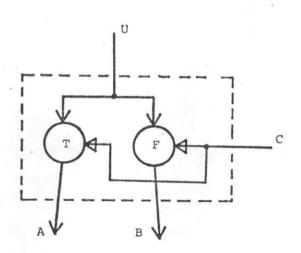


Figure 2

A switch built from gate-if-true and gate-if-false operators

operates as follows: the ith data input arriving at the switch along line U is <i,u>, and the ith control input <i,c> arrives along line C. The values on lines U and C are sent to both the gate-if-true and gate-if-false operator. This is because there is a fork in each line (represented by the smaller black spot on each of lines U and C); the single token input to the fork is duplicated and two tokens are output by the fork, one token to each destination. Thus, one output line may feed several inputs. In the above example, if c is true then the token <i,u> will appear on line A and no output will appear on line B, or if c is false then <i,u> will appear on line B and no output will appear on line A. A switch thereby directs the flow of data along one of two paths depending on some boolean value. In any dataflow program, we require that any operator expecting boolean values along some line receives only booleans.

A merge operator is shown in Figure lc and is, in a sense, the inverse of a switch. The purpose of a merge is to join data from two input paths onto a single output path, the order being determined by a third boolean input. Note that each input line has its own history of values and from those two histories and a control history, a third output history is produced.

The D operator serves to introduce an intial token onto a line, and otherwise behaves as the identity function. An example of a dataflow program composed of the above operators is shown in Figure 3. This program has one input line A_1 and one output line X_6 .

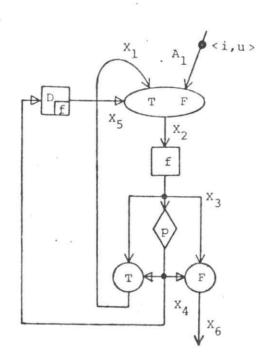


Figure 3

A program in Dennis' dataflow language

The last operator is the apply operator. It accepts two inputs: a dataflow program, and an argument. The value produced is the result of applying the program to the given argument. Only one argument is allowed; multiple arguments may be handled by a suitable encoding of data.

The above has been a brief description of the operators, and one small example of how the operators may be composed to form a program (a term we define more formally below). Of particular importance is that dataflow programs are "functional" since no operator has any side-effects, and no two ouput lines may be connected to a single input line — the so-called single-assignment restriction. Note that one output may feed many distinct inputs; this produces no side-effects.

3. THE INTERPRETERS

If we assume we have a program comprising a set of m input lines, n operators and thus n output lines (we allow only one output line per operator, but in fact this is no restriction), then there are many possible ways in which the operators could be defined to behave when tokens flow along arcs and arrive at an input. In particular, we can say that each line behaves as a FIFO queue so that an operator may fire using token i from input line L only if token i-l from L has already been absorbed. That is, the lines behave as pipes down which a sequence of values flows, and the values can never be taken out of sequence. Actually, this is the usual model of dataflow systems [3,5,8,9,10] and we call the underlying interpreter for this model QI meaning Queued Interpreter. Later we will introduce a more asynchronous interpreter UI meaning Unraveling or Unfolding Interpreter which produces results at least as defined as QI, and sometimes more defined. Our interest in UI is due to its asynchrony which, although in most cases produces no more results than QI, it does so with far greater freedom. Then given a suitable architecture [1] that can exploit this asynchrony, the same results can be computed much more quickly.

The model presented in this paper allows us to explicitly capture the notion of asynchrony and thereby make statements about how systems compare with regard to how they compute, and not just what they compute. This is our primary interest.

4. THE MODEL - GENERAL REMARKS AND DEFINITIONS

We model the dataflow language, regardless of interpreter, by considering the history of all tokens produced on a line due to the action of some operator on some histories given to it as input. By varying the definition of the operator as to how it computes outputs for a given input, we can represent each interpreter of interest.

The history of some line L is a set of ordered pairs $\langle k, v_k \rangle$:

$$L = \{\langle 1, v_1 \rangle, \langle 2, v_2 \rangle, \dots, \langle k, v_k \rangle, \dots \}$$

where L is a function, i.e., $\langle k, v_k \rangle_{\epsilon L} \Rightarrow L(k) = v_k$. More precisely,

$$L: J^+ \rightarrow D \cup \{\bot\}$$

where I is the special undefined value, and where $J^+ = \{0,1,\ldots\}$ is the set of non-negative integers and D is any value domain over which a dataflow program may compute, for example, reals, integers, booleans (denoted true and false here), strings, etc. The range Du{I} of a history function L will be a chain-complete poset with order relation \subseteq , where for any $v \in Du\{I\}$.

and
$$v \subseteq v$$
.

Note that the value ${\scriptscriptstyle \perp}$ is not in D, and no confusion arises if we define for any history L

$$L(i) = \begin{cases} v & \text{if } \langle i, v \rangle_{\epsilon} L \\ 1 & \text{otherwise} \end{cases}$$

Note that L is a "naturally-extended" function and is thus monotone.

We use the symbol $\underline{\ }$ to represent a relation on any pair of histories L and L':

If we consider a history to be a function defined by a set of <integer, value> ordered pairs, and thus a set of tokens on a line, the L \subseteq L´ is exactly token set containment. In particular, there is the totally undefined history represented by the empty set ϕ , where $\phi \subseteq L$ for all histories L.

Let $H = \{L \mid L:J^+ \to D \cup \{1\}\}$ be the set of all possible histories. Histories have certain properties which we need to define in order to conveniently specify the semantics of dataflow operators under various interpreters. The predicate "pr" (pr for "proper") is defined in the domain $H \times J^+$ as

$$pr(L,i)$$
 iff $(\forall j)(1 \le j \le i \Rightarrow L(j) \ne \bot)$

that is, a history is proper up to and including position i if all tokens from 1 through i are in the history; this is equivalent to saying pr(L,i) iff $(\forall j \leq i)$ ($\langle i,v \rangle_{\epsilon}L$ for some v). Note that pr(L,0) is always true. We also need two mappings from $H \times J^+$ into J^+ :

$$tc(C,i) = \#\{j | L(j) = \underline{true} \text{ and } l \le j \le i\}$$

$$fc(C,i) = \#\{j | L(j) = \underline{false} \text{ and } l \le j \le i\}$$

where #A for some set A means the cardinality of the set A. Thus tc(C,i) is the count of the number of true tokens on line C up to and including the token in the ith position on C. The corresponding

statement holds for fc(C,i). As an example in Figure 1b, tc(C,4)=2=tc(C,3), and fc(C,3)=1. At this point we prove some useful facts about histories.

Proposition A - Let A, B, and C be histories, where A \in B and $i \in J^+$.

- 1. $A(i) \neq i \Rightarrow A(i) = B(i)$ 2. $pr(A,i) \Rightarrow pr(B,i)$
- $pr(A,i) \Rightarrow tc(A,i)=tc(B,i)$ and
- fc(A,i)=fc(B,i) $C(i) = \underline{true} \implies tc(C,i) = tc(C,i-1)+1$ $C(i) = \overline{false} \Rightarrow fc(C,i) = fc(C,i-1)+1$

Proof

1. by the definition of \leq 2. $pr(A,i) \Rightarrow (\forall j) (1 \le j \le i \Rightarrow A(j) \ne 1)$ \Rightarrow (Vj) (1 \le j \le i \Rightarrow B(j) \ne 1) tc(A,i) = $\#\{j \mid A(j) = \underline{\text{true}} \land 1 \le j \le i\}$ = $\#\{j \mid B(j) = \underline{\text{true}} \land 1 \le j \le i\}$ by proposition (A.1) by definition of pr by proposition (A.1) = tc(B,i); similarly for fc.

4. $tc(C,i) = \#\{j \mid C(j) = \underline{true} \land 1 \le j \le i\}$ = $\#\{j \mid C(j) = \underline{true} \land i \le j \le i-1\} + \#\{i \mid C(i) = \underline{true}\}$ = tc(C,i-1)+1

Consider now the single-input DDF program shown in Figure 3. As stated before, input histories will be designated A_1, \ldots, A_m , and with no loss in generality we assume each of the n DDF operators in the program has only one output and it is labelled as line L_{i} for operator τ_i of the program, where

$$\tau_i : H^n \times H^m \rightarrow H$$
 $1 \le i \le n$

Thus each L_i is a history, and a dataflow operator τ_i (function, gate-if-true, merge, etc.) is a functional that maps histories generated internally by the program (the L_j) and histories given as input (the Ak), into an output history Li.

A dataflow program is a set of n equations on n history variables and m history input parameters where

$$L = \tau[L;A] \tag{4.1}$$

or notationally,

$$L = \tau_{A}[L]$$

and the symbols stand for vectors of the corresponding symbol.

A meaning of such a program is some fixpoint solution to (4.1). this paper the least fixpoint of $\tau_{\rm A}$ will be the meaning of a DDF program [9,11,12]. Theorem 2 proves that the least fixpoint is actually the solution that a machine would compute given some rather mild conditions.

The following subsections detail the definitions of the operators as functionals and prove them monotone under interpreter QI and then under UI. To do this precisely, we need one additional semantic notation for "definition by case". The expression

$$(p_1 \rightarrow t_1 ; p_2 \rightarrow t_2 ; ... ; p_n \rightarrow t_n ; t_{n+1})$$

means "if p_1 is true then the result is t_1 otherwise, if p_2 is true then the result is t_2, \ldots , if p_n is true then t_n is the result, and if none of p_1, p_2, \ldots, p_n is true then the result is t_{n+1} ". We will often nest case definitions, so some of the t_i may themselves be case definitions.

5. THE OPERATORS UNDER QI

Recall that QI is the queued interpreter, meaning that each line behaves as a FIFO queue of tokens where the token $\langle i,v \rangle$ is the ith token in the queue L. We can describe the behavior of each operator under QI as it accesses these FIFO queues by requiring that pr(L,i) be true if token $\langle i,v \rangle \in L$ is to be accepted as an input by the operator to produce an output token. If pr(L,i) is not true, then either $\langle i,v \rangle \notin L$ or some predecessor token in the FIFO queue L is not present. In either case $\langle i,v \rangle$ cannot take part in the computation until other operators fire and make pr(L,i) true. We do not prove it here, but the operators under QI will never produce any tokens out of FIFO order, that is pr(L,#L) is always true.

5.1 Functions, predicates

These operators are characterized by the fact that the ith set of inputs produces the ith output, and again as for all operators under QI, the FIFO order of token input holds. We let capital letter F name the history (semantic) value of the operator f. Functions and predicates may have n input lines and one output line. Note in the following definition that as more tokens arrive on the various input lines, output appears only in FIFO order and is monotone (we prove this below). We do not distinguish here between program input lines A_i and other lines L_j since to do so would impose additional burdensome notation. We consider the functions and predicates with two inputs only; the other cases are similar.

$$F[U,V] = \bigcup_{i \ge 1} (pr(U,i) \land pr(V,i) \rightarrow \{\langle i, f(U(i), V(i)) \rangle\}; \phi)$$
 (5.1)

Our reason for writing the operators in this fashion rather than a more abstracted function definition, is to mimic as closely as possible actual machine behavior under these operators and the production of output tokens.

<u>Proposition</u> - F[U,V] is monotone <u>Proof</u> - First we show that F[U,V] is a function if U and V are functions:

$$\langle i, x \rangle \in F[U, V] \implies x = f(U(i), V(i))$$

⇒ x is uniquely determined by f,U,V, and i (since f,U and V are all functions).

Hence, F can output exactly one token in the ith position of the output queue. Now we prove monotonicity of F:

Let U1, U2, V1, and V2 be histories such that U1 \leq U2 \wedge V1 \leq V2 is true. Let I1 = {i | pr(U1,i) \wedge pr(V1,i) is true}.

$$F[U1,V1] = \bigcup_{i \in I1} \{\langle i, f(U1(i), V1(i)) \rangle\} \text{ by equation } (5.1)$$

$$= \bigcup_{i \in I1} \{\langle i, f(U2(i), V2(i)) \rangle\} \text{ by proposition } (A.1)$$

$$\subseteq F[U2,V2] \text{ by equation } (5.1)$$

5.2 Gates

We show only the gate-if-true operator; corresponding remarks can be made for gate-if-false.

GATE-IF-TRUE[C,U] =
$$\bigcup_{i\geq 1} (pr(C,i) \wedge pr(U,i) \rightarrow (C(i)=\underline{true} \rightarrow \{\langle tc(C,i),U(i)\rangle\}; \phi), \phi) (5.2)$$

That is, gate-if-true will fire for the ith input set from C and U if both are proper up to and including the ith input, and an output token is produced if C(i)=true. In case C(i) is true, then the output produced will be the jth output token where j is the number of true tokens so far absorbed by gate-if-true. No output is produced for any false valued token absorbed on line C. Note that in case either C or U is not proper (gate-if-true cannot yet fire for the ith input set) or if C and U are proper but C(i) is false then the empty set is the result for the ith firing.

Proposition - GATE-IF-TRUE[C,U] is monotone.
Proof - First we show that if C and U are functions, then
GATE-IF-TRUE[C,U] is a function.

To establish a contradiction,

$$\langle j, u_1 \rangle, \langle j, u_2 \rangle \in GATE-IF-TRUE[C, U]$$

$$\Rightarrow (\exists j_1, j_2) (j_1 \langle j_2 \rangle \land tc(C, j_1) = tc(C, j_2) = j \land C(j_1) = C(j_2) = \underline{true})$$

But by Proposition A.4, $tc(C,j_1) < tc(C,j_2)$ which is a contradiction. Hence $< j, u_1 >$ and $< j, u_2 >$ cannot be distinct tokens output by GATE-IFTRUE.

To show monotonicity, let Cl,C2,Ul and U2 be histories such that Cl_C2 \land Ul_CU2

Let $I1 = \{i | pr(Cl, i) \land pr(Ul, i) \text{ is true} \}.$

=
$$\bigcup_{i \in I1} (Cl(i) = \underline{true} \rightarrow \{\langle tc(Cl,i), Ul(i) \rangle\}; \phi)$$
 by equation (5.2)

$$= \bigcup_{i \in I1} (C2(i) = \underline{true} \rightarrow \{\langle tc(C2,i), U2(i) \rangle\}; \phi) \text{ by proposition (A.1),}$$

$$(A.2), (A.3)$$

$$\subseteq \bigcup_{i \in I} (C2(i) = \underline{true} \rightarrow \{\langle tc(C2,i), U2(i) \rangle\}; \emptyset) \cup$$

$$\bigcup_{i \in J^{+}-I1} (pr(C2,i) \wedge pr(U2,i) \rightarrow (C2(i) = \underline{true} \rightarrow \{ < tc(C2,i), U2(i) > \}$$

$$; \phi); \phi)$$

= GATE-IF-TRUE[C2,U2] by equation (5.2)

5.3 Merge

$$MERGE[C,U,V] = \bigcup_{i \ge 1} (pr(C,i) \land pr(U,tc(C,i)) \land pr(V,fc(C,i)) \rightarrow (C(i)=true \rightarrow \{\langle i,U(tc(C,i))\rangle\}; C(i)=\overline{false} \rightarrow \{\langle i,V(fc(C,i))\rangle\}; \phi)$$

$$(5.3)$$

Proposition - MERGE[C,U,V] is monotone. Proof - First we show that MERGE[C,U,V] is a function if C,U and V are functions. Let $\langle i,x \rangle \in \text{MERGE}[C,U,V]$. Then x=U(tc(C,i)) if C(i)=true or x=V(fc(C,i)) if C(i)=false. Since C(i) is either true or false but not both, x must be unique. Thus MERGE[C,U,V] is a function.

To show monotonicity, let C1, C2, U1, U2, V1 and V2 be histories such that $C1 \subseteq C2 \land U1 \subseteq U2 \land V1 \subseteq V2$ let $I1 = \{i \mid pr(C1,i) \land pr(U1,tc(C,i)) \land pr(V1,fc(C1,i)) \text{ is true}\}.$

MERGE[C1,U1,V1]

$$= \bigcup_{i \in I1} (Cl(i) = \underbrace{true}_{+} \{ \langle i, Ul(tc(Cl,i)) \rangle \};$$

$$i \in I1 \ Cl(i) = \underbrace{false}_{+} \{ \langle i, Vl(fc(Cl,i)) \rangle \}) \text{ by equation } (5.3)$$

$$= \bigcup_{i \in Il} \frac{(C2(i) = true)}{C2(i) = false} \rightarrow \{\langle i, U2(tc(C2,i)) \rangle\};$$
 by proposition A

<u>← MERGE[C2,U2,V2]</u> by equation (5.3)

5.4 D operator

The D operator introduces an initial token with value b onto a line

$$D_{b}[U] = \bigcup_{i \ge 1} (pr(U,i) \to \{\langle i+1, U(i) \rangle\}; \phi) \cup \{\langle 1, b \rangle\}$$
 (5.4)

<u>Proposition</u> - D_b[U] is monotone. <u>Proof</u> - Clear.

5.5 Apply

 $APPLY[Q,U] = \bigcup_{i \ge 1} (pr(Q,i) \land pr(U,i) \rightarrow \{\langle i, apply(Q(i),U(i)) \rangle\}; \phi) (5.5)$

where apply(q,u) is the meaning of the output line of the dataflow program represented by q with input history <1,u>. We require procedure q to be well-behaved [6] so that exactly one token can be produced in the output history of q.

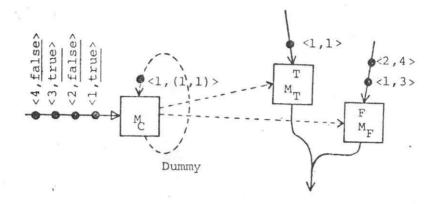
Proposition - APPLY[Q,U] is monotone.
Proof - Just as for the case of functions and predicates.

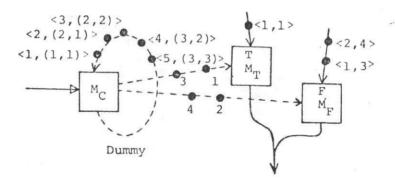
This completes the definition of the operators under QI and the proofs of their monotonicity.

6. THE OPERATORS UNDER UI

We now describe the behavior of the UI interpreter. UI is more asynchronous in its operation than QI, and has the property (shown later) that it may produce even more results than QI. Again, QI is the more usual interpreter and assumes that lines are FIFO queues along which data tokens flow. The position of each token in a queue L corresponds to the relative time at which each token was produced. Time, however, need not be so closely related to queue position. For example, if the ith set of inputs to a function are all present, then regardless of the presence or absence of the i-lth or i+lth set of inputs, the ith output can be calculated. In fact, if each token carried with it a specification of its logical queue position as well as its value (rather than depending upon some physical position in a pipe to specify that information), then by definition the queue position of every token is known and the time at which a token is produced becomes irrelevant. This is precisely what the machine in [1] proposes to do. Theoretically, more results may be produced, but the more important aspect is the increase in asynchrony and potentially concurrent execution, which can be very siginificant. (We have achieved orders of magnitude increases in concurrency of execution with a system based upon the techniques reported here, but where the programmer uses constructs only of the usual arithmetic, if-then-else, and while-do types. By restricting the available constructs, even greater asynchrony can be obtained.)

The following subsections define the behavior of each of the dataflow operators under UI. As an example, consider Figure 4 which shows the internal operation of a merge under UI. Each box may be considered an individual suboperator, where each suboperator behaves just as a function behaves under UI. That is, regardless of the status of the i-lth or i+lth input set to each suboperator, the suboperator may fire the ith output token as soon as the ith input set is present at that suboperator. Thus, Figure 4 shows that merge is three suboperators called M_C, M_T, and M_F. The dashed lines are just like program lines and have histories but they are internal operator communication lines. The function of M_C is to keep track of which token position from U and from V is to supply the next output token from the merge. Thus information is kept on the line DUM (for Dummy) and is essentially the ordered-value token <i,(tc(C,i),fc(C,i))>. Each time M_C fires, it inputs a token from C and the token from DUM. The input from C says which of lines U and V is to supply an input token, and DUM specifies which position within that line. To cause the appropriate token from U or V to be sent to the merge operator's output, M_C sends a token to M_T or M_F. The value





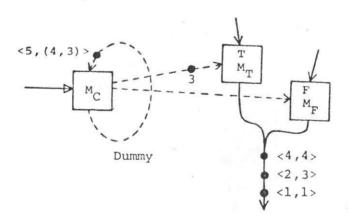


Figure 4

Merge' under the interpreter UI progresses further than merge under QI for the given input

carried by that token then tells $M_{\rm T}$ or $M_{\rm F}$ the output position to be assumed by the token it outputs. Note that $M_{\rm C}$ must be sequential and only one token exists in DUM at any time, while many tokens may be in transit to $M_{\rm T}$ and $M_{\rm F}$ at any instant. In this way, tokens may arrive in arbitrary time order on U and V, and as long as C has defined the appropriate output position, the tokens may be moved by $M_{\rm T}$ and $M_{\rm F}$ to the output whenever they arrive.

To denote the interpreter UI, we use the prime mark ($^{\prime}$) so F is f according to QI while F $^{\prime}$ is f according to UI.

6.1 Functions, predicates - Again we give the case of 2-input functions and predicates.

$$F'[U,V] = \bigcup_{i \ge 1} (U(i) \ne 1 \land V(i) \ne 1 \rightarrow \{\langle i, f(U(i), V(i)) \rangle\}; \phi)$$

$$(6.1)$$

 $\frac{\text{Proposition}}{\text{Proof}} - \text{F'[U,V]} \text{ is monotone.} \\ \frac{\text{Proof}}{\text{Proof}} - \text{Similar to the proof for F[U,V]} \text{ given in section (5.1) provided the set Il is replaced by the set } \{i \mid \text{Ul}(i) \neq_{\perp} \land \text{Vl}(i) \neq_{\perp} \text{ is true}\}.$

6.2 Gates

Again we show only the gate-if-true operator. A schematic of the gate-if-true under UI is shown in Figure 5.

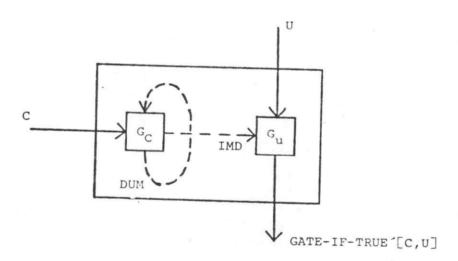


Figure 5

Gate-if-true under interpreter UI

where DUM carries the count of the number of tokens so far output by GATE-IF-TRUE'. Each token on IMD carries an ordered pair as its value. $IMD(i)_1$ selects the first value of the pair and is a copy of C(i). This tells the suboperator G_U whether or not to output U(i). IMD(i) 2 selects the second value of the pair, which tells Gy the proper position of the token in the output history.

GATE-IF-TRUE
$$\begin{bmatrix} C,U \end{bmatrix} = \bigcup_{i \ge 1} (IMD(i) \ne 1 \land U(i) \ne 1 \rightarrow \{ \}; \phi)$$
 (6.2.1)

where, letting
$$t_i = DUM(i) + (C(i) = \underline{true} \rightarrow 1; 0)$$
 (6.2.2)

$$IMD = \bigcup_{i \ge 1} (DUM(i) \ne 1 \land C(i) \ne 1 + \{\langle i, (C(i), t_i) \rangle\}; \phi)$$
 (6.2.3)

$$DUM = \bigcup_{i \ge 1} (DUM(i) \ne i \land C(i) \ne i \rightarrow \{ < i+1, t_i > \}; \phi) \cup \{ < 1, 0 > \}$$
 (6.2.4)

Thus DUM has an initial token in its history, all subsequent members being formed in order iteratively when the corresponding tokens on C arrive. Note that G_{C} necessarily fires in proper order while G_{U} may not. In actual operation this sequentiality of GC is not a great hardship, since predicates in programs are often very short in computation time and waiting for them to arrive in order should not appreciably slow activity.

To prove monotonicity and some other results, we need the following proposition.

Proposition B - Let i≥1 be an integer; then

- 1. $DUM(i+1)\neq 1 \Rightarrow pr(C,i)$ 2. $pr(C,i) \Rightarrow IMD(i)_2=DUM(i+1)=tc(C,i)$
- $IMD(i) \neq i \Rightarrow pr(C, \tilde{i})$

Proof

1. By induction

 $DUM(2) \neq 1 \implies C(1) \neq 1 \implies pr(C,1)$ by equation (6.2.4) i=k: DUM(k+1) $\neq 1 \Rightarrow pr(C,k)$ assume i=k+1: DUM $(k+2)\neq 1 \Rightarrow$ DUM $(k+1)\neq 1 \land C(k+1)\neq 1$ by equation (6.2.4) \Rightarrow pr(C,k) \land C(k+1) \neq 1 by induction hypothesis \Rightarrow pr(C,k+1) by the definition of pr.

2. By induction

- \Rightarrow IMD(k+1)2=DUM(k+2)=tc(C,k)+(C(k+1)=true \rightarrow 1;0) by induction hypothesis.
- \Rightarrow IMD(k+1)₂=DUM(k+2)=tc(C,k+1) by the definition of tc.
- 3. Immediate by proposition (B.1) and equation (6.2.3):

$$IMD(k)\neq 1 \implies DUM(k+1)\neq 1 \implies pr(C,k)$$

Due to proposition B, IMD can be eliminated from equation (6.2.1). Hence

GATE-IF-TRUE
$$[C,U] = \bigcup_{i\geq 1} (pr(C,i) \wedge U(i) \neq i + \{\langle tc(C,i),U(i) \rangle\}; \phi)$$
 (6.2)

Proposition - GATE-IF-TRUE'[C,U] is monotone. Proof - The proof that GATE-IF-TRUE'[C,U] is a function if C and U are functions is identical to the proof given in section 5.2. The proof of monotonicity of GATE-IF-TRUE given in section 5.2 is valid here too, provided set Il is replaced by the set $\{i \mid pr(Cl,i) \land Ul(i) \neq i \text{ is true}\}.$

6.3 Merge

Figure 3 is a schematic for merge, where $IMD_T(i)$ and $IMD_F(i)$ inform suboperators M_T and M_F , respectively, the output line position for the token U(i) and V(i). A token DUM(i) carries an ordered pair where $DUM(i)_1$ is the next instance of suboperator M_T to be activated, while $DUM(i)_2$ is that of M_F .

where as before, we let
$$t_i = DUM(i)_1 + (C(i) = \underline{true} + 1; 0)$$
 (6.3.2)
 $f_i = DUM(i)_2 + (C(i) = \underline{false} + 1; 0)$ (6.3.3)

$$IMD_{\mathbf{T}} = \bigcup_{i \geq 1} (DUM(i) \neq_{\perp} \land C(i) \neq_{\perp} + (C(i) = \underline{true} \rightarrow \{<\mathbf{t}_{\underline{i}}, i>\}; \phi); \phi) \quad (6.3.4)$$

$$IMD_{\mathbf{F}} = \bigcup_{i \geq 1} (DUM(i) \neq_{\perp} \land C(i) \neq_{\perp} + (C(i) = \underline{false} \rightarrow \{<\mathbf{f}_{\underline{i}}, i>\}; \phi); \phi) \quad (6.3.5)$$

$$DUM = \bigcup_{i \geq 1} (DUM(i) \neq_{\perp} \land C(i) \neq_{\perp} \rightarrow \{<\mathbf{i}+1, (\mathbf{t}_{\underline{i}}, \mathbf{f}_{\underline{i}})>\}; \phi) \quad (\{<\mathbf{1}, (0, 0)>\}$$

$$(6.3.6)$$

We need the following proposition to prove monotonicity.

Proposition C - For integers i,j≥l

- 1. $DUM(i+1)\neq i \Rightarrow pr(C,i)$
- 2. $IMD_{T}(j)=i \implies pr(C,i) \land C(i)=true \land j=tc(C,i)$
- 3. $IMD_{F}(j)=i \Rightarrow pr(C,i) \wedge C(i)=false \wedge j=fc(C,i)$
- 4. $IMD_{T}(j)=i \implies (\forall k) (IMD_{T}(k)\neq i)$

Proof

Same as proof of proposition (B.1) IMD_T(j)≠⊥ \Rightarrow ($\exists i$) ($t_i = j \land C(i) = true \land DUM(i) \neq \bot$ by equation (6.3.4) \Rightarrow (3i) (pr(C,i) \land C(i)=true \land j=t;) by proposition (C.1). Now by induction we prove that $t_i = tc(C,i)$ i=1: $t_1 = DUM(1)_1 + (C(1) = true \rightarrow 1;0)$ by equation (6.3.2) = $(C(1) = true \rightarrow 1;0)$ since DUM(1)=0= tc(C,1)by definition of to i=k: $t_k = tc(C,k)$ assume $t_{k+1} = DUM(k+1)_1 + (C(k+1) = true \rightarrow 1;0)$ = $t_k + (C(k+1) = true \rightarrow 1;0)$ by equation (6.3.6) = $t_{C}^{R}(C,k) + (C(\overline{k+1}) = \underline{true} \rightarrow 1;0)$ by induction hypothesis = tc(C,k+1)by definition of tc. hence, $t_i = tc(C,i)$. Hence $IMD_{\mathbf{T}}(j) \neq 1 \implies (\exists i) (pr(C,i) \land C(i) = \underline{true} \land j = tc(C,i))$ Now we show that i such that the predicate $(pr(C,i) \land C(i)=true \land i$ j=tc(C,i)) is true, must be unique. Assume $i_1 < i_2$ and both i_1 and i_2 satisfy the above predicate. Then j=tc(C,i₂)=tc(C,i₂-1)+1 >tc(C,i₂-1) since C(i2)=true Due to the definition of tc, $(\forall i)$ (i<i2 \Rightarrow tc(C,i) \le tc(C,i2)) Hence $j > tc(C, i_2-1) \ge tc(C, i_1) = j$ contradiction. Therefore i1=i2 and $IMD_{T}(j)=i \Longrightarrow pr(C,i) \land C(i)=true \land j=tc(C,i)$ 3. Same as 2. 4. Obvious ${\rm IMD}_{\underline{T}}$ and ${\rm IMD}_{F}$ can be eliminated from equation (6.3.1) due to proposition C. MERGE [C, U, V] $= \bigcup (pr(C,i) \land C(i) = \underline{true} \land U(tc(C,i)) \neq \iota \rightarrow \{\langle i, U(tc(C,i)) \rangle\}; \emptyset) \cup$ $\bigcup_{i} (\operatorname{pr}(C,i) \wedge C(i) = \underline{false} \wedge V(\operatorname{fc}(C,i)) \neq \iota + \{\langle i, V(\operatorname{fc}(C,i)) \rangle\}; \phi)$ $= \bigcup_{i>1} (pr(C,i) + (C(i) = true \land U(tc(C,i)) \neq i + \{\langle i, U(tc(C,i)) \rangle\};$

 $C(i) = false \land V(fc(C,i)) \neq \downarrow \rightarrow \{\langle i, V(fc(C,i)) \rangle\}; \phi\}; \phi$

Proposition - MERGE'[C,U,V] is monotone.

Proof - Same as the proof for MERGE in section (5.3) provided set II is defined as the set $\{i \mid pr(Cl,i) \land ((Cl(i)=true \land U(tc(Cl,i))\neq \iota) \lor (Cl(i)=false \land V(fc(Cl,i))\neq \iota)\}$ is true

6.4 D operator

As under QI, the D operator under UI serves to introduce an initial token; all other tokens pass through.

$$D_{b}[U] = \bigcup_{i \ge 1} (U(i) \ne 1 \rightarrow \{ < i+1, U(i) > \}; \phi) \cup \{ < 1, b > \}$$
 (6.4)

Proposition - D'[U] is monotone.
Proof - Clear.

6.5 Apply

$$APPLY'[Q,U] = \bigcup_{i \ge 1} (Q(i),U(i) \ne 1 \rightarrow \{\langle i,apply(Q(i),U(i))\rangle\}; \phi) \qquad (6.5)$$

where apply (q, u) is the same as discussed previously in section (5.5).

Proposition - APPLY [Q,U] is monotone.
Proof - Clear.

7. COMPARISON OF QI AND UI

Consider a function operator f(u) first under QI and then under UI. Let history

$$U = \{\langle 1, u_1 \rangle, \langle 3, u_3 \rangle, \langle 5, u_5 \rangle\}$$

Then $F[U] = \{\langle 1, f(u_1) \rangle\}$, since pr(U,i) is true only for i=1. No more output can be produced by F[U] until $\langle 2, u_2 \rangle$ arrives in history U. However, under UI the result is $F^*[U] = \{\langle 1, f(u_1) \rangle, \langle 3, f(u_3) \rangle, \langle 5, f(u_5) \rangle\}$ since FIFO order is unnecessary. Whenever $\langle 2, u_2 \rangle$ arrives in history U, then $\langle 2, f(u_2) \rangle$ will be output to $F^*[U]$. This is an example of the greater asynchrony evident in UI than in QI, which we model by saying $F[U] \subseteq F^*[U]$ for all U, and in particular there are histories V where $F[V] \subseteq F^*[V]$. In actual machine operation this

greater asynchrony under UI could very easily manifest itself in greater speeds of execution. As a final example, consider gate-if-true with control input C = $\{<1, true>, <2, true>, <3, false>, <4, true>$ and with data input U = $\{<1, u_1>, <3, u_3>, <4, u_4>\}$. The semantics under QI are GATE-IF-TRUE[C,U] = $\{<1, u_1>\}$. Under UI, we have GATE-IF-TRUE^[C,U] = $\{<1, u_1>, <3, u_4>\}$ and whenever $<2, u_2>$ arrives in history U, it will be output as $<2, u_2>$. Note that the absence of $<2, u_2>$ on input U did not prevent "later" tokens in U from being output under UI. Again we can write GATE-IF-TRUE[C,U] \le GATE-IF-TRUE^[C,U].

The following propositions prove that each operator under UI is, in general, more asynchronous than under QI; when speaking of operators in general we write this relationship as OP $\underline{\circ}$ OP $\underline{\circ}$.

```
Proposition - F < F'
Proof - As before we prove the case for binary functions.
Let I = \{i | pr(U,i) \land pr(V,i) \text{ is true} \}
and \Gamma = \{i \mid U(i) \neq i \land V(i) \neq i \text{ is true}\}.
F[U,V] = \bigcup \{\langle i, f(U(i), V(i)) \rangle\}
                                                                         by equation (5.1)
           \subseteq \bigcup_{i \in I} \{\langle i, f(U(i), V(i)) \rangle\}
                                                                      since I < I'
           = F'[U,V]
Hence F c F'
Proposition - GATE-IF-TRUE GATE-IF-TRUE
Proof
Let I = \{i | pr(C,i) \land pr(U,i) \text{ is true} \}

I' = \{i | pr(C,i) \land U(i) \neq i \text{ is true} \}.
pr(U,i) \Rightarrow U(i) \neq I \Rightarrow I \in I'
GATE-IF-TRUE[C,U]
   = \bigcup (C(i) = \underline{true} \rightarrow \{\langle tc(C,i), U(i) \rangle\}; \phi)
                                                                       by equation (5.2)
   \subseteq \bigcup_{i \in \mathcal{C}} (C(i) = \underline{true} \rightarrow \{\langle tc(C,i),U(i) \rangle\}; \phi)
                                                                      since I < I'
   = GATE-IF-TRUE [C,U]
Proposition - MERGE - MERGE'
Proof
Let I = \{i | pr(C,i) \land pr(U,tc(C,i)) \land pr(V,fc(C,i)) \text{ is true} \}
    I' = \{i \mid pr(C,i) \land ((C(i) = true \land U(tc(C,i)) \neq 1) \lor
                                   (C(i) = false \land V(fc(C,i)) \neq I)) is true}.
   i \in I \Rightarrow pr(C,i) \land U(tc(C,i)) \neq i \land V(fc(C,i)) \neq i
            by definition of I and proposition A.1
         \Rightarrow pr(C,i) \land ((C(i)=true \land U(tc(C,i))\neq1)\lor
                              (C(i) = false \land V(fc(C,i)) \neq \bot)
         ⇒i∈I^
hence I < I'
MERGE[C,U,V]
 = \left\{ (C(i) = true \rightarrow \{\langle i, U(tc(C,i)) \rangle \}; \right\}
    i \in I \ C(i) = false \rightarrow \{\langle i, V(fc(C,i)) \rangle\})
                                                                       by equation (5.3)
    \{(C(i) = true \rightarrow \{\langle i, U(tc(C,i)) \rangle\};
     i \in I'C(i) = \overline{\text{false}} + \{\langle i, V(fc(C,i)) \rangle\}
                                                                       since I < I'
 = MERGE'[C,U,V]
Proposition - Db c Db
Proof - Clear.
```

In the following propositions let C,U and V etc. be any histories.

Proposition - APPLY c APPLY'
Proof - Clear.

This section has shown that $OP \subseteq OP'$ for all operators in the dataflow language. In fact the vast majority of inputs to any operator OP results in the relation $OP \subseteq OP'$. Since for the same input, UI gives results more defined than QI. For example, $F[A] \subset F'[A]$ for $A = \{<1,a_1>,<3,a_3>\}$; UI is less constrained during execution and thus exhibits greater asynchrony.

The following theorem relates the least fixpoint f_p and f_p of any two interpreters that satisfy the hypothesis. In particular, QI and UI are such a pair of interpreters.

Theorem 1 - If τ and τ' are monotonic functionals and $\tau \leq \tau'$, then $f_p \leq f_p'$.

Proof - First we show by induction that

$$(\forall i) (\tau^{i}[\phi] \subseteq \tau^{i}[\phi])$$

hence $(\forall i) (\tau^{i} [\phi] \subseteq \tau^{i} [\phi])$.

Since $f_p = \text{lub}\{\tau^i[\phi]\}$, for any $j \in J^+$

Let $k = \min \min i$ such that $\tau^{i}[\phi](j) = x$

Hence

$$f_p(j)=x \Rightarrow f'_p(j)=x$$

$$f_p \subseteq f'_p$$

Theorem 1 implies that for some inputs to a dataflow program, UI may produce answers while QI may not. However, identical results are computed ($f_p = f_p$) if (1) the program terminates and (2) the program is well-formed*, although UI will still be less constrained than QI *From [6], meaning that dataflow operators may be combined only to form one-in one-out structured constructs such as if-then-else, etc.

during execution.

Suppose A and B represent two interpreters of a language. We say interpreter B is more asynchronous than interpreter A if for all operators OP and all XcH, OP_A(X) \subseteq OP_B(X). This is justified because in an actual machine X includes the partial histories computed before the fixpoint solution is reached, and since OP_A \subseteq OP_B interpreter B may produce more output (get ahead of) interpreter A during the computation, while the inverse is not possible.

Proposition - Let OP' represent the interpretation of a DDF operator according to UI, and let OP represent the interpretation according to any other interpreter that operates under the following rules:

- R(1). Neither guessing of outputs (either the value or the position in a history), nor any redundant calculation is done,
- R(2). No further interpretation is attached to the functions and predicates by the operator.

Then $OP(X) \subset OP'(X)$

<u>Proof</u> - In the proof of this proposition, we say that a history A is <u>complete</u> proper if pr(A, # A) is true. Thus, if A has n tokens, and they are in positions 1,2,...,n in the history (i.e. there are no holes in the history) then A is complete proper.

For function, predicates, the D operator, and apply, the result is clear since R(1) requires that we wait until we know the result must be produced. R(2) prevents us from "knowing" future inputs based upon past inputs, so we must wait until all current inputs are defined. Waiting until all current inputs are defined (i.e. \neq 1) is precisely the condition in the definitions of the above operators.

For the gate-if-true (or gate-if-false) operator, assume there is interpreter I such that GATE-IF-TRUE [C,U] \subseteq GATE-IF-TRUE [C,U] for some histories C and U. Let $<i,v>\in$ GATE-IF-TRUE [C,U] but $<i,v>\notin$ GATE-IF-TRUE [C,U]. Then certainly, by R(1) and R(2) as argued above, we must have a pair of inputs that produced the token <i,v>, where C(j)=true \land U(j)= \lor v. This is the minimal condition on GATE-IF-TRUE : Since the only further condition present on GATE-IF-TRUE : is pr(C,j), we assume : pr(C,j) holds but : is produced under interpreter I.

First, note for any complete proper inputs D and V that $GATE-IF-TRUE `[D,K] = GATE-IF-TRUE _[D,K]$. Thus, if we "complete" C to D by adding tokens to C to construct D, then $\langle i,v \rangle \in GATE-IF-TRUE _[C,U] \implies \langle i,v \rangle \in GATE-IF-TRUE `[D,U]$.

(There is no need to extend U to V since $U(j) \neq 1$ is the only condition on U in both interpreters.) Clearly, by definition of GATE-IF-TRUE, i=tc(C,j)=tc(D,j). But let k,true be one of the tokens used to complete C to D, where k,j, and thus tc(C,j)< tc(D,j). Thus k,v was an incorrect output by interpreter I, and since k was an arbitrary position in C, the condition k,v0 under UI is a necessary condition.

The proof of the proposition for merge is similar to the above.

Since each operator is most defined according to UI, the unravelling interpreter is indeed the most asynchronous interpreter of all interpreters that follow rules R(1) and R(2). According to Theorem 1, if f_p represents the least fixpoint according to some interpreter other than UI and the definitions are expressed as continuous functionals, then $f_p\subseteq f_p{}^{'}.$

8. MACHINES FOR QI AND UI DO COMPUTE THE LEAST FIXPOINT

We have defined the meaning of dataflow programs in terms of the least fixpoint solution to a system of monotone functionals representing various operators. Machines that have been proposed [1,7] for executing dataflow programs represent components of histories by tokens. The process of executing a program is carried out by generating further tokens at each step based on the tokens present and the available hardware resources. The machine terminates execution when no more tokens can be generated, that is, when it reaches a steady-state. This process of starting the program with empty lines and successively refining the history of every line step by step is very similar to the constructive method of finding the least fixpoint by the first recursion theorem of Kleene [12]. However, the process carried out by a machine may not be identical to the one dictated by Kleene's theorem due to lack of resources and scheduling decisions. Theorem 2 below gives some physically reasonable conditions which guarantee that when a machine reaches the steady-state, the history of all lines will be in accordance with the least fixpoint.

First, we give a definition. Given a functional τ and an input function A, a computation sequence of τ and A is any sequence of functions $\{x^{(k)}\}$ that has the following three properties:

1.
$$X^{(o)} = \phi$$

2. Either for some second

$$X^{(s)} = \tau[X^{(s)}, A]$$
 and
$$(\forall k < s) (X^{(k)} \subseteq X^{(k+1)} \subseteq \tau[X^{(k)}, A])$$

or

$$(\forall k \ge 0) (X^{(k)} \nsubseteq X^{(k+1)} \subseteq \tau[X^{(k)},A])$$

3.
$$\#(X^{(k+1)} - X^{(k)})$$
 is finite.

Intuitively, a computation sequence is the sequence of partial results generated by any reasonable machine: (1) a machine that begins with all lines empty, (2) never guesses results and does make progress, and (3) never does an infinite amount of computation in a single step. Such a machine halts only when no operators are enabled (i.e., all operators are in equilibrium). Now we prove a theorem that shows that if such a machine halts then indeed it halts at the least fixpoint of $\tau_{\rm A}$. Furthermore, we also prove that no matter what

path the machines takes (i.e., which of the enabled operators actually fires) they all lead to the same final result. In this sense dataflow systems have the extended Church-Rosser property.

Theorem 2. If $\{X^{(k)}\}\$ is a computation sequence of monotonic functional τ and monotonic function A, and if there exists some s>o such that $X^{(s)} = \tau[X^{(s)},A]$

Corollary - Given a monotone functional τ and a monotone function A, if $(\forall k \gg) (X^{(k)} \neq X^{(k+1)} \subseteq \tau[X^{(k)}, A])$ then

$$lub\{X^{(k)}\} \subseteq lub\{\tau^{k}[\phi,A]\}$$

Proof - Included in the proof of Theorem 2 part 2.

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