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Impact of CF₄ Plasma Treatment on GaN

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Abstract—We present a systematic study of the impact of CF₄ plasma treatment on GaN. It was found that CF₄ plasma etches GaN at a slow rate and yields a smooth etched surface. The effect of CF₄ plasma on electrical characteristics of GaN metal–semiconductor field-effect-transistor structures shows that the CF₄ plasma introduces acceptors into the near surface region of the GaN, which depletes mobile electrons. It was further demonstrated that leakage current of AlGaN/GaN (or GaN) Schottky diodes can be significantly suppressed by proper CF₄ plasma treatment. These unique properties of CF₄ plasma can be utilized for the advanced processing of GaN transistors.

Index Terms—CF₄ plasma, electron depletion, etch, GaN, leakage, transistor.

I. INTRODUCTION

FIELD-EFFECT transistors based on GaN and its alloys have shown great promise in high-frequency and high-power applications [1]. Processing technologies for GaN transistors have significantly advanced in the last decade, leading to devices with superior performance. Ohmic-contact resistance has been reduced to 0.12 Ω · mm [2]. Cl₂-based plasma etching has been extensively developed, enabling proper isolation and accurate gate recess [3]. SiN_x passivation coupled with field-plate structure boosts the power density up to 40 W/mm at 4 GHz [4]. A combination of gate recess, SiN_x passivation, and deep submicrometer gate technology pushes the device performance to 10 W/mm at 40 GHz [5].

CF₄ plasma has been involved in the processing of GaN transistors, mainly for dry-etching the SiN_x dielectric. It was recently discovered that CF₄ plasma treatment plays an important role in the electrical behavior of GaN transistors. The CF₄ plasma treatment has been demonstrated to shift the threshold voltage of AlGaN/GaN high electron mobility transistors (HEMTs) toward positive bias, thus enabling enhancement-mode operation [6]–[9]. Additionally, it was reported that exposure of AlGaN/GaN HEMTs to CF₄ plasma prior to gate metallization can significantly suppress gate leakage [10]. In this letter, we present a detailed investigation on the effect of CF₄ plasma treatment on GaN transistors. Our results show that the CF₄ plasma treatment has three major effects on GaN. First, the CF₄ plasma treatment etches GaN at a slow rate. Second, it

introduces acceptors into GaN, which deplete mobile electrons. Finally, the CF₄ plasma treatment significantly reduces the leakage current of Schottky gates, which can be attributed to the formation of a thin insulating layer on the GaN surface after the CF₄ plasma treatment. Experimental details are described in Section II. The effects of CF₄ plasma treatment are discussed in Section III. Finally, we summarize this letter in Section IV.

II. EXPERIMENTS

Metal–organic chemical-vapor-deposition grown GaN or AlGaN/GaN epilayers with an Al content of 22% are employed in this study. A reactive-ion-etch (RIE) system is used for the CF₄ plasma treatment. The CF₄ flow is 20 sccm, the chamber pressure is at 3 mT, and the bias is either 100 or 250 V. Atomic force microscope (AFM) was used to characterize the surface morphology and the etch depth. Scanning transmission electron microscope (STEM) and energy-dispersive X-ray (EDX) analyses were performed to study the structural characteristics of CF₄-treated samples. To study the effect of CF₄ plasma treatment, Schottky diodes and Hall patterns were fabricated on a wafer. Ohmic contacts were made by alloying Ti/Al/Ni/Au, mesa isolation was achieved by Cl₂ RIE etching, and Schottky contacts with the diameter of 180 μm were formed by Ni/Au. Hall measurements using Van der Pauw's method were performed to extract the electron density and mobility of the sample subjected to the CF₄ plasma treatment. With the Schottky diodes, capacitance–voltage (*C*–*V*) and current–voltage (*I*–*V*) characteristics were measured.

III. DISCUSSION

A. Etching Effect

Initial studies indicated that CF₄ plasma has a minimal etching effect on GaN [6]. This is consistent with the known formation of nonvolatile GaF₃ when the CF₄ plasma reacts with GaN. Our experiments show that with a 250-V plasma bias, CF₄ plasma etches approximately 130 nm of GaN in 90 min, corresponding to an etch rate of around 1.4 nm/min. An AFM scan of the etched region shows that the etched surface is very smooth, with a root-mean-square (rms) roughness of 0.6 nm. When there is no etching or when the plasma exposure time is short, step flow features can be observed. When the etch time is long, e.g., up to 90 min, features of step flow disappear, leaving a smooth etched surface. The disappearance of step flow features can be related to the buildup of a thin surface reaction layer as a consequence of the CF₄ plasma treatment. More information about this thin surface layer will be discussed in the last part of this section.

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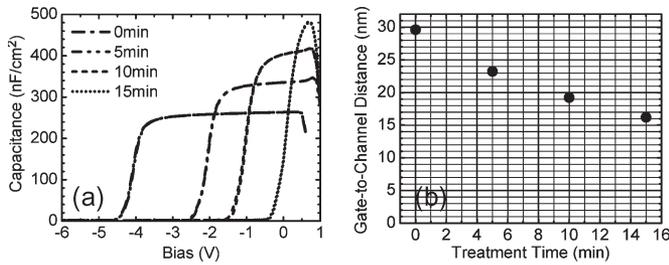


Fig. 1. (a) C - V characteristics of AlGaIn/GaN Schottky gate diodes with varied CF_4 plasma treatment times; the dc bias for the plasma treatment is 250 V. (b) Gate-to-channel distance of AlGaIn/GaN Schottky gate diodes extracted from C - V .

The smooth etching of GaN by CF_4 plasma provides an alternative means for the gate recess of AlGaIn/GaN HEMTs. Gate regions of AlGaIn/GaN HEMTs were treated by CF_4 at 250-V plasma bias and varied time prior to gate metallization. The C - V characteristics of AlGaIn/GaN Schottky gate diodes with different CF_4 plasma treatment times are shown in Fig. 1(a). From the C - V curves, we can observe that with the CF_4 plasma treatment, the gate-to-channel distance decreases, the electron density drops (as reflected by the integration of capacitance over bias voltage), and a positive threshold voltage shift takes place. With a 15-min treatment, the threshold voltage became positive, thus enabling enhancement-mode devices. The dependence of the gate-to-channel distance on treatment time was extracted from the C - V characteristics and is plotted in Fig. 1(b). We estimate that the CF_4 plasma etches AlGaIn at a rate of 1 nm/min. It is noteworthy that an increase of the transconductance can usually be observed for HEMTs subjected to the CF_4 plasma treatment. The similar transconductance increase can also be found in [7]. While Cai *et al.* [7] attributed the increase of the transconductance to the sample nonuniformity, we believe that it is simply due to the etching of the AlGaIn in our case.

B. Electron Depletion

The CF_4 plasma treatment can cause depletion of the 2-D electron gas at the AlGaIn/GaN interface. The actual cause leading to this electron depletion has been unclear. The electron density in the AlGaIn/GaN heterostructure can decrease due to several factors. First, as the CF_4 plasma etches AlGaIn, the electron density can decrease simply due to a thinner AlGaIn layer. Second, CF_4 plasma can modify the AlGaIn surface and increase the surface barrier so that more electrons are depleted. Third, the CF_4 plasma treatment can introduce acceptors into the AlGaIn/GaN structure, reducing the electron density.

To clarify the actual cause of electron depletion in the CF_4 plasma treated AlGaIn/GaN structures, we characterized the influence of CF_4 plasma treatment on mobile electrons in a simpler structure, which is the metal-semiconductor field-effect transistor (MESFET). Prior to gate metallization, the gate regions were treated with CF_4 plasma. The treatment time was varied from 5 to 25 min, and the bias was kept at 250 V. The electron density and mobility extracted from room temperature Hall measurements are shown in Fig. 2. The electron density monotonically decreased with an increasing treatment time; at

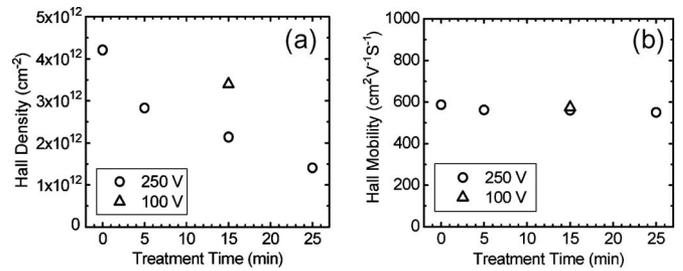


Fig. 2. Room temperature (a) Hall density and (b) mobility of GaN MESFET structures under different CF_4 plasma treatment times and bias conditions.

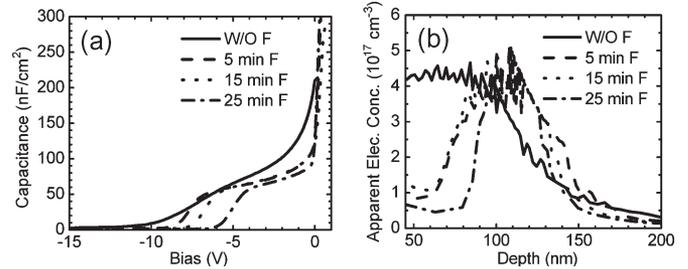


Fig. 3. (a) C - V characteristics of GaN Schottky gate diodes with varied CF_4 plasma treatment times; the dc bias for the plasma treatment is 250 V. (b) Apparent electron concentration profile in GaN Schottky gate diodes extracted from C - V .

lower plasma bias, there was a smaller decrease in electron density. The electron mobility was nearly unaffected by the CF_4 plasma, regardless of treatment time and plasma bias.

The C - V characteristics of this MESFET structure sample are shown in Fig. 3(a). From these C - V characteristics, the apparent electron concentration profile can be extracted using a method introduced in [11]. As shown in Fig. 3(b), electrons in the near surface region were depleted after the CF_4 plasma treatment. Longer treatment time leads to more electron depletion. Since the etch rate is too slow to account for the large loss of electron density, etching is not the dominant factor leading to the reduction of electron density. According to electrostatic analysis with Poisson's equation, the surface barrier height has to be greater than 4 eV to have a depletion width of 100 nm if there are no acceptors or acceptor-type defects introduced into the GaN. Since the band gap of GaN is 3.4 eV, a 4-eV barrier height is implausible. Therefore, acceptors introduced by CF_4 plasma should be responsible for the electron depletion. The nature of these CF_4 -induced acceptors requires more detailed study.

C. Leakage Reduction

Leakage current of Schottky gates on GaN and AlGaIn/GaN heterostructures can be significantly reduced by exposing the gate region to CF_4 plasma prior to Schottky gate metallization. In view of the great interest in gate leakage reduction for HEMTs, we chose to delineate the effect of a CF_4 plasma treatment on leakage current of AlGaIn/GaN Schottky diodes. The effect on GaN Schottky diodes is similar. The I - V characteristics of the Schottky diodes with different CF_4 plasma treatment times are shown in Fig. 4(a). The leakage current continuously decreases with an increasing treatment time. With

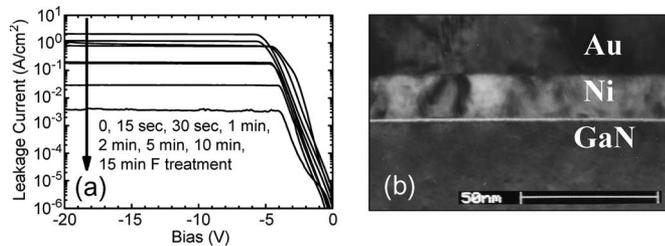


Fig. 4. (a) Reverse leakage characteristics of AlGaIn/GaN Schottky gate diodes with varied CF₄ plasma treatment times; the dc bias for the plasma treatment is 100 V. (b) Cross-sectional TEM image of a GaN-Ni/Au Schottky diode with CF₄ plasma treatment prior to Schottky metal evaporation.

a 15-min treatment, the reverse leakage current drops by nearly three orders of magnitude. A BCl₃/Cl₂ shallow etch following the CF₄ treatment increased the leakage current to a level that is similar to that without the CF₄ plasma treatment, suggesting that a surface effect rather than a bulk effect played a critical role in leakage current reduction.

The CF₄ plasma may react with GaN (or AlGaIn) to form nonvolatile F-containing compounds. This surface reaction may form an insulating surface layer that blocks leakage current. Fig. 4(b) shows the STEM image of the structure across the interface between Schottky metal and GaN, which was treated with CF₄ plasma at 250 V for 25 min. A very smooth, uniform, and thin interlayer could be observed between the Schottky metal and the GaN with an abrupt interface. An unambiguous signal corresponding to the element F was detected from the thin interfacial layer by an EDX measurement, suggesting that the thin layer contains F. This thin layer was not found in a control sample without the CF₄ plasma treatment. Buildup of this layer covered the GaN surface, so that the typically observed step flow features of as-grown GaN could not be observed for GaN after prolonged CF₄ plasma treatment. This is consistent with the AFM observation. The formation of this thin surface layer is a dynamic process, i.e., CF₄ plasma forms a thin layer on the surface, etches it away, and forms it again. More detailed study is required to completely understand the correlation between the thin interfacial layer and the leakage reduction.

IV. CONCLUSION

In conclusion, we studied the impact of CF₄ plasma treatment on GaN. Slow and smooth etching effect was observed.

The introduction of acceptors into GaN (or AlGaIn/GaN) was deduced to be the major cause of electron depletion by the CF₄ plasma treatment. Schottky gate leakage can be effectively reduced by the CF₄ plasma treatment. The leakage reduction effect is related to the formation of an F-containing surface layer. These effects, which are associated with the CF₄ plasma treatment, can be deployed for advanced processing of GaN-based transistors, such as fabricating enhancement-mode HEMTs and HEMTs with low gate leakage.

REFERENCES

- [1] U. K. Mishra, P. Parikh, and Y. F. Wu, "AlGaIn/GaN HEMTs—An overview of device operation and applications," *Proc. IEEE*, vol. 90, no. 6, pp. 1022–1031, Jun. 2002.
- [2] F. M. Mohammed, L. Wang, and I. Adesida, "Ultralow resistance Si-containing Ti/Al/Mo/Au ohmic contacts with large processing window for AlGaIn/GaN heterostructures," *Appl. Phys. Lett.*, vol. 88, no. 21, p. 212107, May 2006.
- [3] D. Buttari, A. Chini, G. Meneghesso, E. Zanoni, P. Chavarkar, R. Coffie, N. Q. Zhang, S. Heikman, H. Xing, C. Zheng, and U. K. Mishra, "Systematic characterization of Cl₂ reactive ion etching for gate recessing in AlGaIn/GaN HEMTs," *IEEE Electron Device Lett.*, vol. 23, no. 3, pp. 118–120, Mar. 2002.
- [4] Y. F. Wu, M. Moore, A. Saxler, T. Wisleder, and P. Parikh, "40-W/mm double field-plated GaN HEMTs," in *Proc. Dig. Device Res. Conf.*, Jun. 2006, pp. 151–152.
- [5] T. Palacios, A. Chakraborty, S. Rajan, C. Poblenz, S. Keller, S. P. DenBaars, J. S. Speck, and U. K. Mishra, "High-power AlGaIn/GaN HEMTs for Ka-band applications," *IEEE Electron Device Lett.*, vol. 26, no. 11, pp. 781–783, Nov. 2005.
- [6] Y. Cai, Y. Zhou, K. J. Chen, and K. M. Lau, "High-performance enhancement-mode AlGaIn/GaN HEMTs using fluoride-based plasma treatment," *IEEE Electron Device Lett.*, vol. 26, no. 7, pp. 435–437, Jul. 2005.
- [7] Y. Cai, Y. Zhou, K. M. Lau, and K. J. Chen, "Control of threshold voltage of AlGaIn/GaN HEMTs by fluoride-based plasma treatment: From depletion mode to enhancement mode," *IEEE Trans. Electron Devices*, vol. 53, no. 9, pp. 2207–2215, Sep. 2006.
- [8] Y. Cai, Z. Cheng, W. C. K. Tang, K. M. Lau, and K. J. Chen, "Monolithically integrated enhancement/depletion-mode AlGaIn/GaN HEMT inverters and ring oscillators using CF₄ plasma treatment," *IEEE Trans. Electron Devices*, vol. 53, no. 9, pp. 2223–2230, Sep. 2006.
- [9] T. Palacios, C. S. Suh, A. Chakraborty, S. Keller, S. P. DenBaars, and U. K. Mishra, "High-performance E-mode AlGaIn/GaN HEMTs," *IEEE Electron Device Lett.*, vol. 27, no. 6, pp. 428–430, Jun. 2006.
- [10] L. Shen, T. Palacios, C. Poblenz, A. Corion, A. Chakraborty, N. Fichtenbaum, S. Keller, S. P. DenBaars, J. S. Speck, and U. K. Mishra, "Unpassivated high power deeply recessed GaN HEMTs with fluorine-plasma surface treatment," *IEEE Electron Device Lett.*, vol. 27, no. 4, pp. 214–216, Apr. 2006.
- [11] J. Hilibrand and R. D. Gold, "Determination of the impurity distribution in junction diodes from capacitance–voltage measurements," *RCA Rev.*, vol. 21, pp. 245–252, 1960.