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# Investigation of Arrays of Two-Dimensional High- $T_C$ SQUIDs for Optimization of Electrical Properties

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**Abstract**—In this work, we investigate two-dimensional arrays of high- $T_C$  superconducting quantum interference devices (SQUIDs) for optimization of their electrical transport characteristics. Specifically, we look at devices with different electrode configurations in between the series segments to gain insight into how the array spacing, in the direction of the bias current, affects the voltage magnetic field characteristics. Our results suggest that for spacing dimensions greater than the penetration depth interactions are minimal. Furthermore, comparisons of voltage field characteristics reveal higher modulation voltages and narrower peaks with as the numbers of SQUIDs in the parallel direction increases from 1 to 6. For larger numbers of SQUIDs in parallel greater than six little change is observed. These results suggest a pathway to SQUID array scaling for very large numbers of SQUIDs within in a small area.

**Index Terms**—SQUID, inductance, large scale.

## I. INTRODUCTION

ARRAYS of superconducting quantum interference devices (SQUIDs) may be able to provide improvements to the dynamic range, bandwidth and linearity of SQUID-based magnetometers [1]–[5]. This is particularly important for small signal detection applications, such as broadband RF antenna, operating unshielded or in the presence of large background interference [6]–[9]. These applications require a very large number of SQUIDs or equivalently Josephson junctions with uniform reproducible parameters. Ion damage high- $T_C$  Josephson junctions [10] are ideal for this purpose because very large numbers of freely positional junctions are possible with 10%

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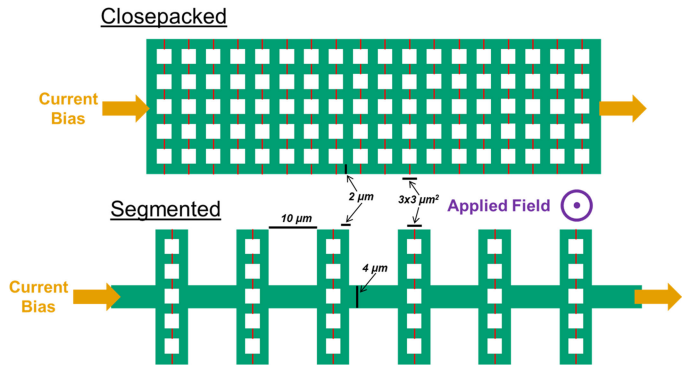


Fig. 1. Compact and extended array geometries for the devices used in this study. The number of SQUIDs connected in parallel for each segment was varied between 1 and 9 (5 is illustrated here). Both geometries contained 1000 series segments.

uniformity [11] and excellent temporal stability [12]. Large arrays of ion damage junctions previously demonstrated, exhibited much smaller voltage modulations than those predicted from the sum of the junction  $I_C R$  products [10], [11]. A computational modeling study suggested that this could be related to mutual interactions between adjacent SQUID loops [13]. In this manuscript, we take a closer look at these interactions with an experimental comparison of spatially extended and close-packed SQUID arrays for insight into how geometry affects the current distribution and voltage response to determine the potential scalability of these devices.

## II. EXPERIMENT

Two types of SQUID array layouts developed for this experiment are shown in Fig. 1. In the close packed geometry, the devices are placed as close as possible within the bounds of the process critical dimension of  $2 \mu\text{m}$ . In between each segment in the series direction the interconnecting electrode spans the entire width of the array. Whereas, in the extended geometry, an additional electrode several microns long is placed in the center in-between adjacent series segments. The choice of this extension was intended to reduce the mutual inductance between each segment. Arrays in both configurations were fabricated with the number of SQUIDs connected in parallel ranging from 1 to 9. All devices have identical segments connected in series.

Arrays were fabricated using the following process. A 150-nm thick YBCO thin film was deposited on a cerium oxide buffered 4-inch r-plane sapphire wafer by reactive coevaporation [14].

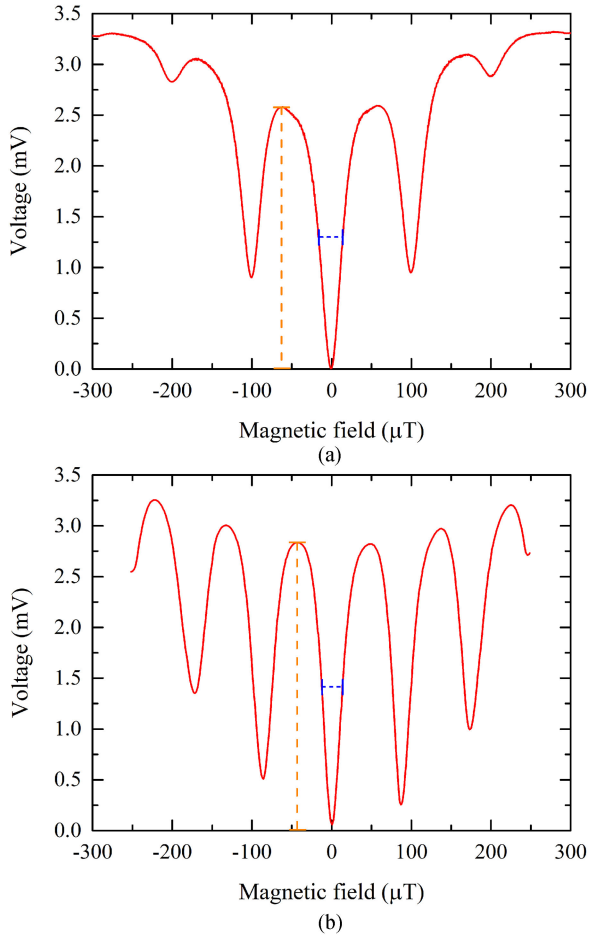


Fig. 2. Voltage-field characteristics of a (a) close packed (measured at 49 K) and (b) extended (measured at 45 K) array with eight SQUIDs in parallel and 1000 segments in series. Data were shifted by 5.2 mV (a) and 31.4 mV (b) for clarity. The slight difference in field period and shape of the voltage modulation are due to the different amounts of flux focusing from the electrodes.

A subsequent 200-nm thick layer of gold was thermally evaporated *in-situ*. Electrodes were patterned into both layers using photolithography and argon ion milling. A subsequent lithography and KI-I-etch was used to pattern the gold contact layer. An ion implantation mask consisting of 800 nm-thick photoresist, 25 nm-thick germanium, and 100 nm of polymethylmethacrylate (PMMA) was constructed on top of the electrodes. Electron beam lithography with a Vistec VB6 100 keV nanowriter was used to expose 25-nm wide lines in the PMMA in the regions intended for Josephson junctions. After development the pattern was transferred into the underlying germanium layer using an HBr reactive ion etch. Subsequently, a second oxygen reactive ion etch was performed to transfer the pattern into the resist layer thereby forming a high aspect ratio implantation mask. The process is explained in a previous publication [15]. Last, the wafer was irradiated with 200 keV neon ions with a dose of  $2 \times 10^{13}$  ions/cm<sup>2</sup> to disorder the underlying YBCO film causing a reduction in  $T_C$  to form weak link Josephson junctions.

The implanted devices were diced and measured in a pumped liquid nitrogen cryostat for measurements in the temperature

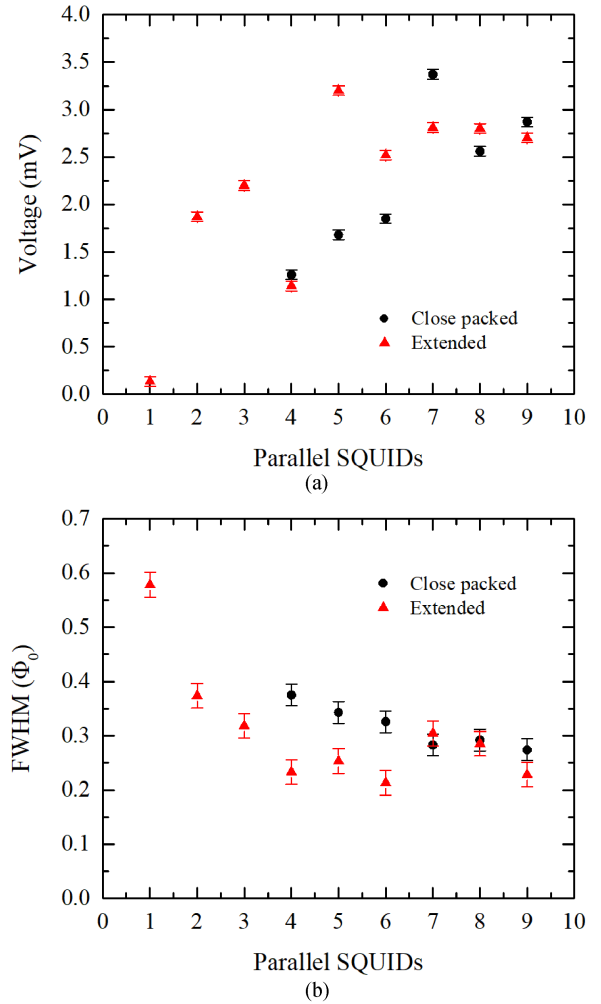


Fig. 3. (a) Maximum voltage modulation taken at different temperatures for different number of SQUIDs in parallel. (b) SQUID oscillation peak width as a function of different SQUIDs in parallel, the red indicates close packed array while the blue dots are extended arrays.

range 45 K to 77 K and a helium cryostat for lower temperatures. A layer of  $\mu$ -metal shielding and Bi-2223 superconductor shield were added to exclude external magnetic field. For testing, a DC bias current was driven through the array and voltage as a function of magnetic field  $V$ - $B$  was measured over a wide range of temperature. The temperature and current bias that produced the largest voltage response was determined to optimize the characteristics for comparison to one another. We found a temperature spread in these optimum working points to be approximately 10 K which we attribute to process variations. Defects in the patterning prevented characterization of the close packed arrays with 1, 2, and 3 SQUIDs in parallel. Representative  $V$ - $B$  for both geometries are shown in Fig. 2. Fig. 2(a) shows  $V$ - $B$  of an  $8 \times 1000$  close packed array optimally biased at  $84 \mu\text{A}$  with voltage modulation of 2.56 mV and a maximum slope of  $90 \mu\text{V}/\mu\text{T}$ . The SQUID oscillation period is  $88 \mu\text{T}$  and full width at half maximum (FWHM) is  $30.9 \mu\text{T}$ .

Similarly, Fig. 2(b) shows  $V$ - $B$  of an extended array optimally biased at  $193 \mu\text{A}$  with voltage modulation of 2.8 mV and the slope of  $118 \mu\text{V}/\mu\text{T}$ . The SQUID oscillation period is  $100 \mu\text{T}$

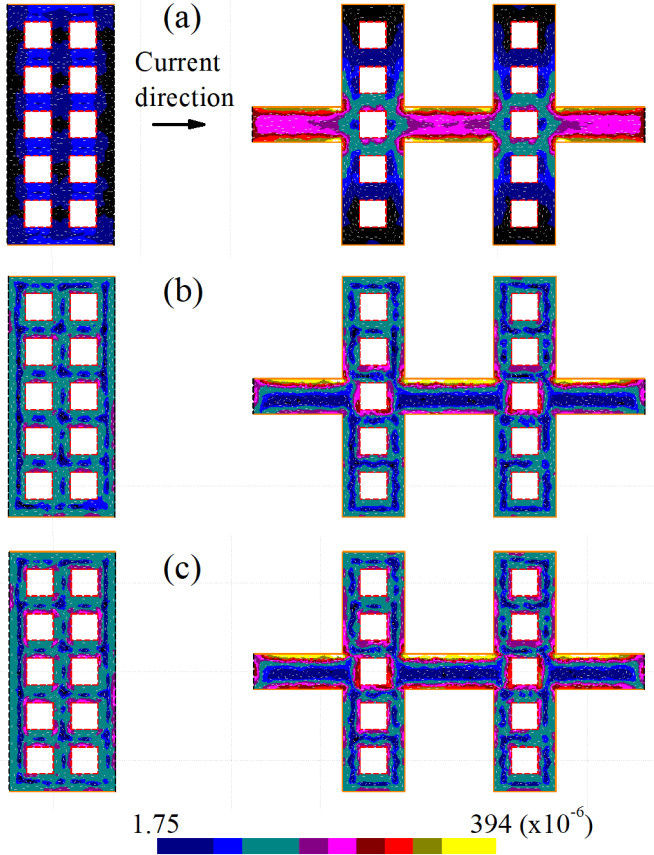


Fig. 4. 3D-MLSI simulation of current distribution of a close packed array and an extended array with (a) five SQUIDs in parallel and no external field, (b) magnetic field parameter of 0.5, and (c) 1 and current uniformly enters from the whole edge on the left. The color scale indicates a range from  $1.75 \times 10^{-6}$  to  $394 \times 10^{-6}$ .

with FWHM of  $27.1 \mu\text{T}$ . The peak of the modulation voltage ( $V_P$ ) was determined for the central peak of each array along with its FWHM as shown in Fig. 2.

Fig. 3(a) shows  $V_P$  as a function of the number of SQUIDs connected in parallel for both the close packed and extended geometries. We observe an increase in  $V_P$  with increasing parallel SQUID number up to approximately 6 SQUIDs, with little change for higher numbers. Furthermore, there is no measurable difference for the two device geometries.

Similarly, FWHM (normalized to the flux quantum) is plotted in Fig. 3(b). Narrowing of the central peak is quite evident as the numbers of SQUIDs increase from 1 to 6 but as in the previous case little change is noted at higher numbers. Furthermore the different electrode geometries show very similar results.

### III. DISCUSSION

To gain insight into our results we simulated the array segments using 3D-MLSI inductance simulation software for multi-layered circuits [16], [17]. The approach is based on the London equations written for the so-called stream function instead of electric current. A finite element method has been used for fast and precise calculations. For our particular simulations, we initialize the simulation with penetration depth  $\lambda = 200 \text{ nm}$ , a bias current of  $1 \mu\text{A}$ , and a triangular mesh consisting of 3654

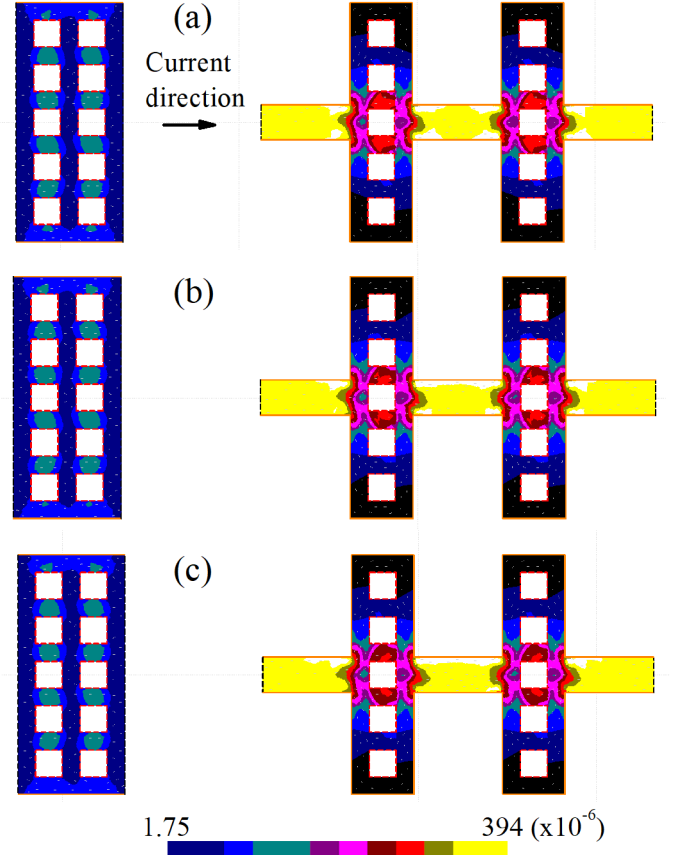


Fig. 5. 3D-MLSI simulation of current distribution of a close packed array and an extended array using the same condition as Fig. 4 with hole dimension reduced to  $100 \text{ nm}$  such that is much less than  $\lambda$ . The color scale indicates a range from  $1.75 \times 10^{-6}$  to  $394 \times 10^{-6}$ .

pieces. We remark that this simulation does not take into account the presence of the resistances from the Josephson junctions. However, we believe the results are valid for studying the distributions of the supercurrents below the critical current of the array. Fig. 4(a)–(c) shows simulations for both geometries with applied flux  $\Phi = 0, 1/4$  and  $1/2 \Phi_0$  respectively. The contours depict the pathways of the circulating currents and the color scale indicates a range from  $1.75 \times 10^{-6}$  to  $394 \times 10^{-6}$ .

In the case of zero applied flux (Fig. 4(a)) current is concentrated in the center of the segment but as applied flux increases it redistributes across the SQUIDs evenly. With the exception of the centrally located SQUID, the circulating currents for both geometries are quite similar. We attribute this to be due to the fact that the electrodes are much larger than the superconducting penetration depth. To test this reasoning, we simulated a hypothetical device with critical features of  $100 \text{ nm}$  for both holes and electrodes which is smaller than the penetration depth of  $200 \text{ nm}$ . Fig. 5(a)–(c) shows simulations for both scaled down geometries with applied flux  $\Phi = 0, 1/4$  and  $1/2 \Phi_0$  respectively. In contrast to the larger devices the nanoscale devices are strikingly different. All of the close packed arrays show a uniform current distribution whereas the extended geometry shows a very high current density at the electrode that is independent of applied

flux. At these smaller dimensions array geometry is very important and needs to be carefully considered for optimization of performance variables.

#### IV. CONCLUSION

In conclusion, we experimentally investigated two types of SQUID array geometry for optimization of closely packed geometries. No measurable difference was observed in the voltage response of our experimental arrays. While the critical dimensions of our devices were only  $2\ \mu\text{m}$ , simulations suggest that they are much larger than the penetration of the shielding currents between the segments, as the current density only changes rapidly near the edges. This result suggests that very aggressive scaling can be utilized to place larger numbers of SQUIDS into a smaller compact sensor with little degradation in performance. Assuming a spacing of  $2\lambda$  (500 nm) between junctions,  $2\ \mu\text{m}$ -wide junction and  $2\lambda$  pitch in a meander pattern, the junction density of this technology can be up to  $10^8\ \text{cm}^{-2}$ .

Furthermore, simulations of arrays with critical dimensions (CD) of 100 nm ( $\text{CD} \ll \lambda_L$ ), reveal current crowding can occur at the electrodes, whereas in the close packed geometry the current remains evenly distributed. How these differences in current translate into the properties of  $V$ - $B$  characteristics is an open question and merits further study at much smaller scales that is beyond the process technology used in this work. The use of helium ion direct-write SQUIDS [18], [19] could be utilized to realize these experiments for higher density arrays and new functionality.

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