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A Study of Signal Generation and Charge Collection in a-Si:H Diodes for Radiation Imaging

I. Fujieda
(Ph.D. Thesis)

October 1990



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**A Study of Signal Generation and Charge Collection
in a-Si:H Diodes for Radiation Imaging**

**Ichiro Fujieda
Ph.D. Thesis**

**Physics Division
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October 1990

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A Study of Signal Generation and Charge Collection in a-Si:H Diodes for Radiation Imaging

Ichiro Fujieda

Abstract

Hydrogenated amorphous silicon (a-Si:H) has been investigated as a material for radiation detectors. Its high radiation resistivity and large-area capability are the expected advantages of this material together with its ability to provide a front-end readout electronics in the vicinity of the sensor element. Electrons and holes created by incoming charged particles, X-rays, γ rays, are drifted by the electric field inside a-Si:H diodes and this carrier movement induces signal charges on electrodes. Charge collection and signal generation process are analyzed in terms of carrier mobilities, lifetimes and electric field. Charge collection in thick a-Si:H diodes is often limited by deep-level trapping of carriers during transit and a finite charge integration time required for single particle counting in some applications and sometimes by volume recombination of carriers for detecting heavily-ionizing particles such as α particles. The charge collection process is also strongly affected by the non-uniform electric field profiles in a-Si:H diodes caused by the fixed space charges inside the material under reverse-bias. Signal generation due to a weak light pulse irradiating each end of a thick diode is measured as a function of a reverse-bias and it gives a valuable information about the fixed space charges. Field profiles can be manipulated by either doping, electrode geometry, or combination of both to improve the charge collection process. One can apply a higher reverse-bias on a diode with an

equivalent thickness by providing buffer layers at each end of the diode and thus suppressing soft breakdown phenomena. X-ray detection with a good sensitivity is demonstrated by an a-Si:H photodiode coupled to an evaporated CsI scintillator. The scintillation quality of evaporated CsI layers can be made almost identical to its single crystal counterpart. Fields of a-Si:H radiation detector application include high energy physics, medical imaging, materials science and life science.

Thesis committee: Selig N. Kaplan (chair) _____
 Victor Perez-Mendez
 Chenming Hu

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in a-Si:H Diodes for Radiation Imaging**

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Chapter 1 Introduction

Hydrogenated amorphous silicon (a-Si:H) is a relatively new semiconductor material. Since as first demonstrated by Spear and LeComber in 1976 that it could be doped to form p or n-type material, it has been widely used in industry as the material for solar cells, facsimile heads and liquid crystal display drivers. In this chapter, some production methods and basic properties of a-Si:H are reviewed first. An extensive review of a-Si:H preparation, structure, optical and electronic properties and device applications (except for radiation detection) can be found in ref.[1]. The historical background of its use as a radiation detector material and some detection principles of these detectors are also described in this chapter.

1-1. Production methods and basic properties of a-Si:H

1-1-1. Production methods

A-Si:H layers are deposited on heated substrates (≤ 250 °C) most commonly by the Glow-Discharge decomposition of SiH_4 gas by 13.54 MHz RF power. This method is called Plasma Enhanced Chemical Vapor Deposition (PECVD). Conditions such as substrate temperature, RF power, flow rate and pressure of SiH_4 gas all play important roles in depositing a-Si:H layers and these are determined largely by lengthy trial-error experiments.

Doping a-Si:H layers can be achieved by adding dopant gases such as PH_3 and B_2H_6 to SiH_4 . Multi-chamber systems such as shown in Fig.1-1 are routinely used to avoid cross-contamination of gases at p-i-n a-Si:H solar cell production facilities.

Radicals (SiH_n , $0 \leq n \leq 3$) created by the RF discharge either stick to or diffuse on the growing layer and eventually form a Si-Si network by chemical reactions assisted by the heat provided from the substrate. If the sticking process dominates over the radical propagation on the substrate surface, the deposition is called Physical Vapor Deposition (PVD) and if the radical propagation dominates, it is called Chemical Vapor Deposition

(CVD). PVD occurs at high deposition rates with high RF power and the resultant Si network is three dimensional island-like, that is morphologically inhomogeneous. Electronic properties of these layers are poor and cannot be used for many device applications. If the RF power is kept low and all other parameters are optimized, closely-packed morphologically uniform amorphous Si network results by CVD and these layers are often called "device-quality" [1].

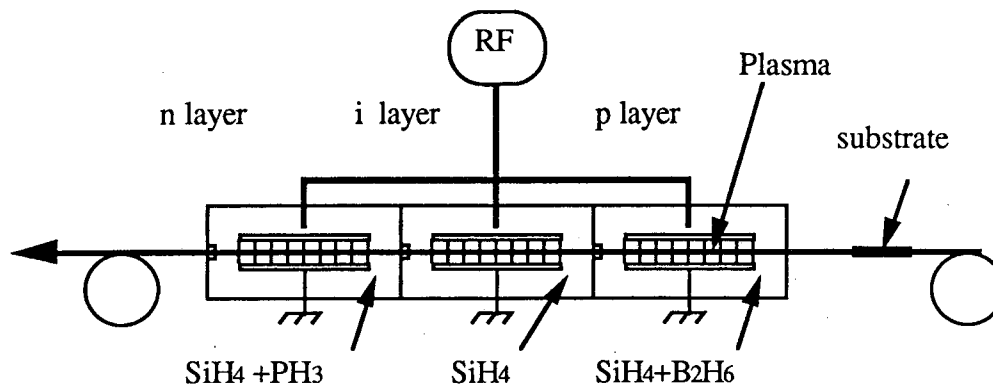


Fig.1-1 Multi-chamber system for p-i-n a-Si:H deposition

The deposition rate of the standard Glow Discharge (GD) method of producing "device quality a-Si:H" is normally about 1 $\mu\text{m/hr}$ and is the limiting factor for throughput of solar cell production. Efforts have been focused on increasing the deposition rate without degrading the properties of a-Si:H. Variations of the GD method include the use of higher RF frequency (VHF-CVD method) [2,3], addition of hydrogen and fluorine radicals (HR-CVD) [4], and the use of magnetic field (Controlled Plasma Magnetron method (CPM)) [5]. Higher deposition rates are also obtained by the use of disilane Si_2H_6 instead of silane [6] or by the plasma CVD induced by Electron Cyclotron Resonance (ECR) [7]. Instead of RF plasma, photo-excited Hg atoms can also decompose silane gas to produce a-Si:H layer (photo-CVD) [8]. Some reported characteristics of a-Si:H layers deposited by these various techniques are summarized in the Table 1-1. The listed defect densities are measured by different characterization techniques. Since different characterization

techniques give different aspects of the "defect density", the table below should convey only a rough picture. VHF and CPM are promising for fast deposition of good quality a-Si:H layers. As for the thickness comparison, many techniques are meant for solar cell and photo-sensor production, hence the thickness limitation has not been pushed beyond 1 μ m. It has been already demonstrated that thick layers can be deposited successfully by GD and VHF.

Table 1-1. a-Si:H preparation techniques

technique	defect density	deposition rate	thickness	ref.
GD (silane)	$2 \times 10^{15} \text{ cm}^{-3}$	1 $\mu\text{m/hr}$	$\leq 50 \mu\text{m}$	1
VHF-CVD	$2-6 \times 10^{15} \text{ cm}^{-3}$	2-6 $\mu\text{m/hr}$	200 μm	2
CPM-CVD	$7 \times 10^{14} \text{ cm}^{-3}$	2-3 $\mu\text{m/hr}$	1 μm	5
ECR-CVD	$2-3 \times 10^{16} \text{ cm}^{-3}$	8 $\mu\text{m/hr}$?	7
HR-CVD	as good as GD	1-2 $\mu\text{m/hr}$	$\leq 10 \mu\text{m}$	4
Photo-CVD	as good as GD	$\leq 1 \mu\text{m/hr}$	1 μm	8
GD (disilane)	higher than GD	2-3 $\mu\text{m/hr}$	1 μm	6

1-1-2. Disordered structure

A schematic view of the a-Si:H network is shown in Fig.1-2. Si atoms have four bonds to form a Si-Si network but not all of them are necessarily used. The unused bonds are called "dangling bonds" and represent an undesirable feature as will be discussed later. The number of dangling bonds are greatly reduced by inclusion of H atoms which "terminate" the unpaired electrons. The interatomic distance is not constant as is the case of crystalline Si. The lack of long-range order while retaining a short-range order in the Si-Si network results in a peculiar density of states which affects carrier transport properties. These properties will be discussed further below.

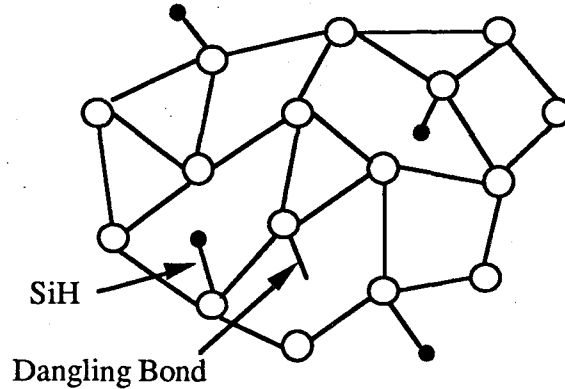


Fig.1-2 Schematic view of a-Si:H network

1-1-3. Density of states and carrier transport

Many characterization techniques have led us to the understanding of the density of states of "intrinsic" a-Si:H as shown schematically in Fig.1-3. There are two major differences from the intrinsic crystalline silicon case, which affect carrier transport properties.

First, the dangling bonds create mid-gap states. The "intrinsic" a-Si:H material is slightly n-type in a sense that the material leaves a small but significant amount of fixed positive charges when these dangling bonds release the unpaired electrons when an electric field is applied. The way that the dangling bonds affect carrier transport properties is two-fold. First, the fixed positive space charge causes the electric field to drop down with distance, producing a non-uniform field profile through the material. Secondly, these mid-gap states act as recombination centers since the thermal release of trapped carriers is less likely than the subsequent trapping of the opposite type carriers.

Second, the edges of the conduction and valence bands become "fuzzy" due to the non-constant interatomic distance. The resultant "tail states" affects the carrier transport properties. It is now largely accepted that electron transport is described by the "multi-trapping model" and hole transport by "hopping" at room temperature [1]. Electrons in the conduction band are easily trapped by the tail states and thermally released in a short time to

continue their transport. The width of the valence band tail states is larger than that of the conduction band tail states. Hole transport occurs largely in this tail as a succession of tunnelings through the potential barriers between two adjacent trapping centers, "hopping."

As a result of these mid-gap states and the tail states, the carrier transport properties, such as mobility and lifetime of a-Si:H are inferior to those of crystalline silicon.

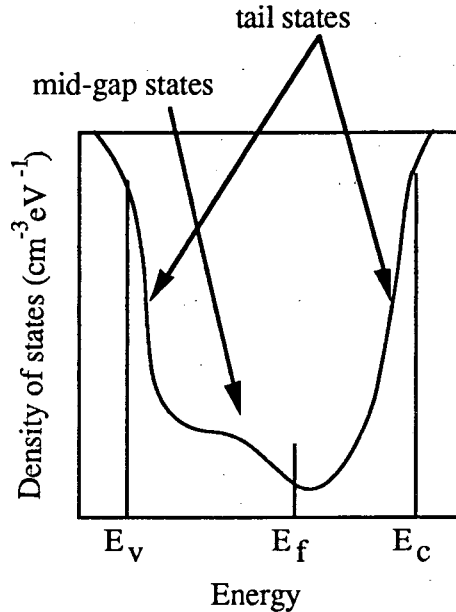


Fig.1-3 Density of states of intrinsic a-Si:H

Important material properties of intrinsic a-Si:H are summarized and compared to those of crystalline Si in the table below.

Table.1-2 Properties of a-Si:H and crystalline Si

properties	a-Si:H	X'tal Si
Density	2.25 - 2.3 g/cc	2.3 g/cc
Dielectric const.	11.8	12.0
Resistivity	$10^{+9} - 10^{+10} \Omega\text{cm}$	$< 2 \times 10^{+5} \Omega\text{cm}$
Band gap	1.7 - 1.9 eV	1.1 eV
Mobility; electron	$1 \text{ cm}^2/\text{Vsec}$	$1350 \text{ cm}^2/\text{Vsec}$
hole	$0.005 \text{ cm}^2/\text{Vsec}$	$480 \text{ cm}^2/\text{Vsec}$

1-1-4. Doping

When a doped a-Si:H material is deposited, the incorporated P or B atoms create an impurity band close to the conduction band or the valence band, respectively, together with the "doping-induced dangling bonds" in the mid-gap according by the following reactions (Street's model) [9].



The + or - sign represents the charge of the doping-induced dangling bonds and the subscript is the number of surrounding Si atoms, known as the coordination number. These charged dangling bonds are partially neutralized by the opposite type carriers so that the resultant material leaves net fixed space charge of opposite sign represented by P_4^+ or B_4^- when the material is under bias.

1-2. a-Si:H radiation detectors

1-2-1. Historical background

a-Si:H has been investigated as a possible alternative for radiation detector material for high energy physics experiments, medical imaging, material and life science studies. Early efforts to make radiation detectors with a-Si:H were limited to the thickness range of 1-2 μm . Wei et al. [10] made a 16 element X-ray sensor with each element size of 3 mm x 16 mm. X-ray fluences were successfully detected by this device with a good linearity between the output signal size and the input X-ray intensity. When coupled to a phosphor such as CdWO_4 and $\text{ZnS}(\text{Ni})$, a larger output was obtained due to the increased interaction efficiency of the individual X-ray photons. Mochiki et al. [11] used a facsimile head to detect X-ray fluences and obtained similar results with an improved spatial resolution. Naruse et al. [12] have proposed the concept of the metal/a-Si:H multilayer structure, which looks like a miniature calorimeter as used in high energy physics experiments.

Recently, Ito et al. fabricated a monolithic type X-ray sensor by depositing a-Si:H layers directly on a ceramic scintillator [13]. Street et al. obtained X-ray images with a 2-D a-Si:H photodiode array coupled to the intensifying screen [14]. All of these groups used 1-2 μm thick a-Si:H layers to detect X-ray fluences. Other groups focused on making thicker a-Si:H layers up to 50 μm for single particle counting so that the incoming radiation deposits enough energy in the material to produce a large signal well above the noise [15,16,17].

1-2-2. Detection principles

Figure 1-4 illustrates some possible detection schemes for a-Si:H radiation detectors. Depending on the kinds of radiations and applications, one of the following detection schemes has advantages over the others.

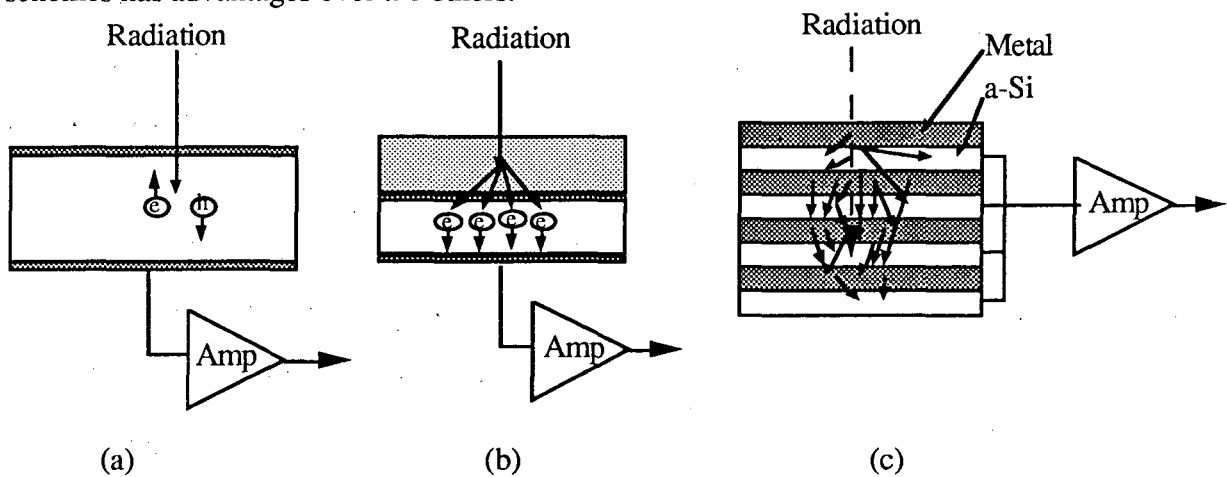


Fig.1-4. Detection schemes of a-Si:H radiation detectors. (a) Direct detection scheme. (b) Indirect detection scheme. (c) Calorimeter scheme.

In the direct detection scheme as shown in Fig.1-4(a), charged particles cause ionization events in the thick depleted a-Si:H layer. The average energy required to produce one electron-hole pair, known as the W value, is about 5 eV [18], almost three times the gap energy of a-Si:H (about 1.8 eV). This ratio of the W value to the gap energy seems to be a characteristic of semiconductors [19]. These radiation-induced electron-hole pairs drift in

the electric field, inducing current in the external circuit which is amplified by the external electronics. Readout electronics may be designed to count either individual particles or fluences, depending on the applications of the detectors. The signal generation process in a-Si:H diodes is discussed in Chapter 2. A problem in operating thick a-Si:H diodes and some possible solutions are described in Chapter 3.

In the indirect detection scheme shown in Fig.1-4(b), a phosphor (scintillator) converts the incident radiation (charged particles, γ rays or X-ray fluences) into visible light ($\lambda=350-600$ nm) which is then absorbed mostly at the surface of the thin ($1-2\mu\text{m}$) a-Si:H layer. Electrons and holes are created near the surface and are drifted by the field. This scheme is discussed in detail in Chapter 4.

In the calorimeter scheme as shown in Fig.1-4(c), a succession of alternating metal and a-Si:H layers are stacked. Incident high-energy photons undergo photoelectric, Compton, and pair-production interactions preferentially in the metal layers due to their higher atomic number. The resultant electrons which emerge from the metal converter layers produce electron-hole pairs in the a-Si:H layers.

Since a-Si:H is prepared by decomposition of SiH_4 gas in glow discharge on low temperature substrates, large area detectors can be easily fabricated at low cost. The disordered Si-Si network makes the material less sensitive to radiation-induced damages. Much of such damage can be removed by subsequent annealing at low temperature [16]. Although its material parameters, such as carrier mobility, life time and defect density are less attractive than those of crystalline silicon, there are various applications in which the advantages of a-Si:H described above outweigh its less attractive material parameters. Some of these expected application fields are described in Chapter 5.

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Chapter 2. Signal generation in a-Si:H radiation detectors

2-1. Introduction

There was a concern about using a thick a-Si:H layer as a detector material because the low carrier mobilities and lifetimes might prevent collection of the radiation-induced electrons and holes in a limited measurement time. However, a higher bias can be applied on a-Si:H layers than on crystalline silicon since the material is highly resistive. To what extent this high bias can compensate the inferior transport properties of a-Si:H is the problem addressed here. Problems associated with high bias operation are discussed in Chapter 3. In this chapter, charge collection and signal generation in a-Si:H are analyzed to show that the material is capable of detecting radiation and to provide tools for detector design optimization.

In many device applications to reduce leakage current, the intrinsic a-Si:H layer is sandwiched between very thin (30-50nm) p and n layers, making p-i-n diodes. (Other structures such as Schottky barrier and heterojunction diodes (using a-SiC:H, Si₃N₄ as barriers) have been studied and a comparison of leakage currents for these structures can be found in ref.[1].) The p-i-n structure is schematically shown in Fig.2-1(a). Also shown in the figure are a charge-sensitive preamplifier and a shaping amplifier, standard electronics used with semiconductor radiation detectors. When such a p-i-n diode is reverse-biased, the movable carriers in the conduction and valence bands are swept away, leaving the positively charged dangling bond sites. Hence, the field profile is non-uniform i.e. linearly decreasing with distance in the first approximation for a uniform charge distribution [2]. Since the "intrinsic" a-Si:H is slightly n-type, depletion starts from the p-i interface. As the applied bias is increased, the depletion width w_d increases. $w_d = \sqrt{(2V_d \epsilon_s / \rho)}$, where V_d , ϵ_s , ρ is the applied bias, dielectric constant and charge density, respectively. At some critical bias, the field extends to the n-i interface and full-depletion

is achieved as shown schematically in Fig.2-1(b). This behavior of depletion can be observed by the following manner [3].

When a small amount of charge is injected at one end of the diode by shining a strongly-absorbing light pulse on its face, the initial carrier distribution looks like that shown in Fig.2-1(c). One type of carrier is immediately collected by the nearby electrode and the other type of carrier traverses the depletion region. Hence, charge collection following a strongly-absorbing light pulse illumination is a single carrier process in a sense that only one type of carriers contributes to the output signal. Charges are induced on both electrodes by the drift motion of these carriers and a signal is generated in the attached charge-sensitive amplifier which integrates the induced current. A shaping amplifier is usually added to provide further amplification and also to improve count rate capability.

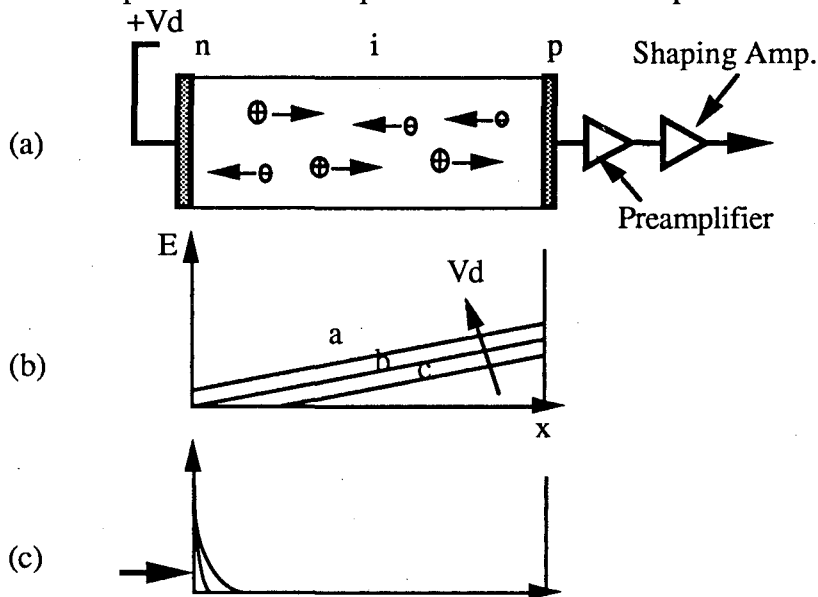


Fig.2-1 (a) Schematic view of a reverse-biased diode and nuclear electronics. (b) Field profile inside the diode at some biases. (c) Initial distribution of photo-induced carriers by a strongly-absorbing light illumination on one end of the diode.

There is a distinct difference in signal development depending on which side of the diode is illuminated by the strongly-absorbing light pulse. When photo-carriers are injected at the n-side, holes are the dominant contributor to the signal. At low bias, the

electric field does not extend to the n-side. Therefore, we would expect no signal until the full-depletion is achieved when the n-side is illuminated. As the bias is increased beyond the full-depletion bias, the signal should saturate since all the injected carriers complete transit through the diode. Thus the "hole signal" should show a threshold-type behavior as a function of bias. On the other hand, there should be a small signal at low bias when the p-side is illuminated because the field is non-zero at the p-i interface and is sufficient to drift electrons. At higher bias this "electron signal" would also saturate after the full-depletion bias is reached.

2-2. Theoretical analysis

When a charge q moves a small distance dx in a diode of physical thickness w , it induces charges $dQ=qdx/w$ [4]. After a certain amount of charge $q(x_0, t_0)$ [C/cm sec] is introduced in the reverse-biased diode at a certain depth x_0 and at time t_0 , the number of carriers decreases due to the deep trapping during the transit and its decrease is characterized by a lifetime τ . The field displaces the remaining carriers, leading to the induced charge dQ on the parallel plate electrodes at time t , which is given by,

$$dQ = \frac{q(x_0, t_0) \exp(-\frac{t-t_0}{\tau})}{w} dx \quad (1)$$

where dx is the displacement and w is the diode thickness. The current Δi due to this injected charge $q(x_0, t_0)$ generated in the external circuit at time t is given by,

$$\Delta i = \frac{dQ}{dt} = \frac{q(x_0, t_0) \exp(-\frac{t-t_0}{\tau})}{w} \frac{dx}{dt} = \frac{q(x_0, t_0) \exp(-\frac{t-t_0}{\tau})}{w} \mu E(x) \quad (2)$$

So far, only the contribution from the charge $q(x_0, t_0)$ is considered. The total current $i(t)$ is obtained by adding all the contribution from the injected charges, namely,

$$i(t) = \int_0^{w_d} dx_0 \int_0^t dt_0 \frac{q(x_0, t_0) \exp(-\frac{t-t_0}{\tau})}{w} \mu E(x) \quad (3)$$

where, the upper integral limit with respect to x_0 is the depletion width w_d . The upper limit with respect to t_0 denoted as t^* here is either the transit time t_r or the present time t , whichever is shorter. The transit time t_r is given by integrating the following equation.

$$\frac{dx}{dt} = \mu E(x) \quad (4)$$

Namely,

$$\int_{x_0}^x \frac{dx}{\mu E(x)} = t - t_0 \quad (5)$$

Since $x=w_d$ (depletion width) at $t=t_r$,

$$t_r = \int_{x_0}^{w_d} \frac{dx}{\mu E(x)} + t_0 \quad (6)$$

The total charge induced on the electrode during some measurement time T_m after the charge injection is simply an integral of eq.(8) for electrons and holes, namely,

$$Q = Q_{\text{electron}} + Q_{\text{hole}} \quad (7)$$

where

$$Q = \int_0^{T^*} i(t) dt \quad (8)$$

T^* is either the measurement time T_m or the time when all the carriers complete transit through the depletion layer whichever is shorter. Combining eq.(3)-(8), one can calculate the amount of charge collected during the measurement time T_m for a known injected charge distribution and electric field profile $E(x)$.

Conversely, from the measured charge collection as a function of the bias, we can determine the field gradient, i.e., the fixed space charge density, based on the known material parameters such as μ and τ and the injected charge distribution.

Although one can estimate charge collection in a diode with parallel plate electrodes for any given injected charge distribution by performing integrals shown above, it is instructive to derive an analytical expression for eq.(7) for a much simplified situation.

2-2-1. Instant charge creation at one end of a diode

First, the case with the following assumptions is treated below: (a) the charge carriers are injected only at one end of a diode instantly at time 0, (b) the field is linear through a diode, (c) it drops to 0 at the end of the depletion layer (abrupt junction approximation) and (d) carrier diffusion and dispersion are neglected. The current in eq.(3) becomes,

$$i = \frac{Q_0}{w} \exp\left(-\frac{x}{\tau}\right) \mu E(x) \quad (9)$$

The collected charge Q is obtained by integrating eq.(9) from time 0 to either the transit time of carriers or the measurement time. Notations used in this analysis are summarized in Table 2-1.

Table 2-1. Notation

Q_0	total injected charge	μ	carrier mobility
w	diode thickness	τ	carrier life time
w_d	depletion thickness	N_d^*	ionizable defect density
V_d	applied bias	ϵ_s	dielectric constant
T_m	measurement time		

When carriers are injected at the p-side of a diode, only electrons contribute to the signal. If a diode is only partially depleted, electrons cannot transit all through a diode, i.e., the measurement time is shorter than the transit time. Therefore, T^* in eq.(8) is substituted by T_m to obtain eq.(10). Just after the applied bias becomes high enough to fully-deplete a diode, the transit time is likely to be longer than the measurement time. So T^* in eq.(8) is substituted by T_m to obtain eq.(11). At higher bias, the transit time becomes shorter than T_m and T^* is substituted by the transit time to obtain eq.(12).

$$Q = \frac{Q_0 \mu}{w \beta} \sqrt{2\alpha V_d} [1 - \exp(-\beta T_m)] \quad \text{for } V_d \leq V_c \quad (10)$$

$$Q = \frac{Q_0 \mu}{w \beta} E_0 [1 - \exp(-\beta T_m)] \quad \text{for } V_c < V_d \leq V_e \quad (11)$$

$$Q = \frac{Q_o \mu}{w \beta} E_o \left[1 - \left[\frac{E_o - \alpha w}{E_o} \right]^{\beta / \alpha \mu} \right] \quad \text{for } V_e \leq V_d \quad (12)$$

where,

$$\alpha = \frac{q N_d^*}{\epsilon_s} \quad (13)$$

$$V_c = \frac{\alpha}{2} w^2 \quad (14)$$

$$V_e = \frac{1 + \exp(-\alpha \mu T_m)}{1 - \exp(-\alpha \mu T_m)} V_c \quad (15)$$

$$\beta = \alpha \mu + \frac{1}{\tau} \quad (15)$$

$$E_o = \frac{V_d + V_c}{w} \quad (16)$$

It is understood that the material parameters are those of electrons. From eq.(10) to eq.(12), the following behavior of the electron signal is evident: (a) the signal increases as a square-root of the bias until the diode is fully-depleted, (b) a shorter measurement time and a carrier lifetime both result in a smaller signal and (c) the signal saturates at Q_o under the condition $\alpha \mu \gg 1/\tau$ at high enough bias.

When carriers are injected at the n-side of a diode, only holes can contribute to the signal as long as the field extends all through a diode. When a diode is partially depleted, holes can not drift at all, i.e., no signal. Just after the applied bias becomes high enough to fully-deplete a diode, the transit time is likely to be longer than the measurement time. So T^* in eq.(8) is substitute by T_m to obtain eq.(18). At higher bias, the transit time becomes shorter than T_m and T^* is substituted by the transit time to obtain eq.(19).

$$Q = 0 \quad \text{for } V_d \leq V_c \quad (17)$$

$$Q = \frac{Q_o \mu}{w \gamma} E_{oo} [1 - \exp(-\gamma T_m)] \quad \text{for } V_c < V_d \leq V_h \quad (18)$$

$$Q = \frac{Q_o \mu}{w \gamma} E_{oo} \left[1 - \left[\frac{E_{oo} + \alpha w}{E_{oo}} \right]^{-\gamma / \alpha \mu} \right] \quad \text{for } V_h < V_d \quad (19)$$

where,

$$V_h = \frac{\exp(\alpha \mu T_m) + 1}{\exp(\alpha \mu T_m) - 1} V_c \quad (20)$$

$$\gamma = -\alpha \mu + \frac{1}{\tau} \quad (21)$$

$$E_{00} = \frac{V_d - V_c}{w} \quad (22)$$

The material parameters μ and τ are those of holes. From eq.(17) to (19), the following behavior of the hole signal is evident: (a) the signal is zero until a diode is fully-depleted, (b) a shorter measurement time and a carrier lifetime both result in a smaller signal and (c) the signal saturates at Q_0 under the condition $\alpha\mu \gg 1/\tau$ at high enough bias.

Charge collection efficiency (Q/Q_0) can be calculated from eq.(10)-(12) for electrons and from eq.(17)-(19) for holes, respectively. An example plot as a function of bias is shown in Fig.2-2 for the fixed peaking time. Assumed parameters for this plot are as follows: $w = 30 \mu\text{m}$, $T_m = 9 \mu\text{s}$, $\mu_e = 1 \text{ cm}^2/\text{Vs}$, $\mu_h = 0.005 \text{ cm}^2/\text{Vs}$, $\tau_e = 1 \mu\text{s}$, $\tau_h = 2.5 \mu\text{s}$, $N_d^* = 7 \times 10^{14} \text{ cm}^{-3}$.

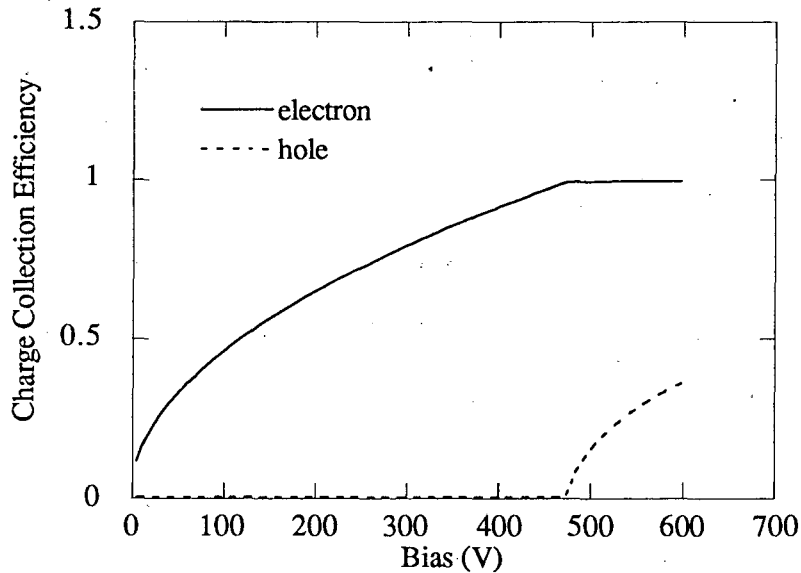


Fig.2-2 Calculated charge collection efficiencies for electrons and holes as a function of bias for a fixed peaking time of $9 \mu\text{s}$. The assumed diode parameters are given in the text.

Calculated efficiency as a function of the peaking time is shown in Fig.2-3 for a fixed bias. Assumed parameters for this plot are the same as Fig.2-2 except for the bias of 500 V and the peaking time which is varied from 0 to 10 μ s.

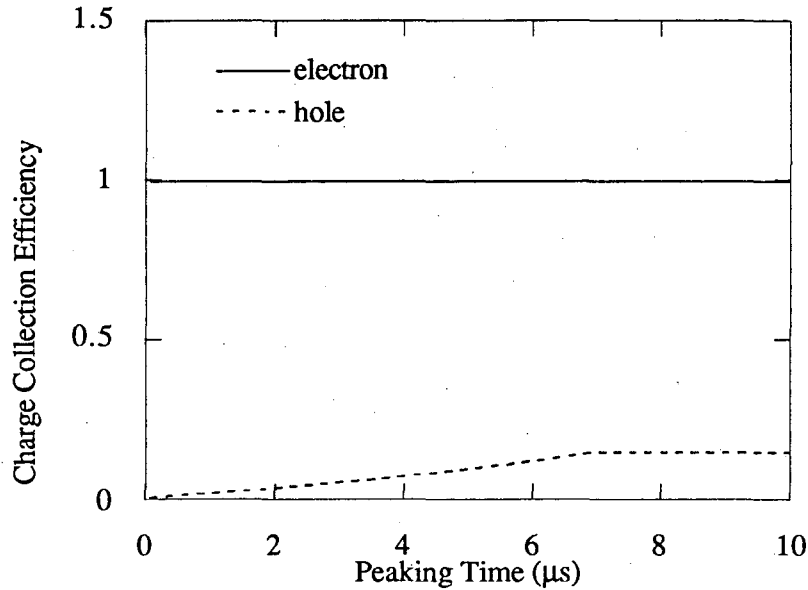


Fig.2-3 Calculated charge collection efficiencies for electrons and holes as a function of peaking time for a fixed bias of 500 V. The assumed diode parameters are given in the text.

2-2-2. Instant charge creation uniformly through a diode

Both carriers contribute to the signal. For each carrier contribution, eq.(9) is integrated over the depletion width and over the transit time or the measurement time, whichever is shorter, namely,

$$Q = \frac{Q_0 \mu}{w} \int_0^{w_d} dx_0 \int_0^{T^*(x_0)} dt E \exp\left(-\frac{t}{\tau}\right) \quad (23)$$

Now, the upper limit of the time integral depends on the location of the created charge. Q_0 is the injected charge per unit length of the diode. All the other notation is the same as before. With the same assumptions (b)-(d) in the previous case, one can perform

integration analytically for each carrier type and for the cases of (i) partial depletion and (ii) deep depletion. In the partial depletion case, each carrier contribution is expressed as,

$$Q_e = \frac{Q_0 \mu_e}{w \beta_e} V_d [1 - \exp (-\beta_e T_m)] \quad \text{for } V_d \leq V_c \quad (24)$$

$$Q_h = \frac{Q_0 \mu_h}{w \gamma_h} V_d [1 - \exp (-\gamma_h T_m)] \quad \text{for } V_d \leq V_c \quad (25)$$

It should be noted that the signal increases linearly with bias. To perform integration for the deep depletion case, the integral range of position x_0 is sub-divided into two ranges, the region where the transit time is shorter than the measurement time and the region where collection is limited by the measurement time. The boundary of these two regions depends on the bias. The resultant analytical expressions for the deep depletion case are complicated and don't give much additional insight on the signal behaviors other than that it saturates at high enough bias.

2-3 Measurement

Three light emitting diodes (LED) were chosen to give light pulses of three different wavelengths. Selection of LEDs was based on their wavelength and speed. Absorption coefficient of a-Si:H at various wavelength can be found in Chapter 1 of ref.[1]. The mean free path for absorption is calculated from this and shown in Fig.2-4. This will help us to choose wavelength of the light source for the experiments described in this chapter.

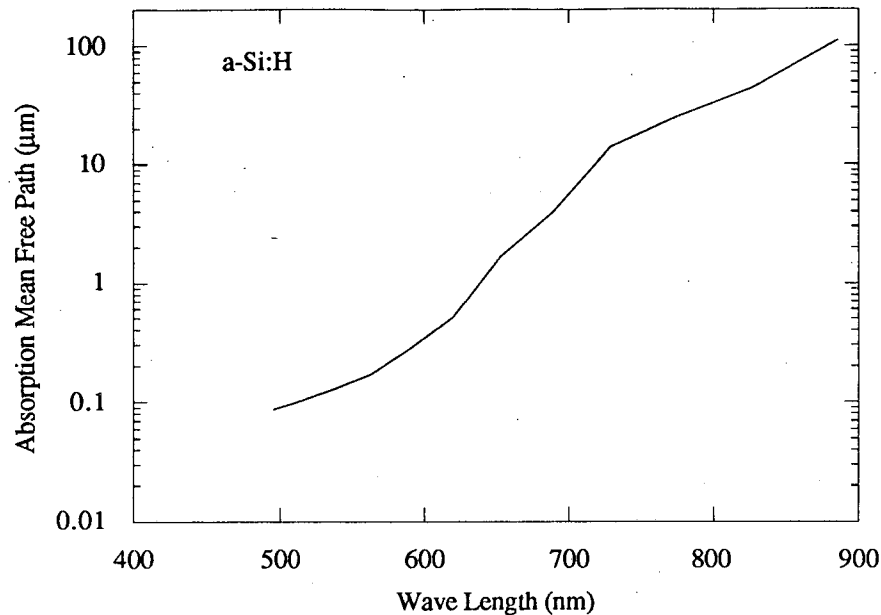


Fig.2-4 Absorption mean free path in a-Si:H at various wavelength.

The Hewlett-Packard LED (type HFBR-1510) was relatively fast (50 nsec rise time) and strongly absorbed at the surface (665 nm). The General Electric LED (type F5E1, 880 nm peak) was uniformly absorbed through a-Si:H but slow (1.5 μsec rise time). The Hitachi LED (type HLP40RA) was fast (20 nsec rise time) and gave intermediate wavelength (760 nm). These were easily available LEDs and gave light pulse intense enough for easy measurement, but were not necessarily the best choice. For example, shorter wavelength and faster light pulse would give better results which are easier to interpret as discussed later. The mean-free-path of these light sources in a-Si:H ranges from about 3 μm (665 nm light) to about 100 μm (880 nm light). The a-Si:H sample was a 27 μm thick p-i-n diode made by GSI. Its structure was Corning 7059 glass substrate/Tin oxide (transparent electrode)/p⁺-type a-SiC:H/i-type a-Si:H/n⁺-type a-Si:H/semi-transparent Cr electrode. This is a typical a-Si:H solar-cell structure except for the extremely thick i layer. A charge sensitive amplifier and a quasi-Gaussian shaping amplifier (variable peaking time 0.1-9

μsec) were installed to detect the photo-induced charges which were kept small enough not to distort the field established by the external bias.

Two parameters of the experiment were the reverse-bias and the peaking time. First, the bias was varied for a fixed peaking time to measure charge collection efficiency as a function of bias. Second, the peaking time was varied for a fixed bias to measure how fast the carriers are collected. In all cases shown below, the collected charges were kept small enough not to distort the existing field by adjusting the intensities of LED outputs.

2-3-1. Signal size dependence on a bias

The results of the first measurement are shown in Fig.2-5. The light pulsing conditions were 100 nsec pulse width for the 665 nm and 760 nm LEDs and about 3 μsec width for the 880 nm LED. Either the n or p side of the diode was illuminated by these light pulses with repetition rates ranging from 60 Hz to 600 Hz. Charge collection corresponding to a single light pulse was recorded as a function of the diode bias with the fixed peaking time of 9 μsec . It was found that the signal size depends also on the time after the bias is applied. The signal decreased first rapidly after the bias application, but it slowly reached an asymptotic value. The highest bias was applied to the diode first and the signal was recorded after a certain time when this equilibrium was reached. Subsequent data were taken by reducing the bias, thus avoiding this transient. This behavior of time dependence will be discussed further in section 2-3-3. The recorded signal size was normalized in the following manner: First, the 880 nm signals at the highest bias for both p and n-side illumination was set to 1.0. Because the 880 nm light is assumed to produce uniform injection through the diode independent of the direction of incidence, this normalization process corrects the difference in the amount of injected charges for p and n-side illumination, assuming that the transparency of the contact electrode and heavily-doped layer for the light is the same. The normalization constant for this process was used to scale the 665 nm and 760 nm signal for each case of p and n-side

illumination. Finally, for each of 665 nm and 760 nm signal, signal size was scaled with respect to the signal size of p-side illumination at the highest bias

The signals produced by the detection of the 880 nm and the 760 nm light pulses are shown in Fig.2-6 as a function of the applied bias for a fixed peaking time of 9 μ s.

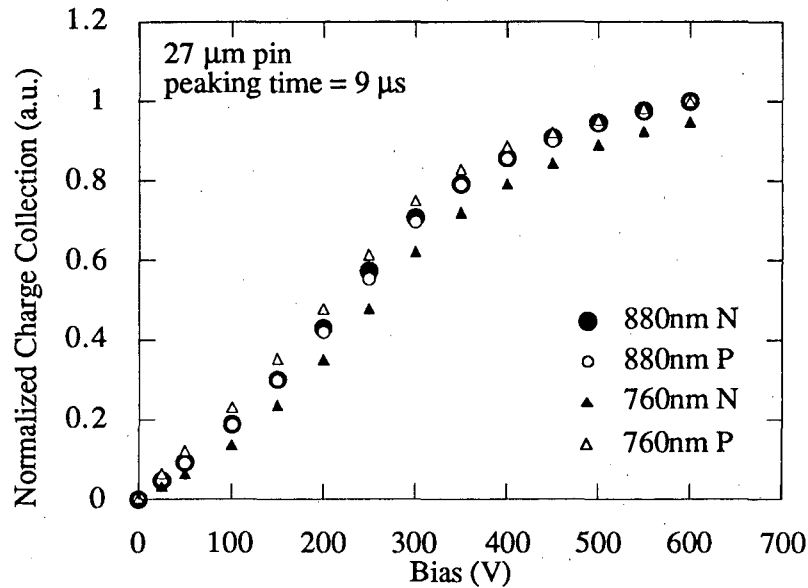


Fig.2-5 Charge collection as a function of the applied bias for detecting uniformly-absorbing light pulses.

It can be seen that charge collection for the p-side illumination is almost identical to that of the n-side illumination for the 880 nm light, confirming the uniform charge creation throughout the diode. The initial rise of these signals is linear with bias as predicted by eq.(24)-(25). As for the 760 nm light, the agreement between the p-side and the n-side illumination is less complete as expected from its shorter mean free path for absorption (about 30 μ m).

The signals produced by the detection of the 665 nm light pulses are plotted in Fig.2-6.

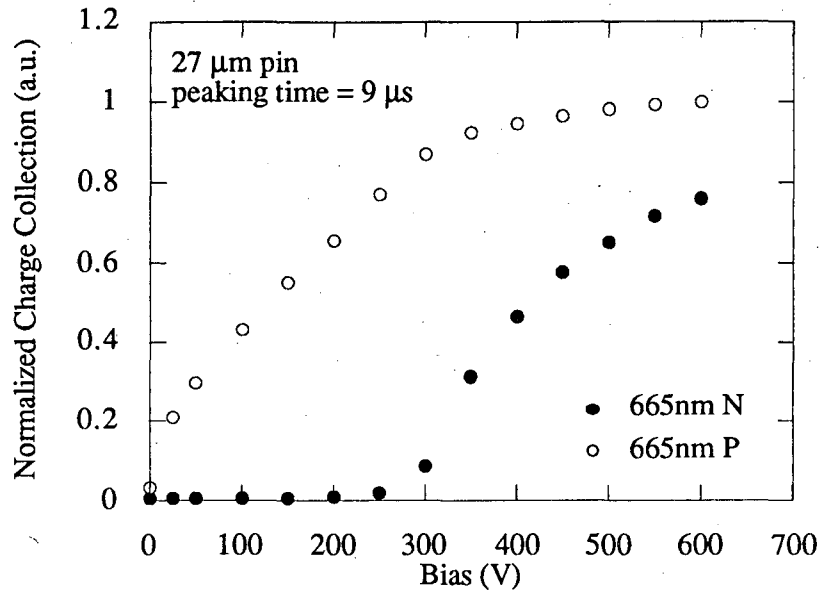


Fig.2-6 Charge collection as a function of the applied bias for detecting strongly-absorbing light pulses.

Now there is a distinct difference between the two cases. The "threshold behavior" was clearly seen in Fig.2-6 for the n-side illumination by the 665 nm light pulse (hole signal). There was not such a behavior for the p-side illumination (electron signal) and it started to saturate at the "threshold bias" of the hole signal. The increase of the electron signal at low bias is proportional to the square-root of bias as predicted approximately by eq.(10).

2-3-2. Signal size dependence on peaking time

Next, the bias was fixed at 500V and the peaking time was varied. This bias was chosen to measure the peaking time dependency at the operational bias. The same pulsing conditions of LEDs to obtain Fig.2-5 and Fig.2-6 were used except the pulse width was reduced to 50 nsec. The 880 nm LED was not used due to its limited speed. Signal size was normalized according to the corresponding data in Fig.2-6 at 500 V of bias and 9 μsec of peaking time.

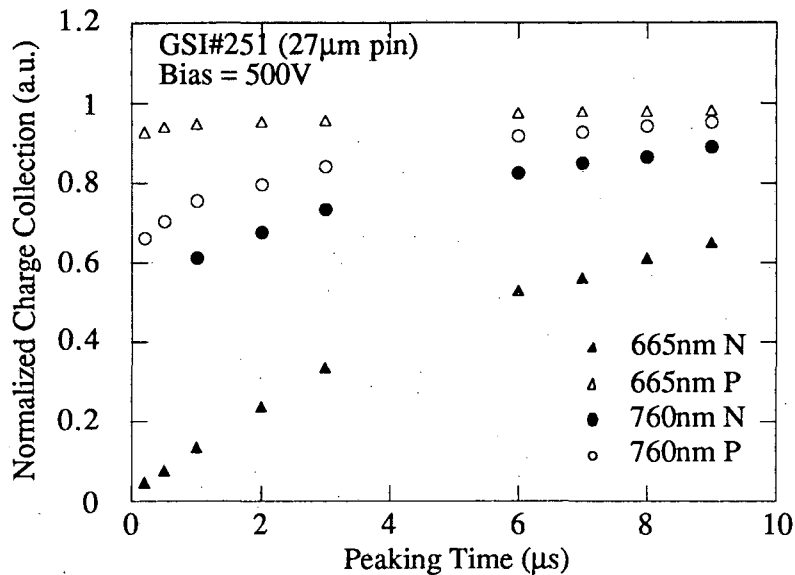


Fig.2-7 Charge collection as a function of the peaking time.

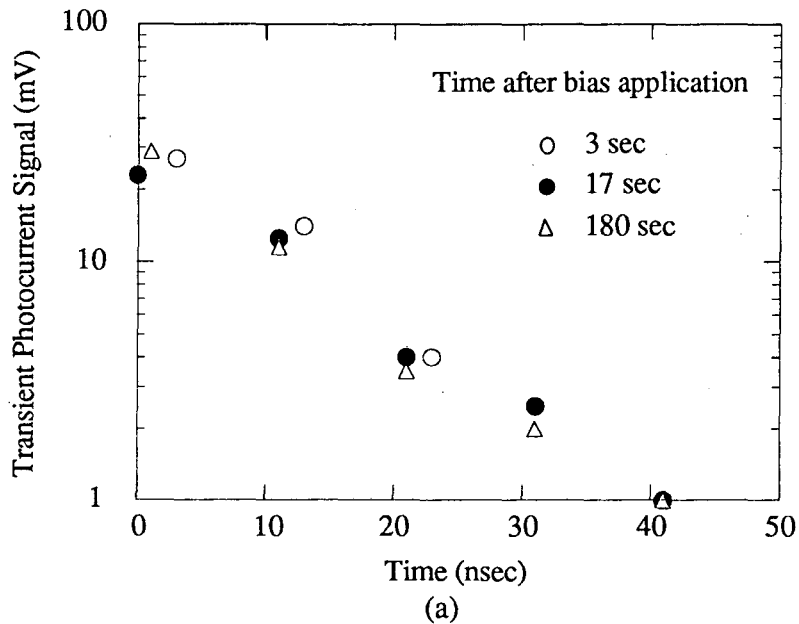
Figure 2-7 shows that more than 90 % of electrons (665 nm P) were collected within less than 100 nsec at this bias although the hole collection (665 nm N) was much slower. This can be attributed to the small hole mobility as well as the dispersive nature of the hole transport. The 760 nm signal is due to the mixture of both carriers and it shows an intermediate behaviour between the electron and hole signal.

2-3-3. N_d^* dependence on time after bias application

The signal size dependence on time after bias application was investigated further. It is related to the field profile dependence on the time, which is determined by the ionized defect density N_d^* . It is known in a-Si:H that defect (dangling bonds) states are located in the middle of the band gap and that they release electrons under DC bias to become fixed positive space charges [2]. The thermal excitation rate of electrons from the dangling bond sites to the conduction band is given by $\omega_0 \exp(-\Delta E/kT)$ where $\omega_0 \approx 10^{12} - 10^{13} \text{ sec}^{-1}$ and $\Delta E \approx 0.8 \text{ eV}$ [2]. N_d^* increases with time after the bias application and reaches an

equilibrium value when the emission rate of electrons becomes equal to the emission rate of holes. This transient regime of N_d^* provides information about the gap state distribution in the energy band as shown by Matsuura [5]. The Time of Flight (TOF) measurements [2] were repeated at various times after bias application to measure the time dependency of N_d^* . A detailed description of the TOF measurement and analysis is given in the Appendix at the end of this chapter.

The TOF signals following a 3 ns wide 510 nm Laser light pulse illumination on a DC biased a-Si:H p-i-n diode are shown in Fig.2-8 for each time after bias application. There is a change in the slope of the transient photocurrents in Fig.2-8 (a) and (b). This exponential decay of the transient photocurrent gives the ionized defect density as shown in the Appendix. This was calculated for each time after bias application and the result is shown in Fig.2-9. The equilibrium is reached about 6-7 min after the bias is applied.



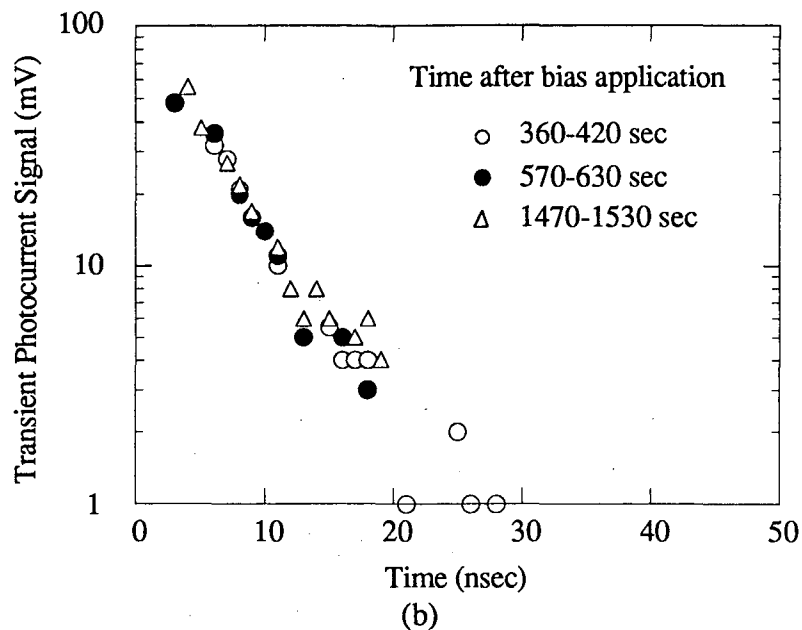


Fig.2-8 Successive TOF measurements for various times after bias application. Photocurrents following a 3 ns wide 510 nm Laser light pulse illumination on a DC biased a-Si:H p-i-n diode are shown for different times after bias application.

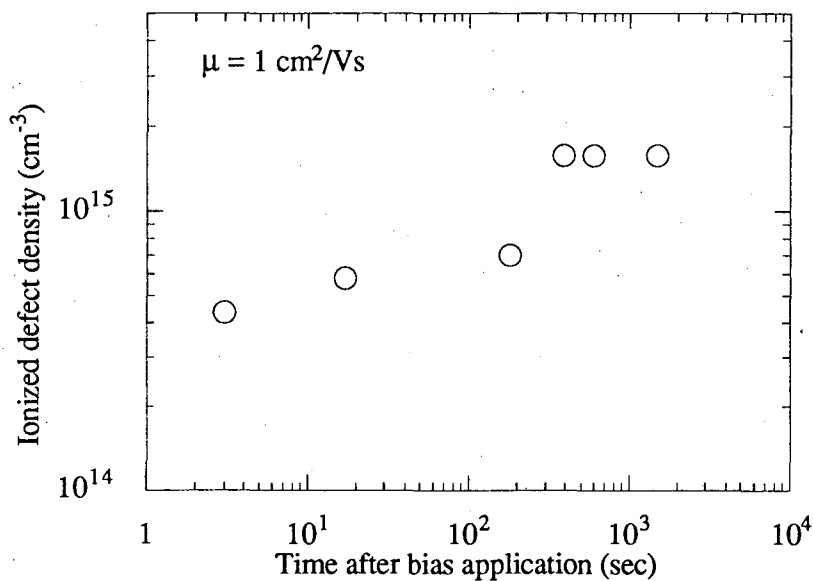


Fig.2-9 N_d^* calculated from the successive TOF measurements shown in Fig.2-8. Electron mobility of $1 \text{ cm}^2/\text{Vs}$ was assumed.

2-4. Discussion

In Fig.2-5 and Fig.2-6, the expected behaviors of signals induced by these LED light pulses are clearly observed as a function of the bias. Namely, the electron signal shows square-root dependency on the bias at low bias and tends to saturate at higher bias, whereas the hole signal shows the "threshold" behavior. The signals from the uniformly-absorbing light pulses show linear dependency on the bias. The expected peaking time dependency of the collection efficiency is observed as well in Fig.2-7. The electron signal is already saturated at the bias and peaking time range used whereas the hole signal slowly increases with the peaking time. The discrepancy between the measurements and the calculations may be due to the fact that the dispersive nature is not taken into account for the hole signal analysis.

Since the onset of the hole signal gives valuable information about the ionized defect density N_d^* , this signal was analyzed further by the theoretical expression given by eq.(17)-(19). The result is shown in Fig.2-10 together with the hole mobility and lifetime measured separately by the TOF analysis [6] with a much shorter pulse of 510 nm Laser light. The theoretical expression can fit the shape of the measured data fairly well. However, the more precise TOF analysis gave N_d^* of $7 \times 10^{14} \text{ cm}^{-3}$ for this diode [6]. The discrepancy between the TOF and this analysis presumably comes from the wavelength of "strongly-absorbing" light. The 665 nm light penetrates the a-Si:H layer further than the 510 nm light and the assumption of surface absorption in deriving eq.(17)-(19) is less valid for the 665 nm light.

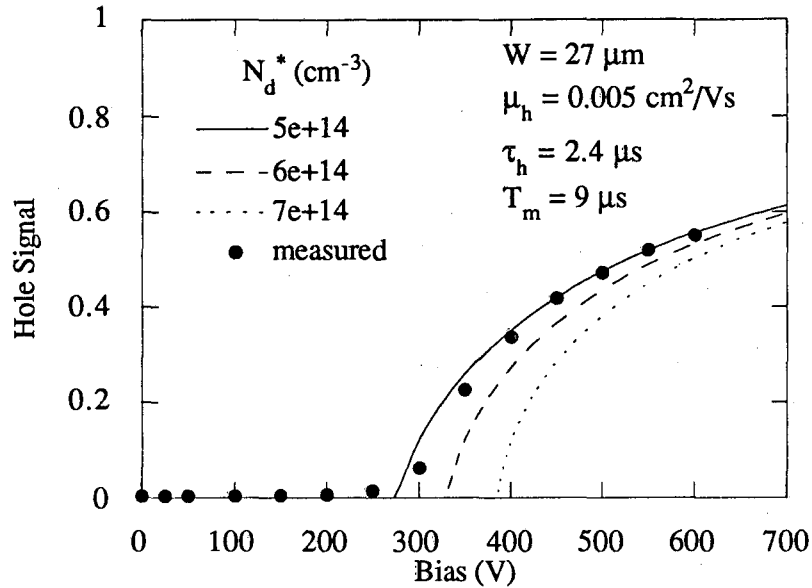


Fig.2-10 Hole signal fit by the theoretical expression (eq.(17),(18),(19)). A value of $7 \times 10^{14} \text{ cm}^{-3}$ for N_d^* was obtained by the TOF measurement. See text.

2-5. Conclusion

The charge collection and signal generation process in a-Si:H was investigated using a short light pulse from LEDs. It was shown that a thick a-Si:H layer can be biased high enough to ensure good charge collection efficiency. Electron collection takes place in a short time but hole collection takes longer and this results in signal loss when a peaking time less than a few μs is used. The theoretical expressions for the signal provide information about material parameters as well as tools for detector design optimization.

Two possible improvements to these experiments are both related to the light source. First, a shorter wavelength light is more strongly absorbed on the surface and the photo-carriers represent a better sheet-like spatial distribution. This results in a better "threshold behavior," i.e., the onset of the hole signal in Fig.2-6 becomes sharper. Second, using a faster LED or Laser can decrease the lower limit of peaking time in Fig.2-7.

Appendix Time of Flight analysis

Charge collection analysis described in this chapter is a quick test of depletion in a-Si:H diodes and it assumes that the basic material parameters such as mobility and lifetime are already known. These parameters are often deposition-dependent and are obtained by some characterization techniques, most notably by the Time of Flight (TOF) method. In this appendix, how one can calculate mobility, lifetime and field profile is described.

A1. Time of Flight Basics

Figure A1 shows the basic setup of the TOF method for the carrier mobility measurements. It consists of a pulsed Laser as a light source and transient current recording system. A wide-band amplifier is used to observe the voltage drop across the 50 Ω resistor and the clipping diodes are provided for amplifier protection. Photocarriers are injected by a strongly absorbing pulsed laser light through a semi-transparent electrode into an a-Si:H diode. One sign of carriers is drifted through the diode by the electric field created by the external bias. The other sign of carriers is immediately collected at the electrode and do not contribute to the photocurrent. The transient photocurrent due to the one type of carrier is recorded by the digitizing oscilloscope. From the measured transit time t_r and the known diode thickness w and applied bias V , the carrier mobility μ is calculated by the simple relation,

$$\mu = \frac{w^2}{V t_r} \quad (A1)$$

The following precautions about the experiment conditions must be made for meaningful and clean results.

- (i) The laser light must be absorbed strongly near the surface to make the initial carrier distribution close to a sheet. A wavelength of 510 nm is commonly used. The absorption depth of a-Si:H at this wavelength is less than 1 μm .

- (ii) The amount of the injected carriers Q must be much smaller than the "CV limit", where C is the capacitance of the diode and V is the applied bias, in order to ensure that the existing field is not disturbed by the drifting space charge. The often used criterion is,

$$Q < \frac{CV}{10} \tag{A2}$$
- (iii) The external bias must be applied shortly before the photoinjection to ensure that the field is uniform before and during the carrier transit. This delay time used in the experiment described in section 2-3-3 was 50 μ s.
- (iv) For repeated measurements by pulsing the bias and the laser, the frequency must be low to eliminate possible contribution from the trapped carriers which are released during the subsequent photoinjection. Two successive laser pulses were separated by at least 10 sec in the experiments described in section 2-3-3.

Some practical precautions about the setup are as follows:

- (i) The system must have enough time resolution. The main components of our ordinary TOF setup consist of a 150 MHz scope (Tektrix 2430) and a laser with 3 nsec pulse duration. This is good enough if we sample a number of pulses because the scope can fill the digitization gaps by multiple samplings, essentially increasing the sampling density. For a single pulse measurement as in the case of section 2-3-3, only one sampling is done every 10 ns. A higher sampling is desirable for more accurate measurements.
- (ii) The scope is triggered by the fast signal from the crystalline photodiode. The driving pulse of the N_2 laser should not be used for this purpose because laser pulsing has time fluctuation.
- (iii) The diode and the signal pick-up line must be well shielded from the outer world noise especially from the pulsing laser. A separate shielding box with a metal mesh on the laser input window is helpful. The filter for the bias should be kept close to the diode.
- (iv) The product of the contact resistance R and the capacitance C of a diode must be small compared with the transit time because it slows down the transient signal. Typically,

rise time less than 10 ns was obtained with the 3 ns laser pulse and 10-50 μm -thick diodes with 3 mm-diameter 50 nm-thick Cr contacts.

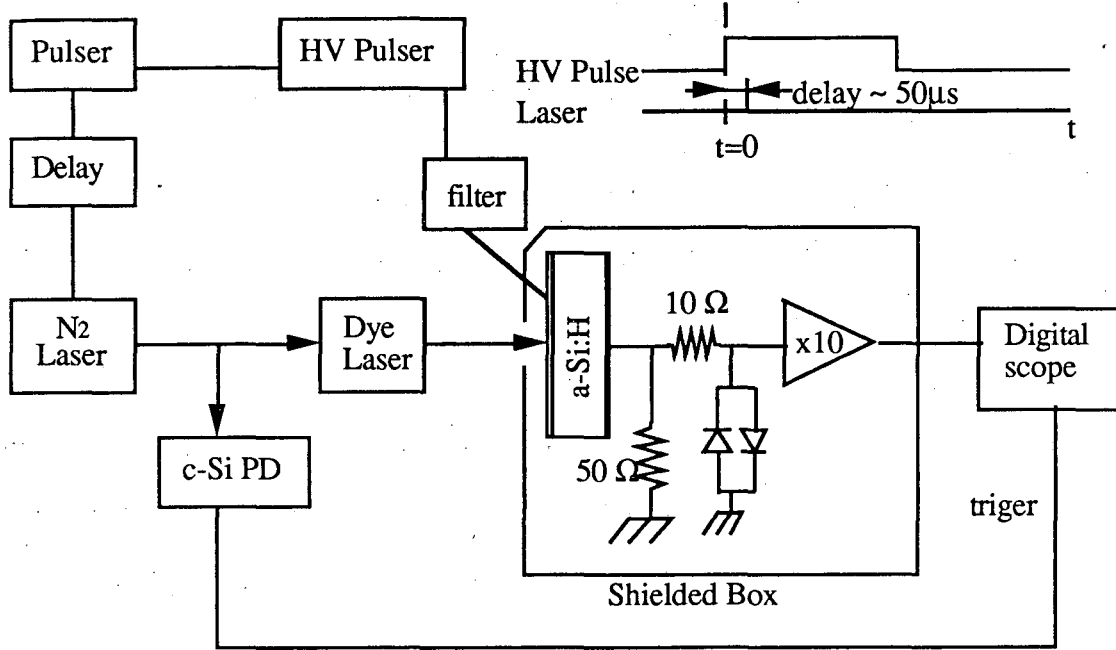


Fig.A1 Time of Flight setup

A carrier lifetime τ is another important parameter of the material and the product $\mu\tau$ is estimated by the so-called Hecht analysis. Here, the integrated photocurrent is recorded as a function of the bias. At the low bias, some carriers are lost due to trapping during the transit and cannot contribute to the charge collected. The carrier density n decreases from the initial value n_0 according to the relation,

$$n = n_0 e^{-t/\tau} \quad (\text{A3})$$

The induced charge on the electrode due to the drifting charges n is given by

$$dQ = n \frac{q dx}{w} = \frac{n_0 q}{w} e^{-t/\tau} dx \quad (\text{A4})$$

The total charge induced Q during the transit is,

$$Q = \frac{n_0 q}{w} \int e^{-t/\tau} dx \quad (\text{A5})$$

This integral is converted to the time integral from the relation $dx/dt = \mu V/w$ and is performed from time 0 to the transit time t_r . The result is,

$$Q = \frac{n_0 q V}{w^2} \mu \tau (1 - e^{-t/\tau}) \quad (\text{A6})$$

Hence, the $\mu\tau$ product is given by fitting the Q-V curve for $t_r \gg \tau$.

A2. DC bias TOF

The fixed space charge density N_d^* in an a-Si:H diode under DC bias is estimated from the electron transient photocurrent. Electron transient is preferred to hole transient which often shows dispersive nature.

Constant N_d^* case (simplified picture)

To show the functional dependence of electron transient photocurrent on time, the following simplified analysis may be helpful. First, the electric field can be assumed linear under the DC bias condition so that we have the following expression for the field profile.

$$E(x) = \frac{qN_d^*}{\epsilon_s} x - E_0 \quad (\text{A7})$$

Here, E_0 is the field strength at the surface where the photoinjected charges are located ($x=0$) initially ($t=0$). Noting that electrons are negatively charged, the electron drift velocity is then given by,

$$\frac{dx}{dt} = -\mu E(x) = -\frac{q\mu N_d^*}{\epsilon_s} x + \mu E_0 \quad (\text{A8})$$

This 1st order ordinary differential equation can be solved with the initial condition $x=0$ at $t=0$ to give the following solution.

$$x = \frac{\epsilon_s}{qN_d^*} E_0 (1 - e^{-\frac{q\mu N_d^*}{\epsilon_s} t}) \quad (\text{A9})$$

The field profile is rewritten as,

$$E(x) = \frac{qN_d^*}{\epsilon_s} \frac{\epsilon_s}{qN_d^*} E_0 (1 - e^{-\frac{q\mu N_d^*}{\epsilon_s} t}) - E_0 \quad (\text{A10})$$

The induced charge on the electrode by n electrons drifting through the parallel-plate electrodes of separation w is given by,

$$dQ = \frac{nqdx}{w} \quad (\text{A11})$$

The current is given by,

$$i(t) = \frac{dQ}{dt} = \frac{nq \frac{dx}{dt}}{w} = - \frac{nq}{w} \mu E(x) \quad (\text{A12})$$

Substituting the expression of $E(x)$ in the above equation, we get,

$$i(t) = \frac{nq}{w} \mu E_0 e^{-\frac{q\mu N_d^*}{\epsilon_s} t} \quad (\text{A13})$$

This simple analysis shows that the transient photocurrent diminishes exponentially with time under the DC bias condition. The slope of the $\ln i(t)$ - t plot is proportional to N_d^* .

The measured transient current has a somewhat modified shape at the beginning of the curve due to the built-in potential close to the electrode and/or the finite response time of the measuring system. However, we can identify the time range during which the transient current decays exponentially when the transient photocurrent is plotted in the semi-log scale. This range corresponds to the range of the linearly changing field. We can estimate N_d^* by the following relation.

$$N_d^* = \frac{\epsilon_s}{q\mu} \frac{\ln(i(t_1)/i(t_2))}{t_2 - t_1} \quad (\text{A14})$$

Non-constant N_d^* case (Field profiling)

The method described above for the constant N_d^* case can be extended to the non-constant N_d^* case, namely, the electric field profile of a reverse-biased diode can be obtained from the analysis of the transient photocurrent under the DC bias condition.

The transient photocurrent under the DC bias condition is related to $E(x)$ by eq.(A12).

$$i(t) = \frac{dQ}{dt} = \frac{nq \frac{dx}{dt}}{w} = - \frac{nq}{w} \mu E(x) \quad (\text{A12})$$

The measured quantity is $i(t)$. So the field profile $E(x)$ is given by a function of time.

Converting the variable t to x is done by the relation,

$$x = \int_0^t -\mu E(x) dt = \frac{w}{nq} \int_0^t i(t') dt' \quad (\text{A15})$$

Each value of t corresponds to each value of x by the above equation. Rescaling eq.(A12) by eq.(A15) gives the field profile $E(x)$.

Once the field profile $E(x)$ is estimated by above equations, the spatial distribution of the space charge density $N_d^*(x)$ is obtained by $N_d^*(x) = (\epsilon_s/q) dE(x)/dx$.

The anomaly of $N_d^*(x)$ within a few μm of the diode ends obtained by this method is likely due to the built-in potential as well as due to the real fluctuation in N_d^* .

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Chapter 3. Field profile tailoring

3-1. Introduction

To detect radiation directly, intrinsic a-Si:H layers considerably thicker than in most of other applications of a-Si:H are desirable. Diodes of simple structures such as Schottky barrier or p-i-n with 5-50 μm thick intrinsic layers have been fabricated and tested successfully with charged particles, X-rays and γ rays. Due to its low carrier mobility and lifetime, a high bias must be applied to ensure good charge collection efficiency as discussed in Chapter 2.

However, the highest reverse-bias applicable to these diodes is limited to about 10-20 V times the thicknesses of diodes in μm (or an average field of 10-20 V/ μm) by the onset of a "pop-corn" type noise [1,2]. On oscilloscope observation of the shaping amplifier output, this noise has a burst-like, intermittent behavior. Typical noise and current-bias (I-V) characteristics for some p-i-n a-Si:H diodes as measured with standard nuclear electronics with peaking time 6.4 μs is shown in Fig.3-1. These p-i-n diodes had 16-17 μm thick intrinsic layer with thin (30-50 nm) doped layers. The top contact was a 3 mm diameter disk-shaped evaporated Cr. The bottom contact was about 1 x 1 cm^2 evaporated Cr. Both electrodes were about 500 \AA thick. These samples were cut from a larger piece of a-Si:H layer deposited on a 0.8 mm thick 4' x 4' Corning 7059 glass coated with Cr. The noise onset at high bias is not associated with current increase and therefore neither the shot noise [3] nor the 1/f noise [4] is responsible for this sudden noise increase. Moreover, there is a wide variation in this highest bias even among the samples made in a single deposition run as shown in this example shown in Fig.3-1. This proposes a serious problem of nonuniformity for large area applications.

A possible explanation to this phenomenon is the microplasma breakdown [5,6] in the vicinity of the diode surface or the p/i interface where the field strength becomes maximum. Since the p layer is very thin, any material inhomogeneity close to the surface increases the

chance of local breakdown. The local field created by the material inhomogeneity can reach the critical field for avalanche multiplication temporarily and then the space charges created by the avalanche shield the external field, suppressing the avalanche. As the space charges are swept away, the high local field is established again by the material inhomogeneity and this avalanche-field collapsing cycle continues. Although this hypothetical mechanism is an interesting topic to investigate, a more practical requirement is to find how to suppress this noise onset at high bias or perhaps to harness it for avalanche multiplication. If we can control the field profile in a diode, the local field in the vicinity of surfaces can be suppressed while keeping high field in the majority of the diode.

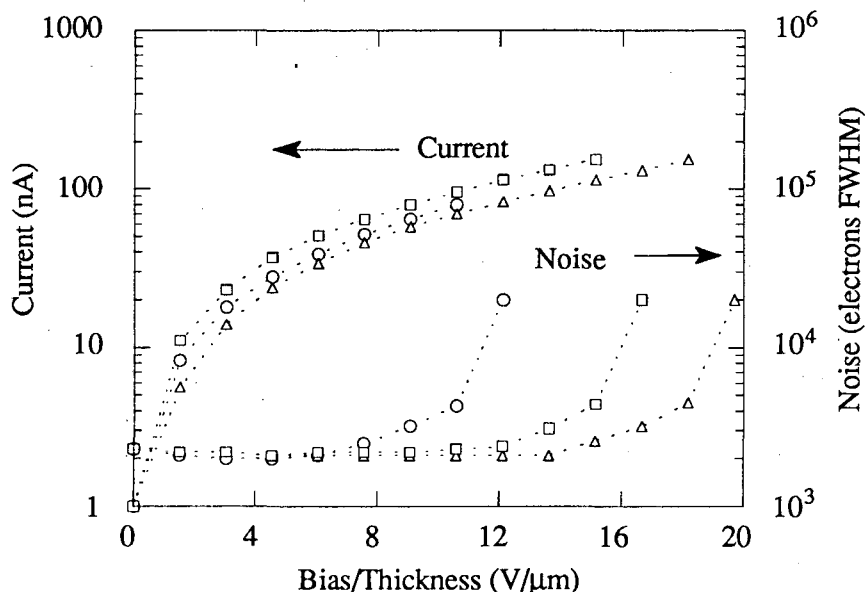


Fig.3-1 I-V and noise characteristics of three representative p-i-n diodes. These p-i-n diodes had 16-17 μm thick intrinsic layers sandwiched by thin (30-50 nm) doped layers and they were made from a single deposition run. The peaking time of the shaping amplifier was 6.4 μs .

Another problem associated with a thick a-Si:H layer with parallel plate electrodes is the field gradient determined by the ionized defect density N_d^* as discussed in Chapter 2. Unless the material quality is improved dramatically, the field gradient in a-Si:H layers may prevent full depletion of a diode under the highest reverse bias possible before the noise

onset. It would be advantageous to control the field profile which determines carrier transport.

In this chapter, the idea of tailoring the field profile is introduced to suppress the noise onset as well as to compensate for the effect of the field gradient. Two methods of manipulating the field profile to achieve these objectives, via doping and electrode geometry, are discussed. Field manipulation by doping utilizes the fixed space charges of the ionized donor and acceptor atoms in the doped layers. The characteristics of a "buried p-i-n" structure will be described in detail. Charge collection for α particle detection is shown to improve by the higher field achieved in this structure. A simple charge collection model for detecting a high LET particle with a low-mobility semiconductor material is developed to account for the charge collection process observed in α particle detection. Other structures such as making thicker doped layers in the p-i-n structure are also described.

3-2. Tailoring by doping

Doping a-Si:H is most commonly achieved by mixing gases during deposition. Abrupt compositional change at interfaces between doped layers and intrinsic layer is possible under this condition. Superlattice structure by doping modulation has been extensively investigated. Based on the same production method, field profile tailoring in a thick a-Si:H layer becomes possible.

3-2-1. Field profile calculation for multilayer structures

Field profiles of some doping-modulated multilayer structures are calculated by solving the Poisson equation in each layer and connecting each solution at each boundary. The assumptions for simplification of this mathematical problem are as follows. A parallel plate electrode configuration is assumed so that the Poisson equation reduces to an one

dimensional equation. Only the full-depletion case is treated here and charge density in each layer is assumed to be constant. The field in each layer is therefore linear and its gradient is proportional to the fixed space charge density arising from the doping atoms and the ionized dangling bonds.

Figure 3-2 shows a schematic multilayer structure. The space-charge density and thickness of each layer is denoted as N_i and x_i , respectively. Under the depletion approximation, the space-charge density N_i is assumed to be equal to the donor or acceptor density in the case of doped layers and to the ionizable defect density in the case of intrinsic layers under bias. This assumption is only valid where the bias is high enough to fully-deplete the whole layer and injected carrier density is negligible compared to N_i .

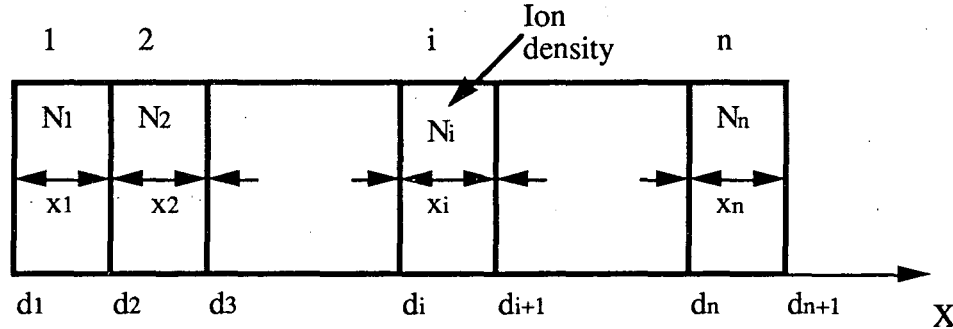


Fig.3-2 Multilayer structure

The Poisson eq.(1) is solved in each layer and each solution is connected at boundaries using the continuity conditions eq.(2).

$$\frac{d^2V_i}{dx^2} = -\frac{dE_i}{dx} = -\frac{qN_i}{\epsilon_s} \quad \text{for } i = 1, n \quad (1)$$

$$V_{i-1}(d_i) = V_i(d_i) \quad , \quad E_{i-1}(d_i) = E_i(d_i) \quad \text{for } i = 2, n$$

$$V_1(d_1) = V_a \quad , \quad V_n(d_{n+1}) = 0 \quad (2)$$

where, V_i and E_i is the potential and the electric field in the i th layer, respectively. ϵ_s is the dielectric constant of the material and q is the charge of an electron.

The first step is to integrate eq.(1) directly to give the following expressions.

$$E_i = -q_i x + a_i \quad (3)$$

$$V_i = -\frac{q_i}{2} x^2 + a_i x + b_i \quad (4)$$

where, $q_i = qN_i / \epsilon_S$ and a_i and b_i are integral constants to be determined below.

Substituting eq.(4) into eq.(2) with $d_1=0$, we have the following expressions for the coefficients a_i and b_i .

$$\begin{aligned}
 b_1 &= V_a \\
 b_i &= b_{i-1} + \frac{q_{i-1} - q_i}{2} d_i^2 \quad \text{for } i=2, n+1 \\
 a_n &= \frac{q_n}{2} d_{n+1} - \frac{b_n}{d_{n+1}} \\
 a_{i-1} &= a_i + (q_{i-1} - q_i) d_i \quad \text{for } i=n, 2
 \end{aligned} \tag{5}$$

The last step is to substitute eq.(5) into eq.(4) to give the solution E_i and V_i in all the layers.

Three structures are considered here as follows.

3-2-2. Buried p-i-n diode

The first structure considered here is a "buried p-i-n" diode. Two lightly-doped layers were inserted in a simple p-i-n diode. The object of such a structure was to reduce the peak field at the surface while keeping high field strength in most of the diode. An example structure and calculated field profile are shown in Fig.3-3. The assumed parameters are given in the caption. The maximum field occurs in a region away from the surface. This is the one of the structures already fabricated and measurements on it are discussed next.

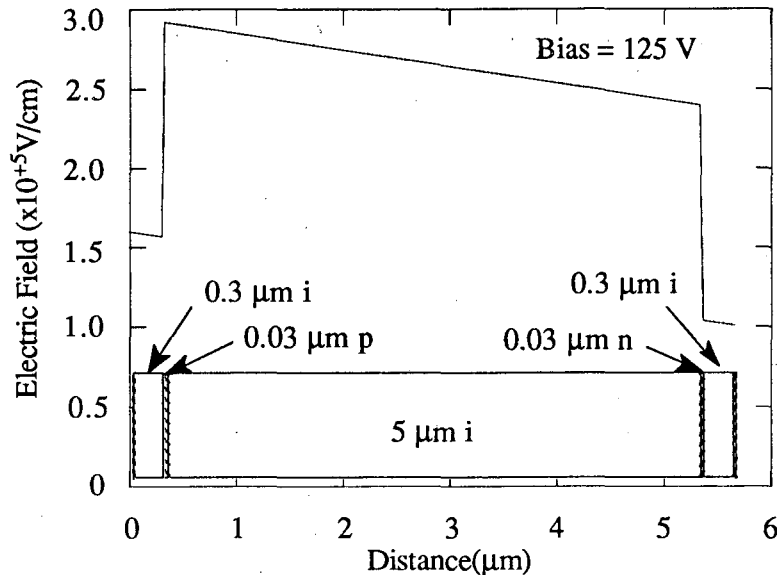


Fig.3-3 Structure and calculated field profile of a buried p-i-n diode. The heavily doped layers at both ends of the diode are not shown. The space density assumed in each layer is $7 \times 10^{14} \text{ cm}^{-3}$ in the i layer and $\pm 3 \times 10^{17} \text{ cm}^{-3}$ in the lightly-doped layers. A bias of 125 V is assumed.

3-2-2-1. Fabrication

Intrinsic layers and lightly-doped layers were successively deposited at Xerox PARC to make a structure consisting of glass substrate / Cr / $0.03 \mu\text{m n}^+$ / $0.3 \mu\text{m i}$ / $0.03 \mu\text{m n}$ / $5 \mu\text{m i}$ / $0.03 \mu\text{m p}$ / $0.3 \mu\text{m i}$ / $0.03 \mu\text{m p}^+$ / Cr as shown schematically in Fig.3-3. Top Cr electrode is a disk 3 mm in diameter and it forms a parallel plate electrode together with the bottom Cr layer. The dopant gas concentrations for the lightly-doped layers are determined from the preparation conditions [7].

3-2-2-2. Noise and I-V measurements

The noise and current-voltage (I-V) characteristics of the buried p-i-n diodes were measured with a low noise charge-sensitive preamplifier and a quasi-Gaussian shaping amplifier (6.4 μsec peaking time) connected. Figure 3-4 shows the measured noise and I-

V characteristics of this 5.7 μm thick buried p-i-n diode. The rapid noise increase of the buried p-i-n diodes occurs at an average field of about 50 $\text{V}/\mu\text{m}$ which is about three times larger than the case of the p-i-n diodes shown in Fig.3-1.

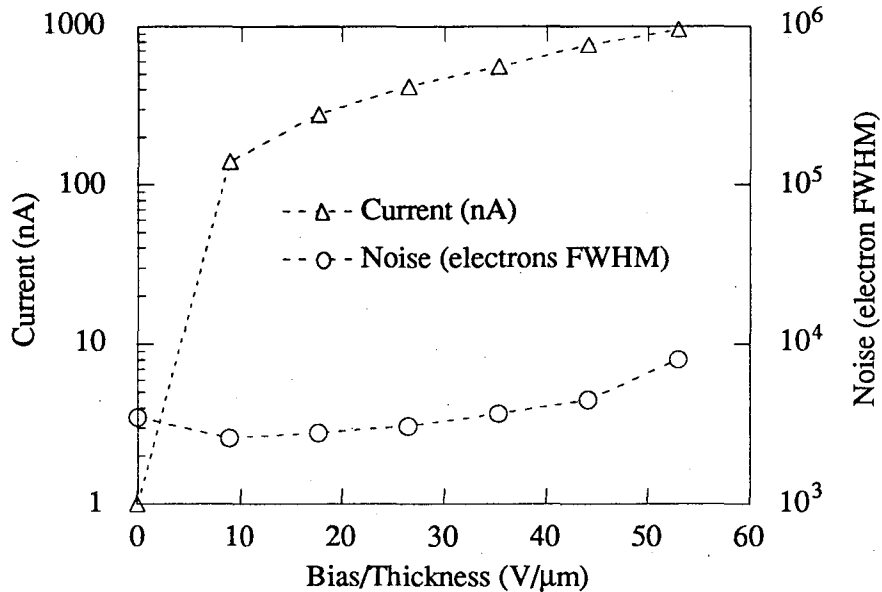


Fig.3-4 I-V and noise characteristics of the 5.7 μm thick buried p-i-n diode. The structure and the calculated field profile are shown in Fig.3-3.

The onset of this noise varied even among samples made from a single deposition run for both the conventional p-i-n diodes and these buried p-i-n diodes. Breakdown bias was defined somewhat arbitrary as the bias at which the noise exceeds three times the minimum noise level and a systematic study of this breakdown bias was conducted. Results are shown in Fig.3-5. The p-i-n diodes were all 16-17 μm thick with a 3 mm diameter disk Cr top electrode made from a single deposition run. This distribution for p-i-n diodes is a representative one for samples with different thickness. The buried p-i-n diodes were the ones described above. This comparison clearly shows that the buried p-i-n structure is effective in suppressing the onset of the noise at high bias.

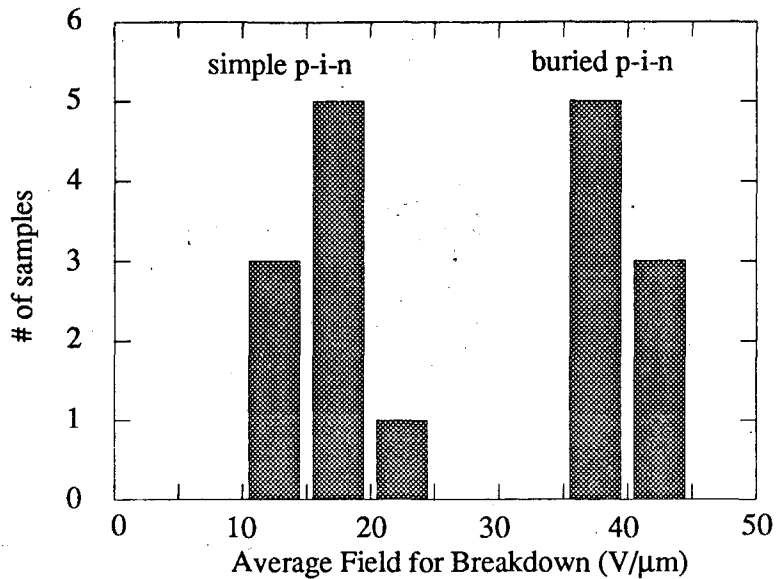


Fig.3-5 Comparison of the breakdown bias for the simple p-i-n diodes and the buried p-i-n diodes.

3-2-2-3. Signal generation from α particles

It has been shown that a-Si:H radiation detectors suffer recombination loss when detecting high LET particles such as α particles [2,8,9]. With the higher field achieved by the buried p-i-n diodes, charge collection for α particle detection should be greatly improved. A model of charge collection and carrier recombination loss following an α particle transit through a fully-depleted a-Si:H diode and α particle detection by the buried p-i-n diodes are described. A simple theory which takes into account both "plasma erosion" and carrier recombination is developed and applied to α particle detection by the buried p-i-n diodes.

Theory

The plasma erosion process in crystalline silicon has been modeled by many authors who analyzed the fission fragment detection by surface-barrier type silicon detectors [10] and more recently by groups working on the bit-inversion error caused by energetic particle

passage in digital integrated circuit chips [11]. The analysis described here follows the treatments by Seibt [10] for "end erosion" and Shur [11] for "surface erosion." A model of these two erosion mechanisms are schematically shown in Fig.3-6.

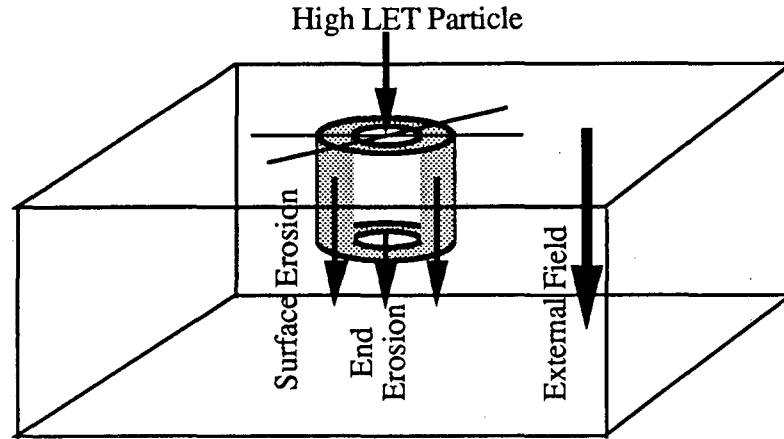


Fig.3-6 Plasma erosion model Two sources of erosion current are considered here.

The model by Shur assumes that the plasma column around the particle track is eroded by charge sweep-out at the column surface where the carrier density is at the level of the substrate doping and hence the external field is at its normal level. Carriers inside the plasma column have to move to the surface by ambipolar diffusion since the external field is shielded. The plasma density $P_{eh}(r)$ is given by a partial solution of the diffusion equation in a cylindrical coordinate system;

$$P_{eh}(r) = \frac{N_o}{4\pi Dt} \exp\left(-\frac{r^2}{4Dt}\right) \quad (6)$$

where D is the ambipolar diffusion coefficient, t the time after the particle passage, and N_o is the line plasma density defined as the normalization constant, i.e.,

$$N_o = 2\pi \int_0^{\infty} P_{eh}(r) r dr \quad (7)$$

At the surface of the plasma column ($r=r_s$), the plasma density drops to bN_A where N_A is the substrate doping level and b is a fitting parameter. The number of carriers available for drift is given by,

$$p_s = 2\pi \int_{r_s}^{\infty} P_{eh}(r) r dr = 4\pi b N_A D t \quad (8)$$

The erosion current due to these carriers is $I = qp_s\mu F$ where q is the electron charge, μ is the carrier mobility and F is the external field. The time derivative of the line density N_o is given by,

$$\frac{dN_o}{dt} = -\frac{I}{qL_c} = -\frac{p_s \mu F}{L_c} \quad (9)$$

where L_c is the length of the plasma column, i.e. the track length. Introducing dimensionless parameters N and T by the following expressions,

$$N = \frac{N_o}{N_{oo}}, \quad T = \frac{t}{t_s} \quad \text{where } t_s = \frac{N_{oo}}{\pi D b N_A} \quad (10)$$

where N_{oo} is the initial line density, and combining eq.(8) and (9), we obtain,

$$\frac{dN}{dT} = -\frac{4 N_{oo} \mu F}{\pi D b N_A L_c} T \quad (11)$$

Another possible source of the erosion current comes from the space charge limited current (SCLC) from the end of the plasma column initially located at the end of the track or the end of the detector layer whichever is shorter. The column length becomes shortened with time by the SCLC. Following the treatment by Seibt and using the notation by Shur given above, this contribution from the end of the column is expressed as,

$$\frac{dN}{dT} = -\frac{8 \mu \epsilon^2 F^3}{D (q b N_A)^2 L_c} T \quad (12)$$

where ϵ is the dielectric constant of silicon.

When we evaluate the coefficient of T in eq.(11) and (12) using parameters for a-Si:H, it turns out that the contribution from the column end (eq.(12)) is the dominant mechanism for plasma erosion in the field range of our interest.

Next, the line carrier density change rate due to the recombination inside the plasma column is considered. This recombination effect is negligible for crystalline silicon but

significant for a-Si:H due to its low carrier mobility. Following the treatment given by Adler [12], the time derivative of the volumetric carrier density n is given by,

$$\frac{dn}{dt} = -\frac{q\mu}{\epsilon} n^2 \quad (13)$$

Converting eq.(13) into the equation for the line carrier density requires a detail knowledge of the radial carrier distribution. Instead, we simplify this process by assuming a uniform distribution inside the column radius r_s so that $N_o = \pi r_s^2 n$. Using the dimensionless parameters defined in eq.(10) again, the recombination rate is given by,

$$\frac{dN}{dT} = -\frac{q\mu}{\epsilon} \frac{N_{oo}^2}{\pi^2 r_s^2 D b N_A} N^2 \quad (14)$$

When we combine the effects of plasma erosion eq.(12) and recombination eq.(14), the line carrier density is given as a function of time from the solution of an ordinary differential equation of the form,

$$\frac{dN}{dT} = -A T - B N^2 \quad (15)$$

This is the so-called Riccati differential equation and can be solved numerically.

If we define the plasma time T_p as the time when the carrier density drops to zero, the number of the collected carriers and the recombined carriers per unit length of the detector are calculated respectively by,

$$N_{coll} = \int_0^{T_p} A T dT = \frac{1}{2} A T_p^2 \quad (16)$$

$$N_{loss} = \int_0^{T_p} B N^2 dT \quad (17)$$

The collection efficiency eff. is given by,

$$\text{eff.} = \frac{N_{coll}}{N_{coll} + N_{loss}} = \frac{N_{coll}}{N_{oo}} \quad (18)$$

Measurements

The 5.7 μm and 15 μm thick buried p-i-n diodes were exposed to normally-incident 5.8 MeV α particles from a Cf-249 source in vacuum. Signal size was recorded as a function

of the bias across the unit diode thickness and is plotted in Fig.3-7. The 5.7 μm buried p-i-n diodes are the one described above. The structure of the 15 μm buried p-i-n diodes is glass / Cr / 0.03 μm n+ / 0.3 μm i / 0.06 μm n / 15 μm i / 0.1 μm p / 0.3 μm i / 0.03 μm p+ / Cr. The contact size and the doping concentrations were all the same with those of the 5.7 μm buried p-i-n diodes. The signal size from the conventional p-i-n diodes are also indicated by the box in the figure. In all diodes, the collected charges increase with bias and collection efficiency never reaches unity. This was attributed to recombination of carriers in the plasma column created by the α particle passage. This recombination loss is reduced by higher field because it erodes the plasma faster. The larger signal size from the buried p-i-n diodes compared to the conventional p-i-n diodes indicates the presence of the high field.

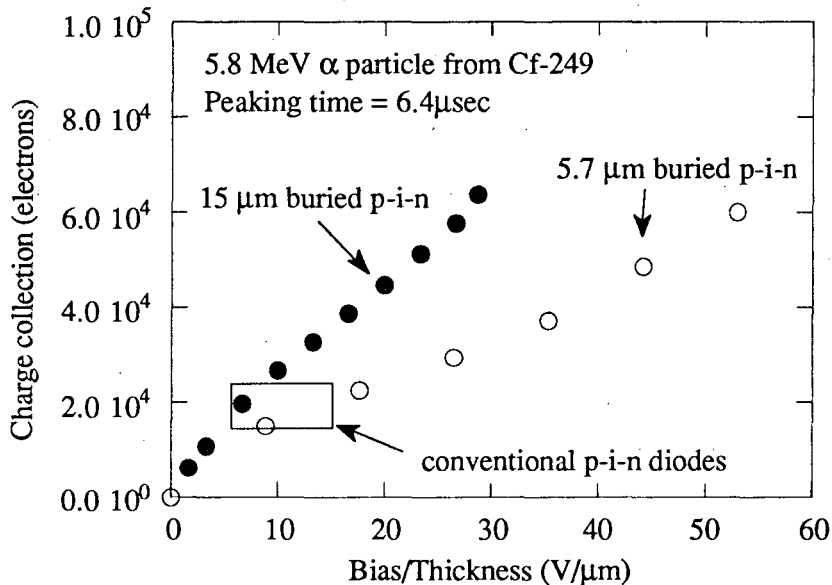


Fig.3-7 α particle detection with buried p-i-n diodes

Analysis

Curve fitting α signal size as a function of bias is attempted with the additional data in high bias region provided by the buried p-i-n diodes. The following parameter set was

used for numerical analysis: $N_{00} = 4.8 \times 10^8 \text{ cm}^{-1}$, $\mu = 1.0 \text{ cm}^2/\text{Vsec}$, $D = 5.2 \times 10^{-4} \text{ cm}^2/\text{s}$, $N_A = 1 \times 10^{15} \text{ cm}^{-3}$, $b = 1$, $L_c = 5 \times 10^{-4} \text{ cm}$, $\epsilon = 1.06 \times 10^{-12} \text{ farad/cm}$, $F = 1.6 \times 10^5 \text{ V/cm}$. The column radius r_s in eq.(14) is the only fitting parameter used and the results with $r_s = 2.3 \mu\text{m}$ give the best fit as shown in Fig.3-9. Figure 3-8 shows $N(T)$ for the same parameter set for six different fields i.e. $F = 1.6 \times 10^5 \text{ V/cm}$. The characteristic time t_s defined in eq.(10) is calculated to be $290 \mu\text{sec}$. Therefore, the plasma times are approximately 80 ns, 35 ns and 15 ns for field strength of 1×10^5 , 2×10^5 , $5 \times 10^5 \text{ V/cm}$, respectively. The column radius introduced in eq.(14) should not be regarded as the real column size. The recombination rate strongly depends on the initial spatial distribution of carriers. The recombination takes place rapidly in the center of the column at the beginning of the charge collection process and subsides with time as the density decreases due to the erosion and recombination. The details of the spatial distribution were neglected for mathematical simplicity while maintaining the functional dependence on carrier density. The use of a more realistic value for the column radius r_s , such as $0.1 \mu\text{m}$ (range of δ electrons) overestimates the recombination loss. This may be explained by the presence of the "latent period" during which carriers are too hot to recombine with each other as discussed in [13]. In any event, the model is too crude to give further details.

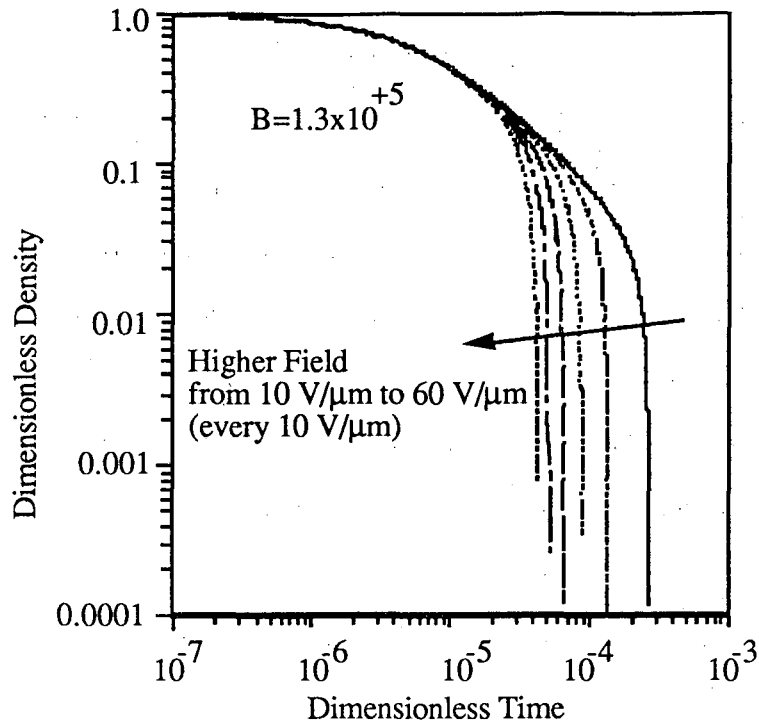


Fig.3-8 Carrier density inside plasma as a function of time for six different values of field. Both quantities are expressed in a dimensionless form defined in the text. The parameter B is chosen to fit the charge collection data shown in the next figure.

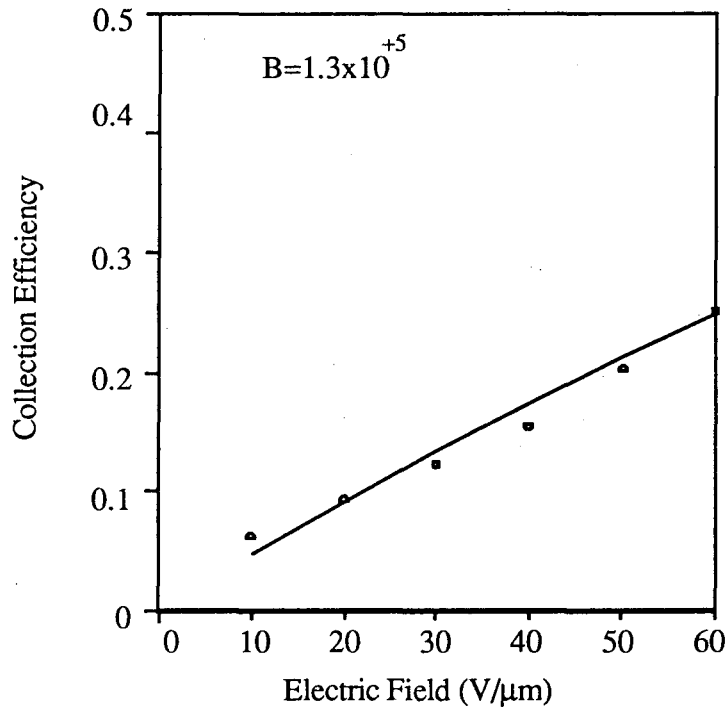


Fig.3-9 Calculated collection efficiency of a $5 \mu\text{m}$ a-Si diode for detecting 6 MeV α particles. The parameter B is chosen to fit the measured data for a buried p-i-n diode which will be shown in the next chapter

3-2-3. p-i-n diode with thick p layer

It has been reported that making the heavily doped p layer thicker in the p-i-n structure allows one to apply high bias on a-Si:H diodes [14]. A thick p layer works as a buffer layer to move the maximum field region away from the metal contact as in the case of the buried p-i-n diode. Assuming the same space charge densities with those in Fig.3-3, the field profile of a p-i-n diode with a thicker p is calculated and is shown in Fig.3-10. Because thicker layers are more conductive, a large edge current flows from the top electrode to the bottom electrode through these heavily doped layers, in effect, bypassing the diode. This edge current has to be reduced for low noise operation by etching the p layers surrounding the contact electrode [15]. Aside from this additional process step, the thick p layer scheme seems simpler to implement than the buried p-i-n structure. The noise characteristics of these diodes were not described in ref.[14,15] and it was not clear how effective this structure is in suppressing the noise onset. Therefore, it was worthwhile to fabricate diodes with thick doped layers to measure the noise characteristics and variation in the bias of the noise onset and compare its performance with the buried p-i-n structure.

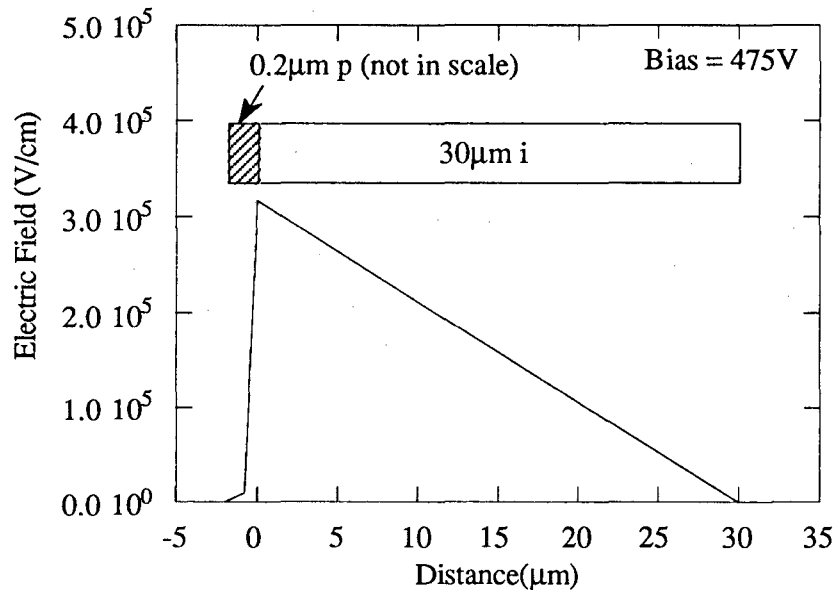


Fig.3-10 Calculated field profile for a p-i-n diode with a thick p layer. The same space charge densities are assumed with those in Fig.3-3. The case of the critical bias for full depletion is shown.

3-2-3-1. Fabrication

Two sets of diodes with different doped layer thickness were fabricated at LBL. The structure of the first set is Glass / Cr / 100Å n+ / 8.7 μm i / 120Å p+ /Cr. The top electrode is a 6 mm diameter Cr layer. The second set of p-i-n diodes has doped layers 10 times thicker than those of the first set. Only the deposition time of the doped layers were varied between these deposition runs. The deposition parameters are listed in Table 3-1.

Table 3-1. Deposition Conditions

layer	dep. time (min)	T _{sub} (°C)	RF (W)	SiH ₄ (sccm)	doping gas (sccm)	Pressure (mmT)	dep. rate (Å/min)
n	1 or 10	320	4	40	10(PH ₃)	680	100
i	420	320	7	40	NA	680	210
p	1 or 10	250	3.3	40	5(B ₂ H ₆) 90(CH ₄)	800	120

Note The substrate temperature is about 2/3 of the setting shown above. The gas cylinders for PH₃ and B₂H₆ contain 5 % PH₃ plus 95 % SiH₄ and 5% B₂H₆ plus 95 % He, respectively.

3-2-3-2. Noise and I-V measurements

The noise and I-V characteristics were measured and results of a representative diode for each set of diodes are shown in Fig.3-11. Thick doped layers were effective in suppressing current at higher bias, indicating that the dominant contribution to current in this bias range is the contact injection. There was a wide variation in the noise behavior for both sets of diodes. Statistical study in Fig.3-12 confirmed that a higher bias can be applied to a diode with thick doped layers before the noise increase limits the detector operation.

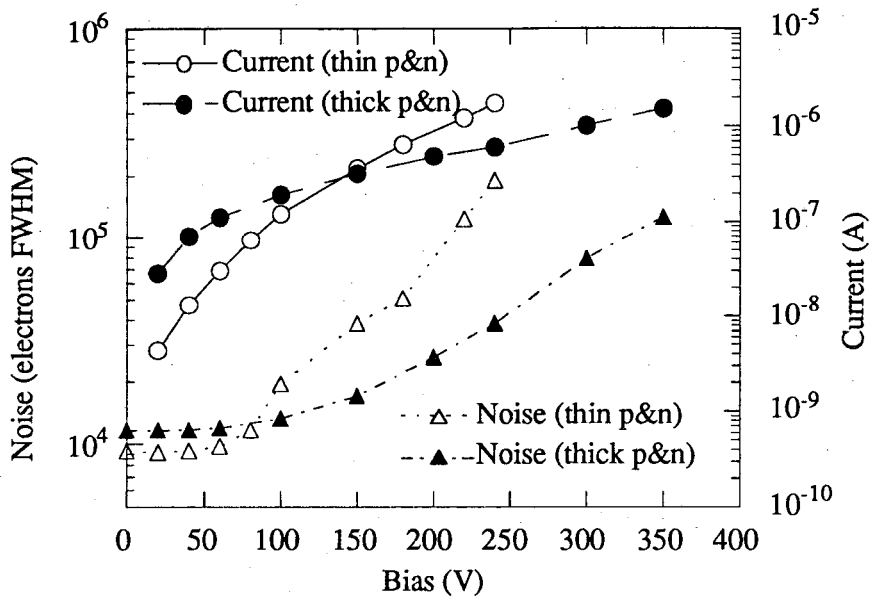


Fig.3-11 Noise and current-bias characteristics of a 8.7 μm diodes with thin doped layers and thick doped layers.

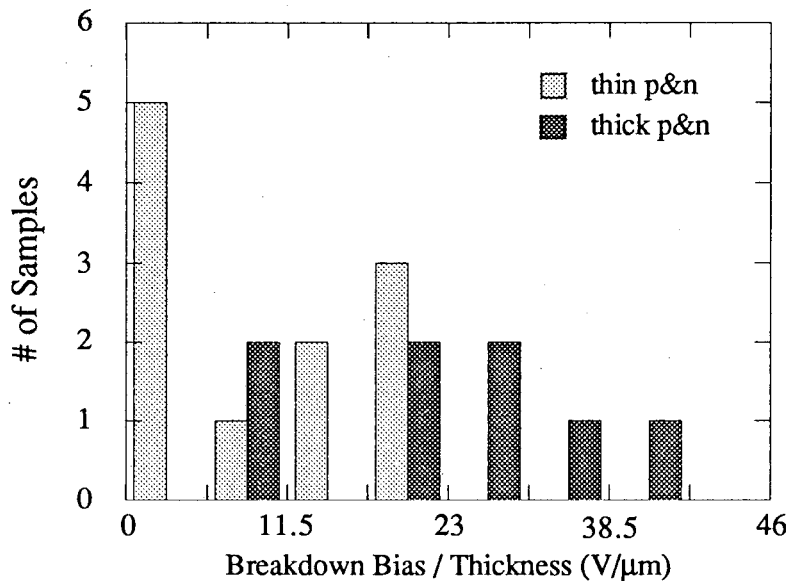


Fig.3-12 Breakdown bias for 8.7 μm -thick diodes with thin and thick doped layers. The top contact is a 6 mm-diameter 500 \AA -thick Cr layer for all the samples.

3-2-3-3. Discussions of relative merits

The statistical studies about the breakdown bias as shown in Fig.3-5 and Fig.3-12 show that the buried p-i-n structure is more effective in suppressing the noise onset, excluding the possible differences in preparation conditions such as substrate cleaning procedures which are presumably equivalent. Therefore, it is worthwhile to insert all the doped layers into intrinsic layer, which can be done easily by a multi-chamber PECVD apparatus as shown in Chapter 1.

3-2-4. Breakdown and ionized defect density

Despite the success of the buried p-i-n structure and the p-i-n diodes with thick doped layers, the argument of the breakdown mechanism is still not convincing because of the wide variation in the breakdown bias among samples made in a single deposition run as well as possible shortcomings for sample preparation. The following experiment was conducted in the hope of giving an additional evidence for the near-surface breakdown hypothesis.

It is known in a-Si:H that in addition to the existing dangling bonds created during deposition, more defects (dangling bonds) are created by heating above a certain temperature when hydrogen atoms terminating unpaired Si bonds become mobile. These defects are located in the middle of the band gap and they release electrons under bias to become fixed positive space charges, which can be estimated by the Time of Flight (TOF) experiment [16]. Therefore, one can investigate the effect of the defect density on the breakdown bias by measuring the noise characteristics of a single diode before and after heating it.

A 12 μm thick p-i-n diode made by GSI was heated at 300 °C for 10 minutes to double this defect density N_d^* [17]. The noise characteristics and N_d^* were monitored before and after the heating. The breakdown bias was decreased and the current was increased by the heating as shown in Fig.3-13. N_d^* measured by the TOF method was about $4 \times 10^{14} \text{ cm}^{-3}$

before and $8 \times 10^{14} \text{ cm}^{-3}$ after the heating. These numbers were smaller than what have been reported earlier [17] because the TOF measurements were done within 5 minutes after the bias application when the defects at the mid-gap states were still releasing electrons, although slowly. The field profiles before and after heating at each breakdown bias were calculated from each N_d^* and shown schematically in Fig.3-14. The maximum fields for both cases coincide approximately, offering evidence for the near-surface breakdown hypothesis.

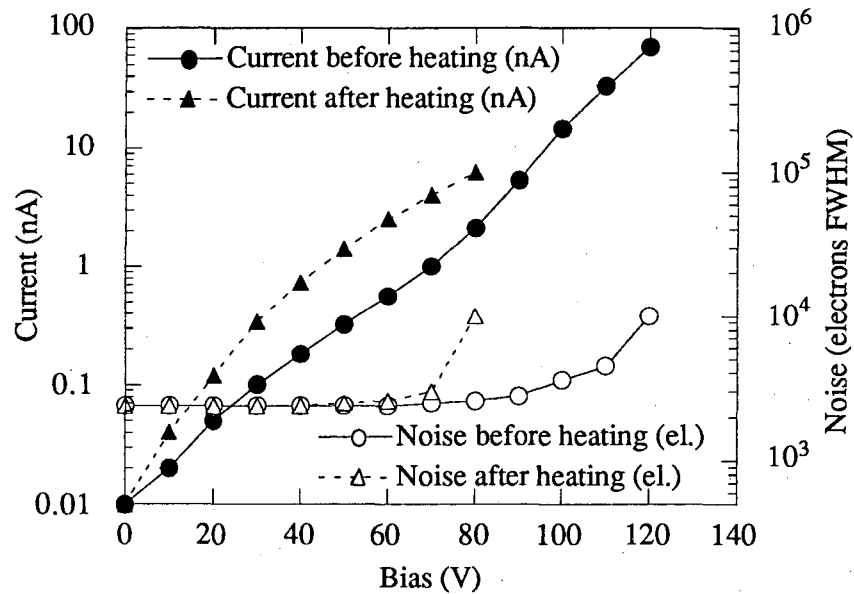


Fig.3-13 Noise and I-V characteristics of a 12 μm thick p-i-n diode before and after heating at 300 $^{\circ}\text{C}$ for 10 minutes. N_d^* measured by the TOF method was increased from $4 \times 10^{14} \text{ cm}^{-3}$ to $8 \times 10^{14} \text{ cm}^{-3}$ by this heating.

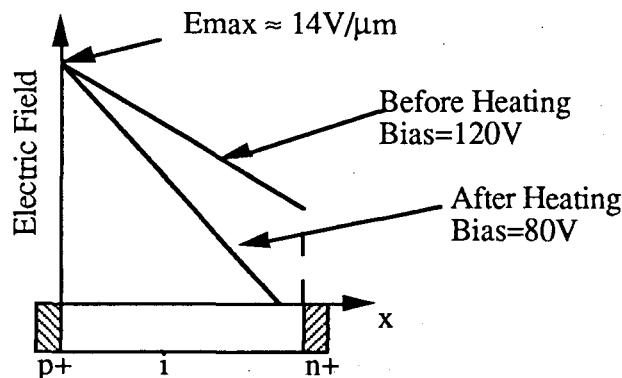


Fig.3-14 Field profiles at each breakdown bias before and after the heating.

3-2-5. p-i-n diode with buried p layer

Thicker a-Si:H p-i-n diodes require higher bias for full depletion since the field inside a diode has a gradient proportional to the ionized defect density in the intrinsic layer. This field gradient can be reversed by inserting a thin p layer which leaves negative space charges under depletion of free carriers [18]. A sample calculation of the field profile of this buried p layer is shown in Fig.3-15. The thin i-p structure is added to this i-p-i structure in order to decrease the field strength in the vicinity of the surface. The same space charge densities are assumed as those in Fig.3-3. The diode is over depleted at a bias of 1000 V and the maximum field strength is about 3.6×10^5 V/cm. On the other hand, a bias of 1300 V is required to fully deplete a 50 μm layer with space charge density of $7 \times 10^{14} \text{ cm}^{-3}$ and the peak field exceeds 5×10^5 V/cm which are calculated from eq.(14) in Chapter 2.

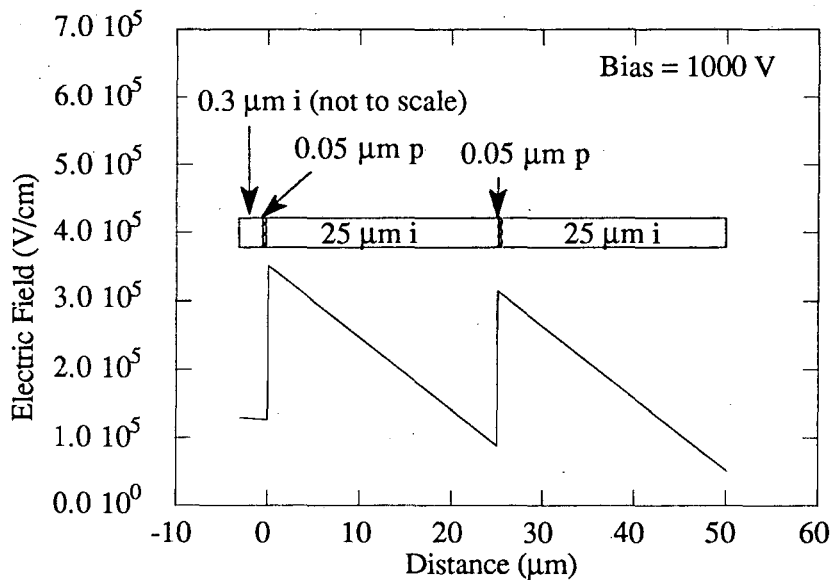


Fig.3-15 Calculated field profile for a buried p diode. The same space charge densities are assumed with those in Fig.3-3.

3-3. Tailoring by electrode shapes

To improve charge collection efficiency of thick a-Si:H diodes, it is advantageous to have a uniform electric field profile. Using thick doped layers is not desirable for good efficiency because the carrier lifetime and mobility are strongly affected by doping [19]. Field profile can also be controlled by changing the shape of the electrodes. In a parallel plate configuration, the field changes linearly due to the fixed space charge in the intrinsic layer under bias. When one of the plates is made smaller than the other, the field close to the smaller electrode is enhanced roughly by the ratio of the electrode sizes. This is because the number of field lines landing on the two electrodes are the same. With both of these effects present, we can make the field profile more or less uniform or, if desired, can make a very non-uniform field profile, by the proper choice of electrode geometry and the right bias polarity.

3-3-1. Field profile calculation for the cylindrical configuration

Assume a coaxial structure of two cylinders of inner radius a and outer radius R , sandwiching a dielectric material of a fixed constant space charge density of ρ and dielectric constant ϵ_s . The Poisson equation is solved analytically with boundary conditions $V = V_o$ at $r = a$ and $V = 0$ at $r = R$ to give the following expression for the field $E = -dV/dr$.

$$E(r) = \frac{\rho}{2\epsilon_s} r + \frac{V_o - \frac{\rho}{4\epsilon_s} (R^2 - a^2)}{r \ln \frac{R}{a}} \quad (19)$$

With $\rho = 0$, the field is inversely proportional to radius r . This is the direct consequence of the electrode shape, namely, all the field lines starting from the inner cylinder surface end up at the much larger outer cylinder surface, resulting to an enhanced field closer to the inner cylinder surface. For the case of intrinsic a-Si:H under bias, ρ is positive. This enhances the field at large r because the field lines starting from the fixed positive charge join the existing lines from the inner to outer radius. With both of these

effect present, we can make the field profile more or less uniform or, if desired, make a very non-uniform field profile, by proper choices of these parameters.

3-3-2. Microstrip electrode

To illustrate the possibility of field compensation by the electrode geometry, a simple case of a $50\ \mu\text{m}$ thick a-Si i layer (the fixed space charge density $7 \times 10^{14}\text{cm}^{-3}$) sandwiched by one continuous electrode and one discrete electrode is treated below. The code POISSON [20] was used to solve the resultant 2D-Poisson equation with suitable boundary conditions. Figure 3-16 shows the cross section of a unit cell of a diode with such electrodes. The bottom electrode is continuous and assumed to be biased at -2000V whereas the top electrode is discrete and grounded. The width and pitch of the top electrode are $4\ \mu\text{m}$ and $30\ \mu\text{m}$, respectively. The equipotential lines calculated by POISSON are shown every $200\ \text{V}$ in Fig.3-16.

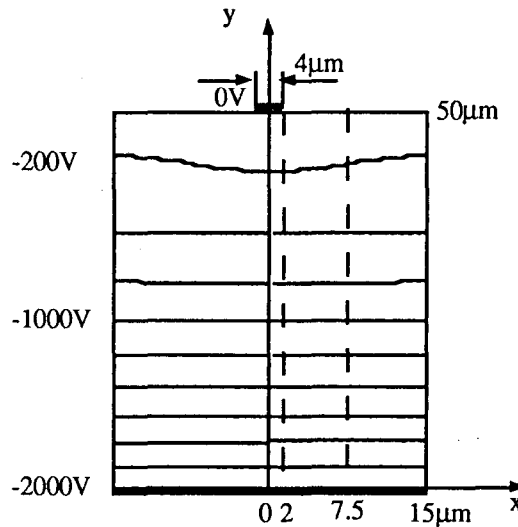


Fig.3-16 The cross-section of the diode with a continuous bottom electrode and a discrete top electrode. Equipotential lines are drawn every 200V when the bottom electrode is biased at -2000V .

The field strength along the lines $x = 0, 2, 7.5, 15\ \mu\text{m}$ in Fig.3-16 are plotted in Fig.17. The field drops linearly with the depth in the region close to the continuous electrode and it increases in the region close to the discrete electrode. The geometrical field enhancement compensates for the field drop due to the fixed space charge in the i layer to some extent.

It is apparent that a low field region exists in the region near the top surface between the electrodes. This region should not have a significant effect on charge collection process for the following reasons: (i) most of carriers generated in the bulk and drifting along field lines don't pass this region and (ii) as for the carriers created in this region, electrons, with their superior transport characteristics, have to drift only a small distance to the top electrode and hole contribution to signal is often lost anyway due to its low mobility and the limited collection time required by some applications. This structure has an additional advantage over the parallel plate configuration, namely, a reduced capacitance and, therefore, smaller delta noise.

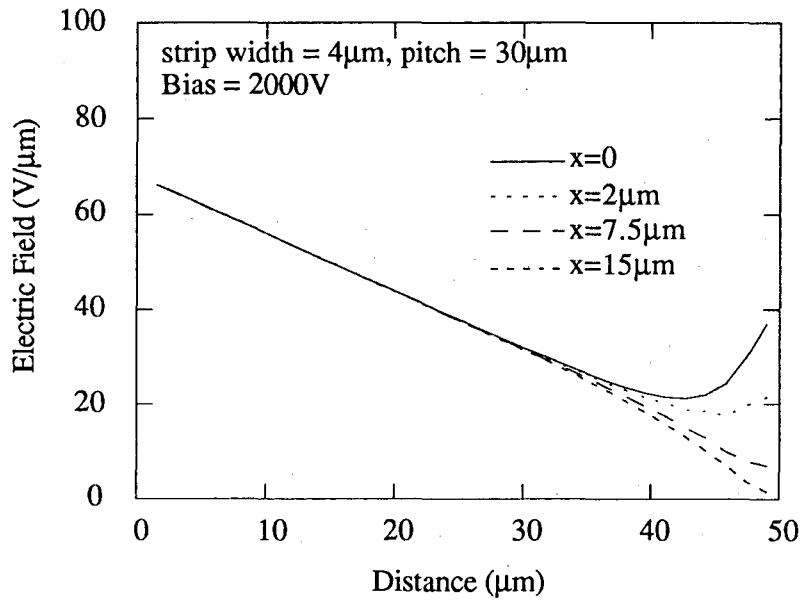


Fig.3-17 Calculated field profile of a diode with a continuous bottom electrode and a discrete top electrode. The field strength along lines $x=0, 2, 7.5$ and $15 \mu\text{m}$ in Fig.3-16 is shown here.

3-3-3. Fabrication and measurements of microstrip electrode

Two types of top Cr electrode patterns as shown in Fig.3-18 schematically were defined by standard photolithography techniques on a $28 \mu\text{m}$ thick pin diode. The solid square portion was added to the strip pattern as shown in Fig.3-18 (b) for ease of making contact.

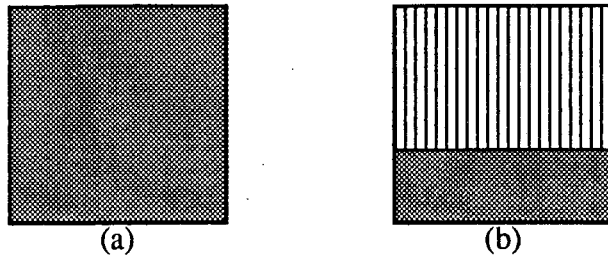


Fig.3-18 Schematic drawing of the two top electrode patterns: (a) solid square and (b) microstrip pattern with $2\ \mu\text{m}$ strip width and $10\ \mu\text{m}$ pitch attached to a $1 \times 3\ \text{mm}^2$ solid rectangle.

The structure of the diode was Glass / Cr / $30\ \text{nm-p+}$ / $28\ \mu\text{m-i}$ / $30\ \text{nm-n+}$. The bottom electrode was a plain Cr layer. The first top pattern was a $3 \times 3\ \text{mm}^2$ solid square which forms a parallel plate electrode with the bottom Cr layer. The second type was a strip pattern with $2\ \mu\text{m}$ strip width and $10\ \mu\text{m}$ pitch. Based on the measured capacitance of these diodes, capacitance of a 100 % microstrip pattern was estimated to be 65 % of that of the solid pattern.

Noise and current-bias characteristics of these diodes were measured and the results are shown in Fig.3-19. The diode with a microstrip top contact showed a slight degradation in the noise behaviour although the currents were almost identical for both contacts. This suggests the presence of a higher local field.

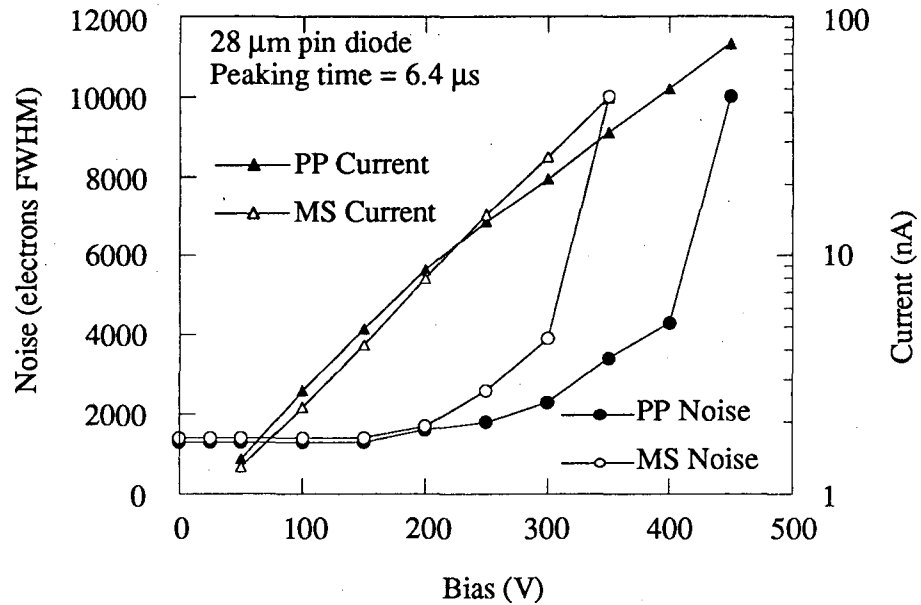


Fig.3-19 Noise and current-bias characteristics of 28 μm pin diodes with the parallel plate electrodes (PP) and the microstrip electrodes (MS).

Single carrier charge collection was measured by illuminating these diodes with 665 nm light pulses as in Chapter 2. The results are shown in Fig.3-20. The similar plateau curves for these two types of electrodes indicate that the low field region in Fig.3-16 does not affect the charge collection process in a diode with a microstrip electrode.

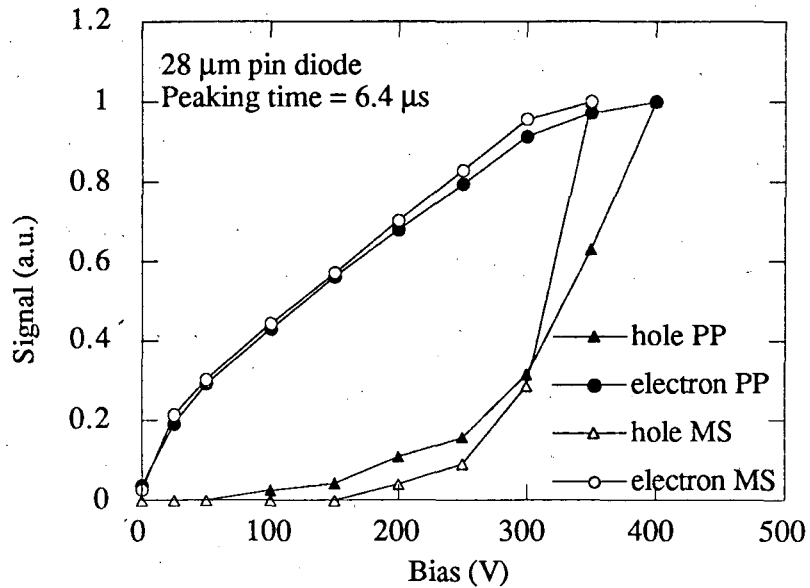


Fig.3-20 Charge collection in the 28 μm pin diodes with the parallel plate electrodes (PP) and the microstrip electrodes (MS).

3-4. Conclusion

Tailoring the field profile in a-Si:H radiation detectors as in the buried p-i-n structure was found to improve the charge collection process by allowing one to apply higher bias presumably through suppressing the near-surface breakdown phenomenon. It also gives a bias safety margin before breakdown for each element of a large area detector. A thicker p layer adjacent to the metal contact also enabled higher bias operation. A microstrip electrode gave charge collection efficiency similar to that of a parallel electrode configuration with a smaller capacitance.

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Chapter 4. Scintillation light detection

4-1. Introduction

In the indirect detection scheme, a scintillator converts incident radiations (charged particles, γ rays or X-ray fluences) into visible light ($\lambda=300-700$ nm) which is then absorbed mostly at the surface of a thin ($1-2\mu\text{m}$) a-Si:H layer, creating electron and hole pairs. As discussed in Chapter 2, the drift motion of these carriers by the external field generates signal in the readout electronics which is designed to either count individual particles or integrate fluences, depending on each detector application.

Choice of a scintillator also depends on the kinds of radiation and photo-sensitive device and count rate capability required by each application. For a quick comparison, some properties of commonly-used scintillators are summarized in Table 4-1 [1,2].

Table 4-1 Properties of commonly-used scintillators

Scintillator	density (g/cm^3)	emission peak (nm)	decay const (μs)	light yield (photons/MeV)
NaI(Tl)	3.67	415	0.23	38000
BGO	7.13	480	0.30	8200
CsI(Tl)	4.51	550	1.0	53000
CsI(Na)	4.51	420	0.63	38000
CsI pure	4.51	305	0.01-0.03	17000
CdWO ₄	7.90	540	5.0	15000
CaWO ₄	6.1	430	6.0	8000
CaF ₂ (Eu)	3.19	435	0.94	24000
GSO	6.71	430	0.06-0.6	14000
NE110	1.1	350-450	0.002-0.02	10000
NE102A	1.1	350-450	0.002-0.02	10000

High density is desirable for detecting X-rays or γ rays to ensure good detection efficiency. Emitted photons form some continuous spectrum and this shape is an important factor for selecting a photon-sensitive device such as photomultiplier and photodiode. Scintillation occurs immediately after absorbing radiation and its light emission decays exponentially with time. It is often sufficient to treat this decay as a single

component exponential in most cases. A shorter decay constant is preferred for applications requiring high count rate capability. Since there is a linear relation between the number of emitted photons and the energy deposited by radiation in most cases, it is common to express a light yield of a scintillator in terms of number of photons per unit energy absorbed. High light yield is desirable for larger output, especially for coupling to a photon detector without internal multiplication mechanisms.

Another important factor to consider especially for imaging applications is the light spread inside a scintillator which degrades the spatial resolution. A scintillation faceplate [3] as shown schematically in Fig.4-1 has been developed to cope with this problem and become available from several manufactures [4]. Glass fibers containing rare-earth elements are fused and drawn to make a few μm columnar structure. Scintillation light is confined in each fiber thus the degradation of spatial resolution caused by the light spread is avoided. Although the light yield of the rare-earth element scintillation faceplates is not high enough to detect single minimum ionizing particles, it is adequate for detecting intense fluences in the current-mode operation.

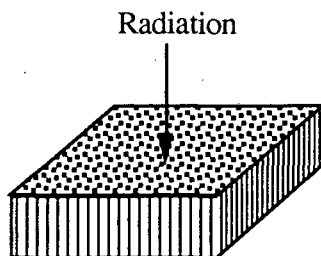


Fig.4-1 Scintillation faceplate. Glass tubing containing rare-earth elements is stacked, drawn and cut. Scintillation light is confined in a narrow region parallel to the plane normal.

Alternatively, a similar columnar structure can be made by CsI(Na) evaporation as used in the manufacturing process of an Image Intensifier [5,6]. Vacuum evaporated CsI forms columns parallel to the evaporation direction. The column diameter can be controlled by the substrate temperature. These columns form natural light pipes that function similarly to the

optical fiber described earlier, confining the light to produce a sharper image output at the phosphor-detector interface. CsI(Na) layers 300 μm thick with 5-10 μm column diameter are routinely made by manufacturers of X-ray image intensifiers for medical imaging [7].

Signal size in number of electron-hole pairs for the indirect detection scheme can be expressed as follows.

$$S = (1-L_{\text{opt}}) \cdot \text{QE} \cdot \text{TY} \cdot E_d \quad (1)$$

where,

$$\text{TY} = \int Y(\lambda) d\lambda \quad (2)$$

$$\text{QE} = \frac{\int \eta(\lambda) Y(\lambda) d\lambda}{\text{TY}} \quad (3)$$

Here, E_d is the energy deposited in the scintillator. A certain number of visible light photons are created by radiation (light yield per unit energy absorbed is denoted as $Y(\lambda)$ where λ is the wavelength), propagated to a photodiode through an optical coupling (if any) with some loss factor denoted as $(1-L_{\text{opt}})$ and electron-hole pairs are created in a photodiode with quantum efficiency $\eta(\lambda)$. Therefore, two correction factors must be known to determine an absolute light yield in terms of number of photons per absorbed energy, namely, the quantum efficiency factor QE and the optical loss factor $(1-L_{\text{opt}})$. The former can be obtained from the known emission spectra and a quantum efficiency curve of the photodiode. Solid curves in Fig.4-2 show the normalized emission intensity $Y(\lambda)$ for pure CsI, CsI(Na) and CsI(Tl) [8] and dotted curves are the quantum efficiencies $\eta(\lambda)$ of a standard photocathode for photomultipliers (Sb-Cs) [9], a crystalline Si photodiode (Hamamatsu S1723-04) [9] and a-Si:H photodiode in ref.[10]. Integration of the product $\eta(\lambda)Y(\lambda)$ is 52% of the total light yield of CsI(Na) for S1723-04. The corresponding value for CsI(Tl) is 70%. The quantum efficiency of this a-Si:H photodiode is superior to that of S1723-04 in the blue to yellow region. The second correction factor, the optical loss factor can be obtained if we use a scintillator whose light yield is accurately known.

In the following discussions, the observed signal size from a photodiode will be given in terms of electrons rather than attempting to unfold the observed signals by these two correction factors.

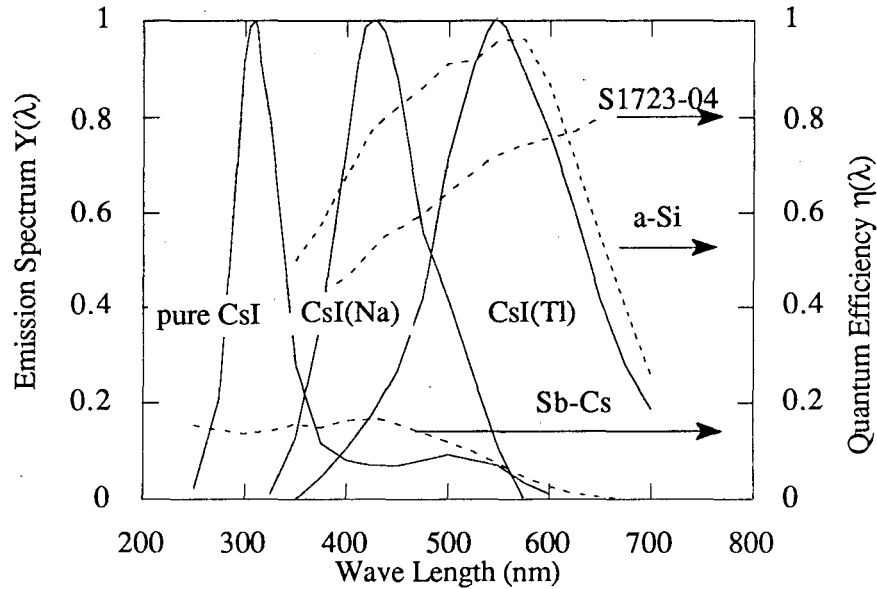


Fig.4-2 Normalized emission spectra of pure CsI, CsI(Na) and CsI(Tl) and quantum efficiencies of a photocathode (Sb-Cs), the Hamamatsu photodiode S1723-04 and the a-Si:H photodiode in ref.[10].

CsI(Na) is used for image intensifier application due to its good emission spectrum matching to the photocathode sensitivity as can be seen in Fig.4-2. However, the following advantages over CsI(Na) are expected from CsI(Tl) for coupling to a photodiode; (a) better emission spectrum matching to a photodiode quantum efficiency, (b) larger light yield and (c) better stability in air, although it has a disadvantage of a slightly slower scintillation decay.

In this chapter, the evaporation process and some characteristics of evaporated CsI layers such as signal yield, columnar structure and radiation damage will be described first. Detection of X-ray pulses and single β particles will be demonstrated with an evaporated CsI layer coupled to a photodiode. A monolithic type detector will be constructed and used to detect X-ray pulses.

4-2. CsI evaporation

The vacuum evaporation setup used is shown schematically in Fig.4-3. The important parameters are the substrate temperature and the boat temperature [6]. The higher substrate temperature tends to give larger fiber structure and better adhesion to smooth glass surfaces. Lower substrate temperature tends to give finer columnar structure. The boat temperature determines the evaporation rate.

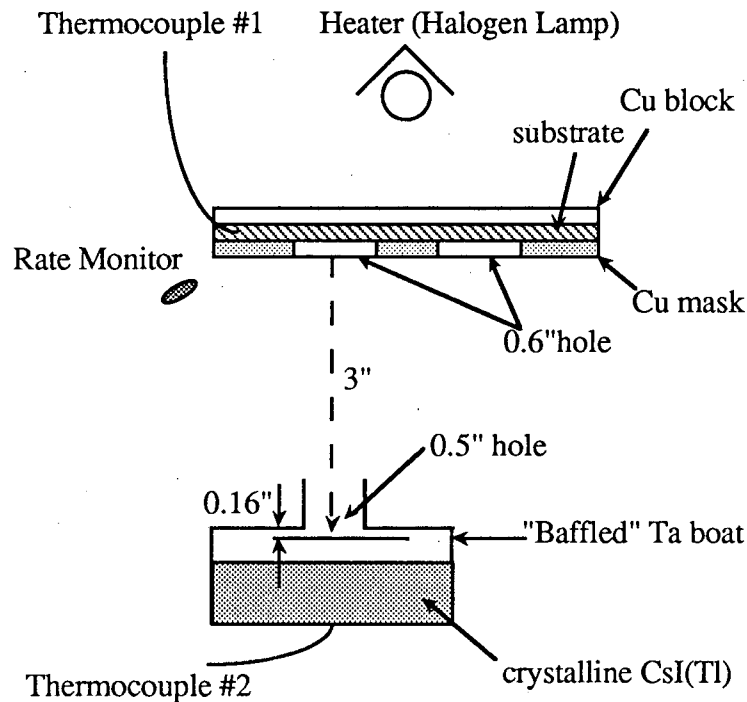


Fig.4-3 The vacuum evaporation setup for CsI faceplate fabrication.

Using the setup shown in Fig.4-3, CsI(Na) and CsI(Tl) layers were evaporated on glass substrates. The glass surface had to be roughened to improve CsI adhesion for evaporation on low temperature substrates ($\leq 300^{\circ}\text{C}$). Thickness of an evaporated layer was determined from the measured weight increase of the substrate and the known area size, assuming that the density of CsI is the same as crystal CsI (4.51 gm/cm^3). It was found later that the physical thickness is 10-20 % larger than the calculated value from the weight increase, indicating that the density of an evaporated layer is 10-20 % smaller than the crystal CsI. However, quoting thickness determined from the weight measurement is

more appropriate for X-ray detection than the physical thickness because the mass thickness (physical thickness multiplied by density) controls X-ray absorption. Therefore, thickness calculated from the weight measurement is quoted for an evaporated layer in the following sections.

First, CsI(Na) layers were evaporated on roughened glass substrates from a powder mixture of 99 % weight CsI plus 1 % NaI. A small amount of powder mixture was put into the Ta boat and was all evaporated for accurate thickness control. The evaporation time was usually about 20 min for a 300 μm thick CsI(Na) layer. The glass substrate was initially heated to 100 °C but its temperature increased to 140 °C by the end of a run due to radiation heating from the Ta boat. The boat temperature also increased by up to 30 °C at the end of a run as the material left the boat.

Second, CsI(Tl) layers were evaporated from a single crystal CsI(Tl) supplied by Engelhard Corp. with the same evaporation conditions. Evaporation from a powder mixture was not attempted because TlI is highly toxic and the vapor pressure of TlI at the melting point of CsI is much higher than that of CsI [5]. For the CsI(Tl), the evaluation rate was varied to improve the light yield. This will be described later.

4-3. X-ray test setup and CsI signal yield measurement

4-3-1. X-ray source and test setup description

The X-ray test set up system shown in Fig.4-4 was installed and used for the experiments described in this chapter. The grid potential of the X-ray tube was controlled by a pulse generator and a Control Box to generate desired X-ray pulsing conditions (pulse width from 1 μs to DC, repetition rate up to 1 kHz, intensity up to 1 mA of current level). The generated X-ray has a continuous energy spectrum whose maximum energy corresponds to the bias applied by the HV Power Supply. Thin brass and Al and brass sheets inserted between the tube and the Shield Box absorbed low energy portion of the X-

ray spectrum, resulting in a narrower spectrum. This narrow spectrum was treated as monochromatic. The error associated with this approximation is analyzed in sec. 4-3-3.

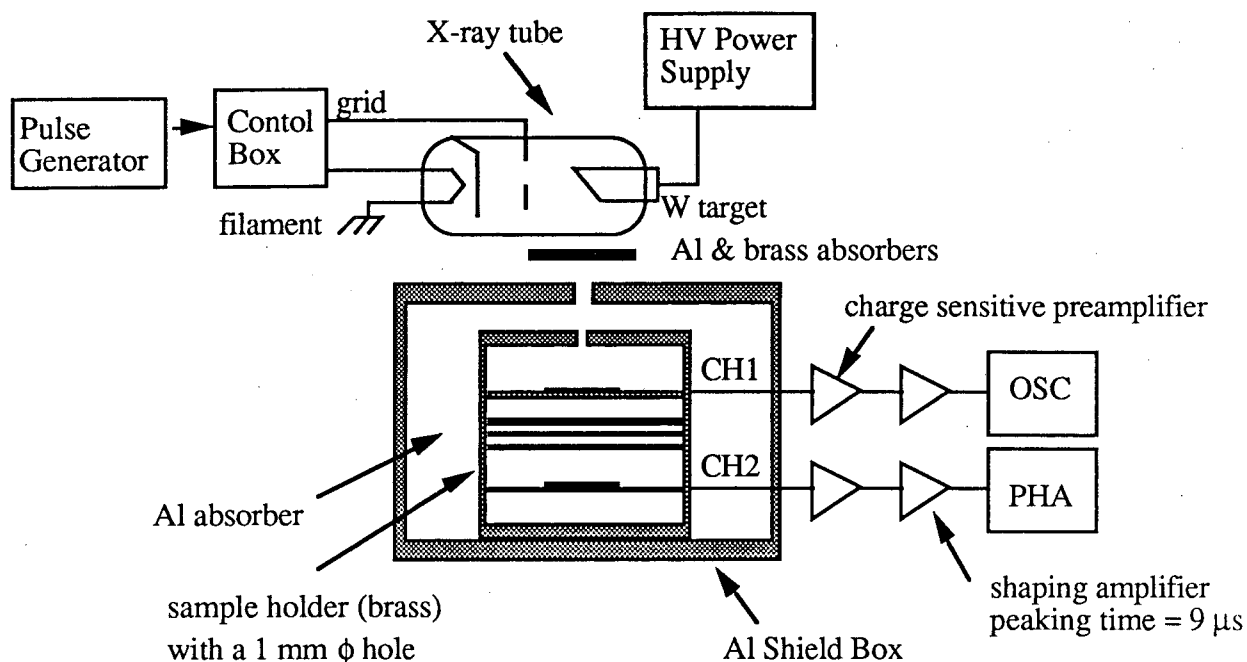


Fig.4-4 Schematic drawing of the X-ray system setup.

In the Shield Box, there are two sets of radiation detectors and their readout electronics (charge sensitive preamplifiers and shaping amplifiers). CH1 is a standard crystalline Si radiation detector without any backing materials. CH1 is used to monitor and control the total X-ray energy dumped into detectors placed in CH2. CH2 is the detector of interest, namely, a detector consisting of an evaporated CsI layer and a photodiode. Additional Al absorber was placed between CH1 and CH2 to determine the average energy of the X-ray spectrum during the calibration process which will be described next.

4-3-2. X-ray source calibration and CsI signal yield measurement

First, an evaporated CsI(Tl) layer 461 μm thick coupled to a crystalline Si p-i-n photodiode (Hamamatsu S1723-04) was placed in CH2. This photodiode was extensively used in this chapter because; (a) it was easy to collect all the scattered scintillation light due

to its fairly large size (1cm x 1cm area), (b) it had a small capacitance (50pF) for low noise operation, (c) its quantum efficiency was calibrated by the manufacturer and (d) it permits testing of phosphors separate from a-Si:H photodiode characteristics.

X-ray pulses were generated for three different settings of the HV Power Supply (50 kV, 30 kV and 20 kV). The pulse width was set to 1.5 μ s, 3 μ s and 5 μ s for 50kV, 30kV and 20kV X-rays, respectively, to produce a large signal for easy detection but short enough to avoid the ballistic deficit, a signal loss when the signal generation takes longer than the peaking time (the peaking time was set to 9 μ s). The thickness of the Al absorber between CH1 and CH2 was varied for each case to obtain the absorption curves shown in Fig.4-5. These three curves were fitted by exponential curves to give effective linear attenuation coefficients of Al, denoted as μ_{Al} , for these X-ray spectra.

Based on the attenuation coefficients given by this curve fitting μ_{Al} , the average energies of the X-ray spectrum coming out of the crystalline Si detector in CH1, denoted as E_a , were determined from the X-ray cross section table for Al. The coefficients for Si and CsI at these energies are given from the cross section table and the Harshaw (Engelhard) catalogue [2]. These are summarized in Table 4-2.

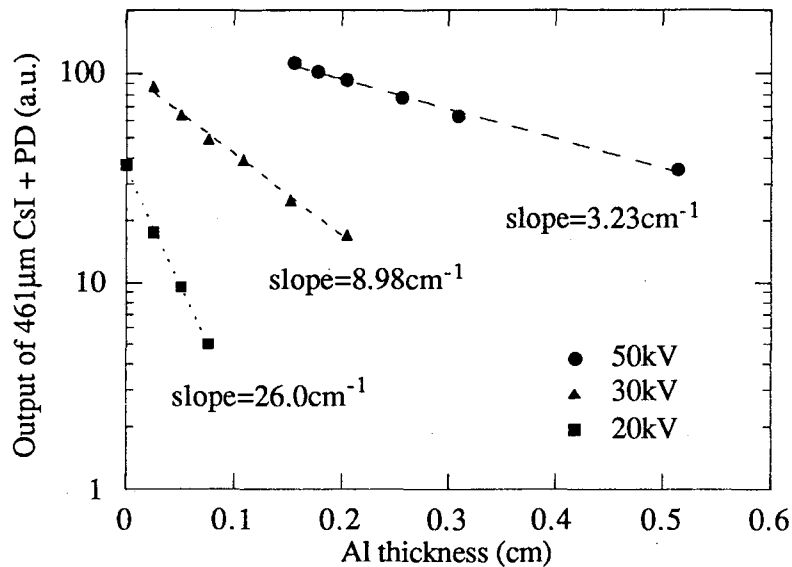


Fig.4-5 Absorption curve of Al for three kinds of X-ray spectra.

Table 4-2 Average energies and attenuation coefficients

HV Power Supply	[kV]	50	30	20
pulse width	[μ s]	1.5	3.0	5.0
μ_{Al}	[cm^{-1}]	3.23	8.98	26.0
E_a	[keV]	30.5	20.8	14.0
μ_{Si}	[cm^{-1}]	3.25	9.65	29.7
μ_{CsI}	[cm^{-1}]	45.3	119	322

Next, a 210- μ m-thick crystalline-Si radiation detector was placed in CH2 and the thickness of the Al absorber was fixed for each HV setting. The same X-ray pulses were generated as before and the output of the Si detector was recorded. Table 4-3 gives the calculated values of energy deposited in the detector. The energy deposited in the 210 μ m Si layer by a single X-ray pulse, denoted as E_d^{Si} , is given by the output of CH2 (expressed in terms of electrons) multiplied by the average energy needed to create an electron-hole pair in crystalline Si (3.6 eV). The energy deposited in a 461 μ m CsI layer by the same

single X-ray pulse, denoted as E_d^{CsI} , is obtained by multiplying E_d^{Si} by the ratio of the absorption probabilities.

Table 4-3 Energy deposition in Si and CsI

HV Power Supply	[kV]	50	30	20
Al thickness	[mm]	2.05	1.02	0.254
E_d^{Si}	[MeV]	0.758	0.855	0.984
E_d^{CsI}	[MeV]	10.1	4.96	2.11

Finally, a CsI(Tl) layer 461 μm thick coupled to the Hamamatsu photodiode (S1723-04) was placed in CH2 and was exposed to the same X-ray pulses. The Hamamatsu photodiode is about 200 μm thick and sensitive to the X-rays. Therefore, the signal from the photodiode can contain a contribution from electron-hole pairs created directly by the X-ray if the X-ray absorption in the scintillator is not 100%. This direct signal contribution can be measured by inserting a thin opaque sheet between the photodiode and a scintillator. The real scintillation signal was obtained by subtracting this direct contribution from the observed signal without the opaque sheet. The photodiode output (expressed in terms of electrons) divided by the energy deposition E_d^{CsI} gives the signal yield of this CsI and photodiode coupling. Table 4-4 lists these values. The CsI signal yields expressed in number of electrons/MeV measured by these three different X-ray spectra roughly agree. When the error analysis described in the next section is taken into account, the agreement is even better.

Table 4-4 CsI signal yield

HV Power Supply	[kV]	50	30	20
CsI Output	[e]	2.82×10^5	1.15×10^5	5.39×10^4
CsI Yield	[e/MeV]	2.79×10^4	2.48×10^4	2.55×10^4
Correction Factor		1.18	1.07	1.08
Cor. CsI Yield	[e/MeV]	2.37×10^4	2.32×10^4	2.36×10^4

4-3-3. Error analysis

The energy deposition in CsI is estimated from the measured energy deposited in a Si detector by assuming a continuous X-ray spectrum as a monochromatic spectrum. The error involved in this average energy analysis is treated here. First, assume some energy dependency on the initial X-ray spectrum $I_0(E)$. After passing through Al absorber of thickness t_{Al} , the spectrum becomes,

$$I(E, t_{Al}) = I_0(E) \exp(-\mu_{Al}(E) t_{Al}) \quad (4)$$

The energy deposited in a Si layer of thickness t_{Si} is written as,

$$E_d^{Si} = \int_0^{E_0} I(E, t_{Al}) E [1 - \exp(-\mu_{Si}(E) t_{Si})] dE \quad (5)$$

The energy deposited in a CsI layer of thickness t_{CsI} is given by,

$$E_d^{CsI} = \int_0^{E_0} I(E, t_{Al}) E [1 - \exp(-\mu_{CsI}(E) t_{CsI})] dE \quad (6)$$

Second, a simple delta function is assumed for a presumably narrow spectrum in the average energy analysis. The initial spectrum is replaced by,

$$I_0(E) = A \delta(E - \bar{E}) \quad (7)$$

Eq.(5) and (6) are then rewritten as,

$$E_d^{Si} = A \exp[-\mu_{Al}(\bar{E}) t_{Al}] [1 - \exp(\mu_{Si}(\bar{E}) t_{Si})] \quad (8)$$

$$E_d^{CsI} = A \exp[-\mu_{Al}(\bar{E}) t_{Al}] [1 - \exp(\mu_{CsI}(\bar{E}) t_{CsI})] \quad (9)$$

Therefore, the ratio of energy depositions in two layers is simply the ratio of the absorption probability in each layer at the average energy.

What determines the error involved in replacing eq.(5) and (6) by eq.(8) and (9), respectively? The obvious factor is the "narrowness" of the spectrum. If the spectrum is as sharp as the delta function, eq.(8) and (9) are exact. If not, the error depends on the attenuation coefficients of the materials in addition to the sharpness of the spectrum. If the coefficients are similar in both materials, the whole spectrum is equally absorbed and the error is small. If there is a big difference in coefficients as in the case of Si and CsI, the

whole spectrum is not equally absorbed. CsI 460 μm thick absorbs almost everything except a small portion of the high energy tail in a 50 kV X-ray spectrum. Si 210 μm thick absorbs only a small fraction of the spectrum and the energy absorbed is mostly due to the lower energy portion of the spectrum.

To express the error involved in the average energy analysis, a correction factor is defined as the ratio of the exact estimate of $E_d^{\text{CsI}} / E_d^{\text{Si}}$ (eq.(6) over eq.(5)) to the approximate estimate of $E_d^{\text{CsI}} / E_d^{\text{Si}}$ (eq.(9) over eq.(8)). The initial spectrum must be known to perform integration in eq.(5), (6). Assume the initial X-ray spectrum of the following form.

$$I_0(E) = A \left(\frac{1}{E} - \frac{1}{E_0} \right) \quad (10)$$

The thickness of Si and CsI used in this analysis are 210 μm and 461 μm , respectively. The thickness of Al is adjusted so that the average energy absorbed in Al as calculated below becomes equal to the energy determined by the experiment. The term $I(E, t_{\text{Al}}) * E$ and the terms in the integrals in eq.(5) and (6) are plotted in Fig.4-6. The discontinuity in the CsI curve is due to the K edge absorption of Cs and I, which are around 34 keV. The centroid of the CsI curve is shifted to right compared to that of the Si curve, namely the average energy absorbed in CsI is higher than that in Si. The areas under the CsI and Si curves give the energy deposited in each material and the ratio is 16.28.

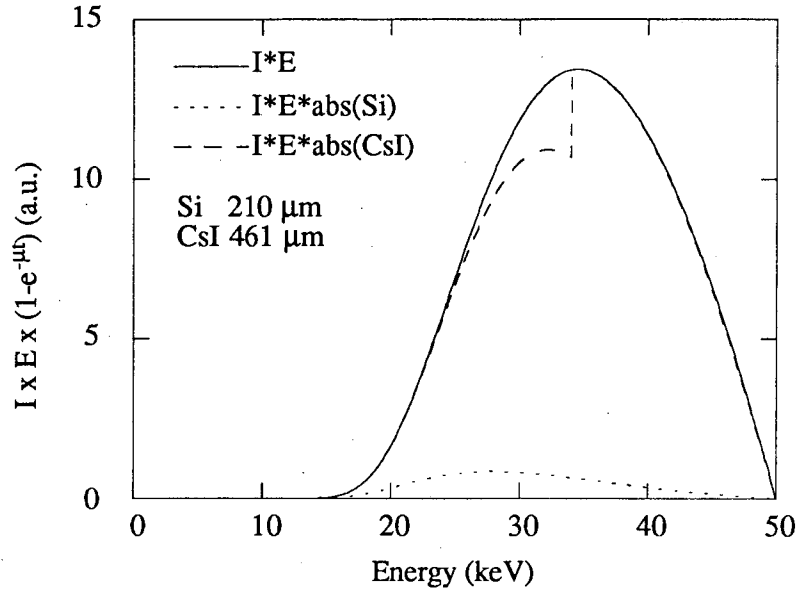


Fig.4-6 Absorption of X-ray spectrum by CsI and Si.

When we use the average energy model, the equivalent ratio is calculated as follows. The average energy is determined by the Al absorption curve. The following equation is used to calculate the average energy.

$$\bar{E} = \frac{\int_0^{E_0} I(E, t_{Al}) E [1 - \exp(-\mu_{Al}(E) t_{Al})] dE}{\int_0^{E_0} I(E, t_{Al}) [1 - \exp(-\mu_{Al}(E) t_{Al})] dE} \quad (11)$$

Its value for this example is adjusted to 31.1 keV. The average energies deposited in Si and CsI are calculated in a similar way and these are 29.0 keV and 33.3 keV, respectively. Based on this average energy determined by Al, the ratio of energy deposited in CsI and Si is calculated from eq.(8) and (9). This value is 13.77 and the correction factor becomes 1.18.

4.4. Light yield optimization

4-4-1. Light yield of CsI(Na)

The light yields from the 300 μ m thick CsI(Na) layers evaporated at LBL and from a single crystal CsI(Tl) made by Bicron (AI-409) are compared. AI-409 is a 5 mm thick 5 mm diameter cylinder-shaped crystal encapsulated by an Aluminum housing with 500 μ m thick window. The table below shows the signal size from the photodiode coupled to these scintillators and exposed to X-ray pulses. The observed signal size measured in the photodiode is S_m . This is divided by E_d^{CsI} to give the total electron yield per unit energy absorbed.

Table 4-5 Light yield of some evaporated CsI(Na)

CsI type	E_d^{CsI} (MeV)	S_m (electrons)	S_m/E_d^{CsI} (electrons/MeV)
CsI(Na) #120489	9.9	8.6×10^4	8.7×10^3
CsI(Na) #121289	9.9	8.9×10^4	9.0×10^3
CsI(Tl) AI-409	7.1	1.5×10^5	2.1×10^4

A handling problem exists for an evaporated CsI(Na) layer. It absorbs moisture upon contact with air and its light yield decreases with time. Scintillation stopped completely after it was left in air for a few days. Sealing it with an Al housing in an Ar-filled glove box after a short exposure to air (< 1 min.) retarded the degradation but still its light yield decreased to a half value in a week or so. A mild baking in air (200°C for 2 hours) recovered light yield for unsealed layers but not for the sealed layers. Due to this hygroscopic nature of CsI(Na), optimization of the CsI(Na) evaporation process was not successful. Optimization of the CsI(Tl) evaporation process was carried out because CsI(Tl) is much less hygroscopic.

4-4-2. Light yield of CsI(Tl)

The light yields of three crystalline CsI(Tl) samples supplied from Bicron and a number of evaporated CsI(Tl) layers were evaluated with the 50 kV X-ray pulse. A thin Al sheet covers the top surface of CsI(Tl) layers so that it reflects back the scintillation light. The results are summarized chronologically in Fig.4-7, showing the evolution of signal yield with time. More detailed data for some evaporated layers are tabulated in the Appendix at the end of chapter.

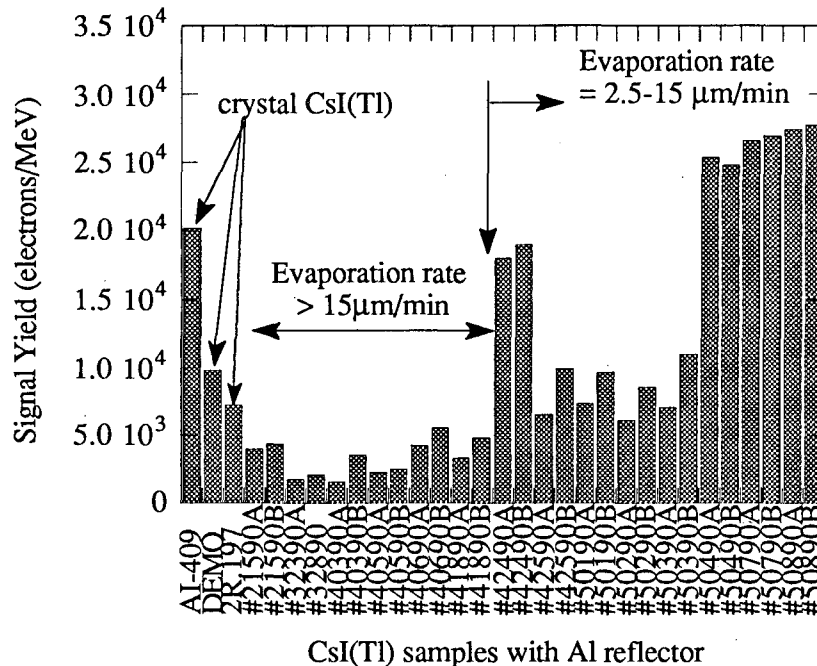


Fig. 4-7 Signal size per unit energy absorbed in a number of CsI(Tl) samples (their ID #'s are also production dates in most cases).

The first three (AI-409, DEMO, 2R.197) are single crystal CsI(Tl) supplied by Bicron. AI-409 is the one described above. 2R.197 is a 5 mm thick 5 cm diameter disk of CsI(Tl) encapsulated by an aluminum housing with 500 μm window. DEMO has the same dimensions with 2R.197 but its 5 mm CsI(Tl) disk is cut by a wire-saw into 2 mm x 2 mm columns and the gaps between columns are filled with reflector materials before encapsulation. The thickness of evaporated layers ranges from 100 μm to 550 μm.

As for the three single crystal CsI(Tl) samples, it is possible that the lateral light spread inside the crystal resulted in the smaller signal size for DEMO and 2R.197. Among the evaporated CsI(Tl) layers, the first twelve samples were evaporated at a rate faster than 15 $\mu\text{m}/\text{min}$. The last sixteen samples were evaporated at a well-controlled constant rate (about 2.5-15 $\mu\text{m}/\text{min}$) and the source crystal was large enough so that there was some residual material left in the boat at the end of a run. Samples labeled as A were centered against the boat and two to three times thicker than the ones labeled as B, which were off-centered. The evaporation rate is based on the thicker "A" samples. The signal size for these layers is increased by as much as about 50% by putting a thin aluminized mylar sheet as a reflector on top of the CsI(Tl) layers.

The light yield of CsI(Tl) is sensitive to the evaporation conditions, especially the boat temperature which determines the evaporation rate. CsI(Tl) layers 230-460 μm thick were evaporated at different boat temperature, and hence different evaporation rates and signal yields were evaluated with the 50kV X-ray. The results are shown in Fig.4-8. A more detail data for some evaporated layers are tabulated in Appendix at the end of chapter.

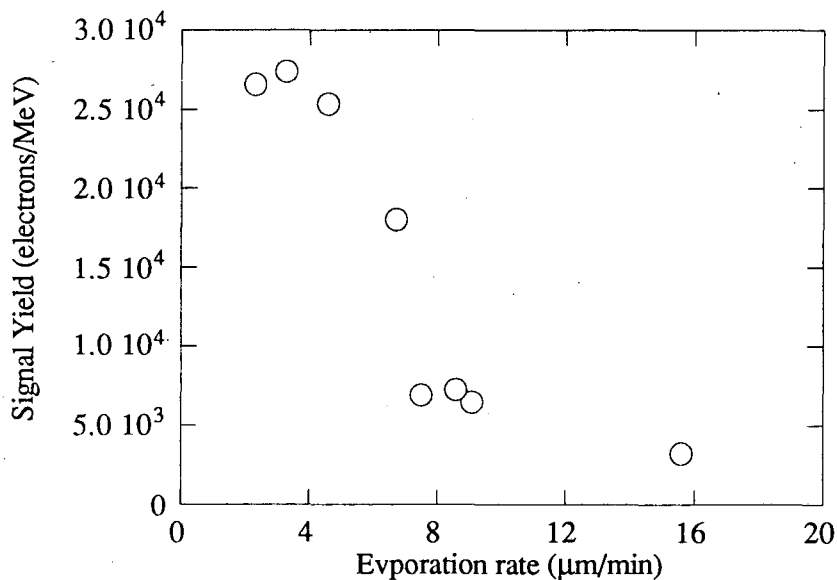


Fig. 4-8 Signal size per unit energy absorbed in CsI(Tl) evaporated at different constant rates. The rate is based on the samples labeled as A which were centered against the boat.

In the temperature range around the melting point of CsI (620 °C), the vapor pressure of TlI is about four order of magnitude higher than that of CsI whereas NaI vapor pressure is almost the same as CsI [5]. This suggests that the poor scintillation quality of most of the early samples in Fig.4-7 was likely due to depleting Tl at the later part of a run. However, with optimum preparation, the light yield of evaporated CsI(Tl) layers can be made close to a single crystal CsI(Tl) as demonstrated by some samples made at later time.

4-4-3. Tl concentration in an evaporated CsI(Tl)

The Tl concentration is known to control the light yield in crystalline CsI(Tl) [11]. The Tl concentrations in our evaporated layers were evaluated by the X-ray Reflection Fluorescence analysis (XRF) [12]. The results are plotted in Fig.4-9 against their signal yields from the X-ray measurements. The same behavior is seen for our evaporated layers and the crystalline CsI(Tl) in ref.[11]. It is confirmed that the layers evaporated at a slower rate contains more Tl than those made at a faster rate. The XRF measurement showed that the CsI(Tl) layers evaporated at slow rate contained 20-30 % less Tl than their source CsI(Tl) crystal. A more detail data for some evaporated layers are tabulated in Appendix at the end of chapter.

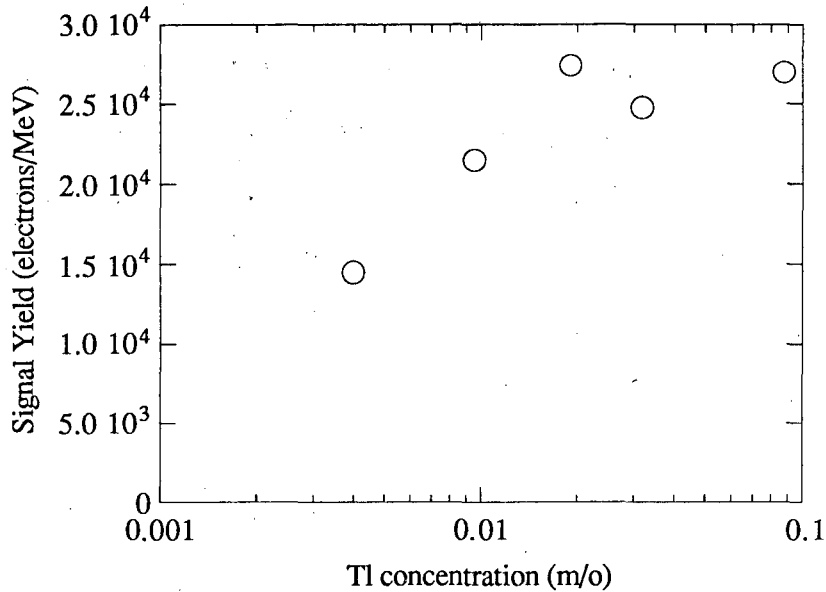


Fig.4-9 Signal size per unit energy absorbed in CsI(Tl) evaporated at different Tl concentrations as measured by XRF.

4-5. Speed of CsI(Tl) and Gd₂O₂S screens

The scintillation yields of our evaporated layers were characterized in the previous section. How fast scintillation decays is another important property of a scintillator. Although the decay constant of crystalline CsI has been well characterized, it was of our interest to confirm that our evaporated CsI(Tl) has a similar decay constant (about 1 μ s) with its crystal counterpart.

There is another type of scintillator that has recently used in conjunction with a-Si:H photodiodes. Hitachi has developed a compound of Gd₂O₂S by introducing doping atoms such as Pr, Ce and F and reported that its light yield is comparable to CsI(Tl) and that its scintillation decays to 1/10 of the initial level in 5 μ s [13]. Lanex (Gd₂O₂S:Tb) made by Kodak is an intensifying screen for X-ray films. Although 1-2 μ m thick a-Si:H photodiodes coupled to Lanex have been shown to give a 100-330 times larger signal size for detecting X-rays used in medical imaging compared to the a-Si:H diode alone [14,15], neither the absolute light yield nor the decay constant of Lanex were quoted and Lanex is

known to have a long decay. How these two compounds of Gd_2O_2S compared with our evaporated layer was interest to us.

The decay constant and light yield of a scintillator can be obtained by analyzing the charge-sensitive preamplifier output in the setup described in Ch.4-2 when the scintillator coupled to a photodiode is irradiated by a short X-ray pulse. The following experiment and analysis were conducted to compare these quantities of our evaporated CsI(Tl) layers with those of Gd_2O_2S screens.

4-5-1. Theoretical analysis

A scintillator may have multiple scintillation components each with its own decay constant, corresponding to a particular carrier transition between energy levels. It is often sufficient, however, to treat the scintillation yield as a single exponential decay with a characteristic time τ , namely, $Y(t) = Y_0 \exp(-t/\tau)$. We expect that the decay constants of CsI(Tl) and the Hitachi scintillator are comparable with the minimum setting of our X-ray pulse ($T_0=1-2 \mu s$), whereas, the RC decay constant of our charge-sensitive preamplifier ($RC = 1 \text{ ms}$) is comparable to the decay constant of Lanex. Since different assumptions will be made in the analysis, these are treated separately.

Case I. $\tau \approx T_0 \ll RC$ (CsI(Tl) and the Hitachi scintillator)

The RC discharge of the charge stored in the feedback capacitor of a charge-sensitive preamplifier can be neglected for $\tau \ll RC$. But the X-ray generation cannot be regarded as instantaneous.

The X-ray pulse of duration Δt_0 at $t=t_0$ ($0 \leq t \leq T_0$) yields scintillation light expressed as

$$Y_0 \exp\left(-\frac{t-t_0}{\tau}\right) \Delta t_0 \quad (12)$$

This light creates charge in the external circuit $\Delta Q(t, t_0)$ given by,

$$\Delta Q(t, t_0) = A \int_{t_0}^t Y_0 \exp\left(-\frac{t'-t_0}{\tau}\right) dt' \Delta t_0 \quad (13)$$

where, A is the factor involving the light collection and quantum efficiencies.

The total charge created by the X-ray pulse of duration T_0 is expressed as,

$$Q(t) = \int_0^t \Delta Q(t, t_0) dt_0 \quad \text{for } 0 \leq t \leq T_0 \quad (14)$$

$$Q(t) = \int_0^{T_0} \Delta Q(t, t_0) dt_0 \quad \text{for } T_0 \leq t \quad (15)$$

These integrals are performed to give the following results.

$$Q(t) = Y_0 \tau^2 \left\{ \frac{t}{\tau} - 1 + \exp\left(-\frac{t}{\tau}\right) \right\} A \quad \text{for } 0 \leq t \leq T_0 \quad (16)$$

$$Q(t) = Y_0 \tau^2 \left[\frac{T_0}{\tau} - \exp\left(-\frac{t}{\tau}\right) \left\{ \exp\left(\frac{T_0}{\tau}\right) - 1 \right\} \right] A \quad \text{for } T_0 \leq t \quad (17)$$

The total charge observed at infinite time is given by,

$$Q_0 = Y_0 \tau T_0 A \quad (18)$$

A dimensionless expression for the preamplifier output is obtained by dividing above expressions by Q_0 .

Case II. $T_0 \ll \tau \approx RC$ (Lanex)

In this case, the X-ray generation is assumed as instantaneous but the RC decay of the charge stored in the feedback capacitor cannot be neglected. The preamplifier output is given by the scintillation decay function $Y(t) = Y_0 \exp(-t/\tau)$ convolved by the response function of the charge-sensitive preamplifier $\exp(-t/RC)$, namely,

$$Q(t) = \int_0^t Y_0 \exp\left(-\frac{t_0}{\tau}\right) \exp\left(-\frac{t-t_0}{RC}\right) dt_0 \quad (19)$$

Therefore,

$$Q(t) = Y_0 \tau' \left[\exp\left(-\frac{t}{RC}\right) - \exp\left(-\frac{t}{\tau}\right) \right] \quad (20)$$

where,

$$\frac{1}{\tau'} = \frac{1}{\tau} - \frac{1}{RC} \quad (21)$$

It should be noted that eq.(20) becomes a simple exponential form with a decay constant RC in the limit of $\tau \ll RC$. This is the case for the CsI(Tl) layer with $\tau = 1 \mu\text{s}$ and RC approximately 1 msec.

4-5-2. Measurement

The same setup as shown in Fig.4-4 was used for this experiment except for that the charge-sensitive preamplifier (Tennelec TC170) output was directly fed into a digital storage oscilloscope (Tektronix 2430) and that digitized wave forms were analyzed by a personal computer. A crystalline Si radiation detector and the Hamamatsu photodiode (S1723-04) are placed in CH1 and CH2, respectively. A scintillator (either $\text{Gd}_2\text{O}_2\text{S:PrCeF}$, Lanex or an evaporated CsI(Tl) layer) was placed on top of S1723-04 and was exposed to the $1.4 \mu\text{s}$ 50 kV X-ray pulses as before (summarized in Table 4-2). The repetition rate of the X-ray pulses were kept low to avoid pile up of output wave forms, which were stored and analyzed by a personal computer.

First, the Hitachi scintillator 1.1 mm thick and our evaporated CsI(Tl) #52490A $461 \mu\text{m}$ thick were compared and their waveforms are shown in Fig.4-10 together with the signal from the c-Si diode alone. Both scintillators absorbed all the X-ray energy. The linear portion of the c-Si signal extends about $1.2 \mu\text{s}$, confirming the duration of a constant X-ray intensity. The asymptotic levels for these scintillators roughly agree and equivalent to 2.5×10^4 electrons/MeV. The Hitachi scintillator is slightly slower than the CsI(Tl). The decay constants will be given in the next section.

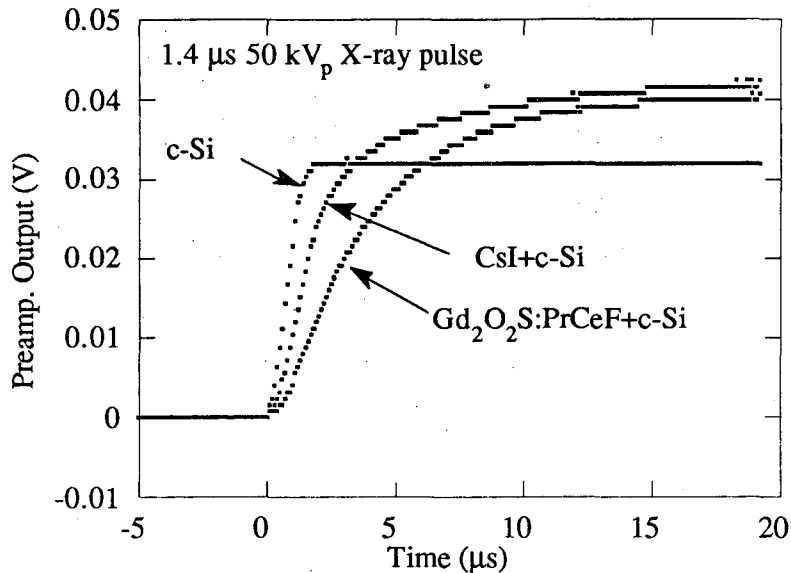


Fig.4-10 Preamplifier output for the CsI(Tl) layer and the Hitachi screen.

Second, Lanex and our CsI(Tl) layer (#50890A) were compared. The Lanex screen was about 300 μm thick and mounted on a thin plastic plate. The CsI(Tl) was 315 μm thick and were evaporated on 1 mm thick glass. Unless a scintillator absorbs all the X-ray energy, the signal contains a contribution from direct electron-hole pair creation by the penetrating X-rays. This is the case for a 300 μm thick Lanex screen and the direct signal contribution was measured as previously by placing a thin opaque sheet between a scintillator and S1723-04. The resultant wave forms were subtracted from the wave forms observed without the opaque sheet. The average energy of the X-ray is 30 keV and the incoming energy contained in a pulse is equivalent to 10 MeV. Under these conditions, the direct contribution was about 20% of the total charge collected by the photodiode in the case of the Lanex screen and 10% for the CsI(Tl) layer. The wave forms after subtraction of the direct contributions are shown in Fig.4-11. The decay constant RC is about 1 ms and this can be determined more accurately from the CsI(Tl) signal as shown below. The Lanex signal shows a peak and then decays more slowly than the CsI signal. This is

because the scintillation characteristic time is comparable with the decay constant of the preamplifier.

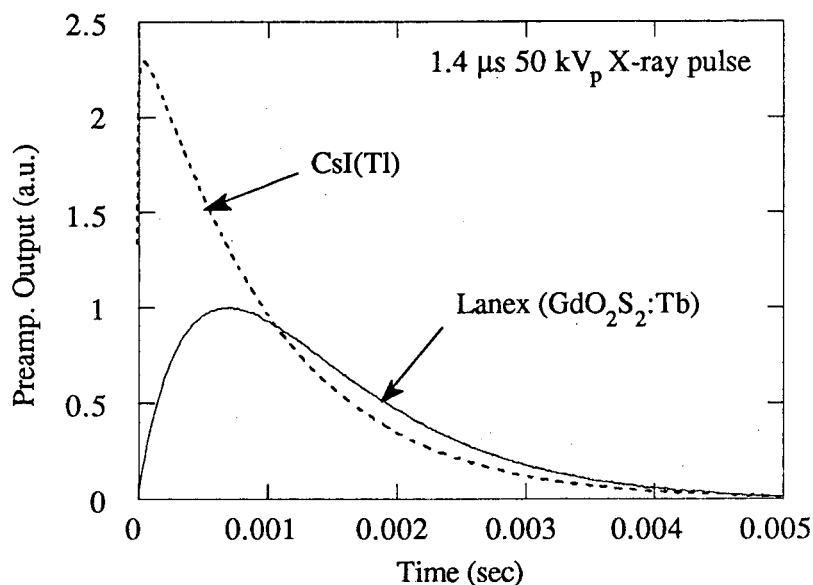


Fig.4-11 Preamplifier output for the CsI(Tl) layer and Lanex. Both curves were normalized against the peak value of the Lanex data.

4-5-3. Analysis

First, the wave forms in Fig.4-10 were normalized against their asymptotic values to give dimensionless output forms and their initial portions during the X-ray pulse ($0 \leq t \leq T_0$) were fitted with the theoretical expression. The decay constant τ is the only fitting parameter used and results with three different τ are plotted in the Fig.4-12 for the two scintillators. The best fit is given by $\tau = 1.1 \mu\text{s}$ for the CsI(Tl) and $\tau = 3.5 \mu\text{s}$ for the Hitachi scintillator, respectively. These values agree well with published data.

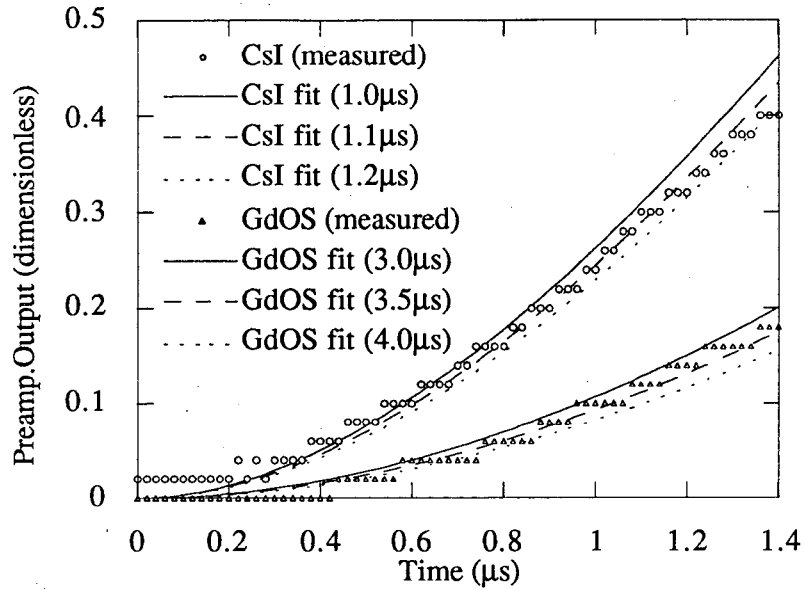


Fig.4-12 Dimensionless preamplifier output fitted with the theoretical expression.

Second, the Lanex data were analyzed. Fitting the exponential tail of the CsI(Tl) signal gives the RC decay constant of 1.06 ms. The Lanex signal can be fitted by eq.(20) using τ as the only fitting parameter as shown in Fig.4-13.

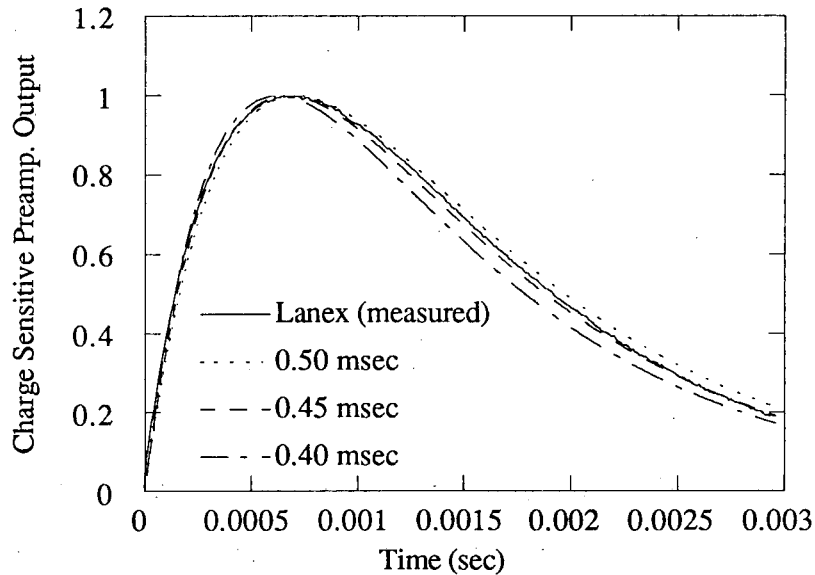


Fig.4-13 Fitting the Lanex signal by the analytical expression for the preamplifier output.

Since the wave form is well described by eq.(20), the peak value (t_{\max} , Q_{\max}) can be used to calculate τ for Lanex. The expression for these are given by,

$$\tau = RC \exp \left(- \frac{t_{\max}}{\tau} \right) \quad (22)$$

$$Q_{\max} = Y_o \tau \left(\frac{\tau}{RC} \right) \frac{\tau}{RC} \quad (23)$$

From the peak value t_{\max} of 0.700 msec for Lanex, the decay constant of 0.48 msec is obtained by solving eq.(23) graphically.

The total light yield is given by

$$\int_0^{\infty} Y_o \exp \left(- \frac{t}{\tau} \right) dt = Y_o \tau \quad (24)$$

The ratio of the total light yield of CsI(Tl) to that of Lanex is calculated by eq.(23) and eq.(24) from the peak values Q_{\max} of both wave forms. This ratio is 1.19.

In conclusion, it has been shown that the evaporated CsI(Tl) has a total light yield equivalent to the Hitachi scintillator and Lanex and that its decay constant is 1.1 μ s whereas those of Hitachi scintillator and Lanex are 3.5 μ s and 480 μ s, respectively.

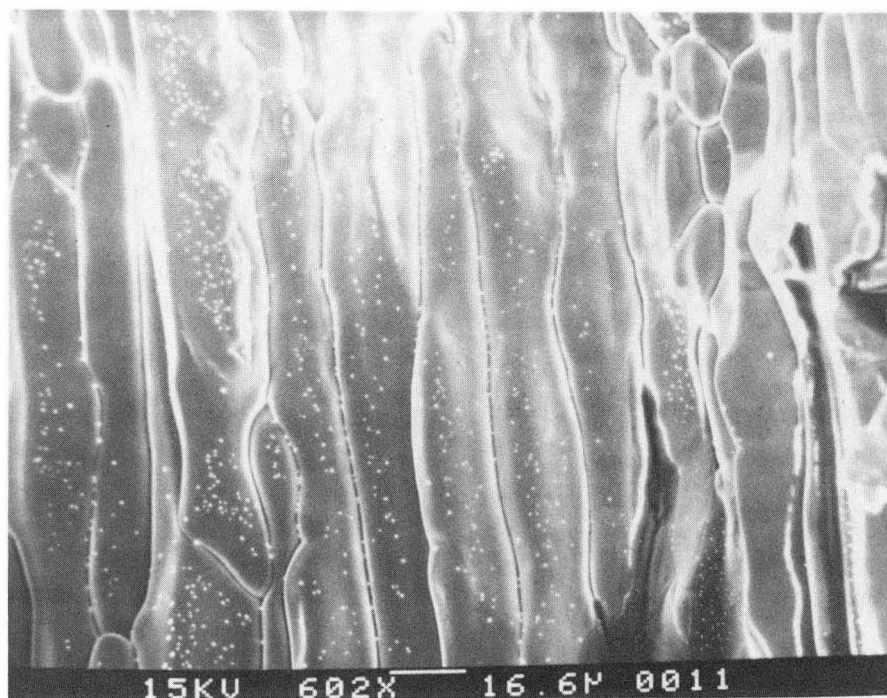
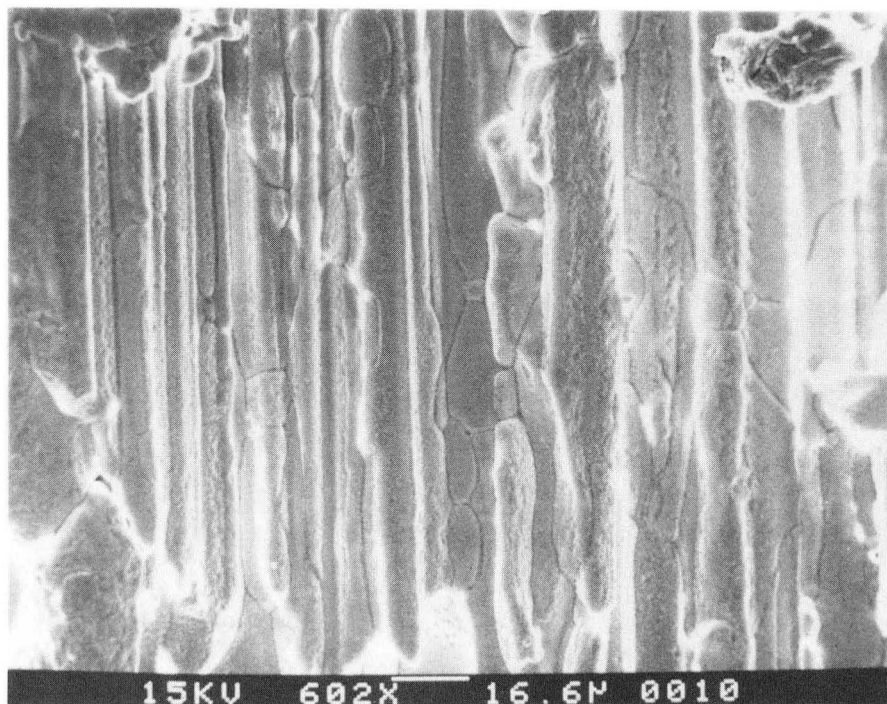
4-6. Spatial resolution measurement

The columnar structure in evaporated CsI(Na) layers [6] was confirmed by a Scanning Electron Micrograph (SEM) of a cracked surface of the layer. Light spread inside a CsI(Na) layer was characterized qualitatively by taking a photograph of a reflected light pattern when the top surface of the CsI(Na) was irradiated by a narrow light beam [6]. A similar structure and light spread were expected from our evaporated CsI(Tl) and the following two experiments were conducted to confirm these points.

First, the columnar structure of evaporated CsI(Tl) layers and the effect of the substrate temperature on the structure were checked by a SEM. A small piece of the evaporated layers were cut with a razor blade and the cracked surfaces were observed with a SEM. The two SEM photographs shown in Fig.4-14 clearly show the columnar structure. The

substrate temperature during the evaporation was 100 °C for Fig.4-14(a) and 200 °C for Fig.4-14(b). The evaporation parameters for the 100 °C layer were optimized for maximum light yield. Only the substrate temperature was raised to 200 °C for the layer shown in Fig.4-14(b). The columnar diameter of the 100 °C layer is about 10 μm whereas that of the 200 °C layer is about 15-20 μm. The light yield of the 200 °C layer was almost the same as the 100 °C layer.

Fig.4-14 SEM photograph of cracked surfaces of CsI(Tl) layers evaporated at substrate temperature of 100 °C (a) and 200 °C (b).



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Second, light spread inside an evaporated CsI(Tl) layer as well as the Hitachi scintillator ($Gd_2O_2S:PrCeF$) and Lanex screen ($Gd_2O_2S:Tb$) used before were characterized by the setup shown in Fig.4-15. The slit was placed as close as possible to the surface of the scintillator and the distance between the slit and the scintillator surface was less than 1 mm in all the scintillators tested.

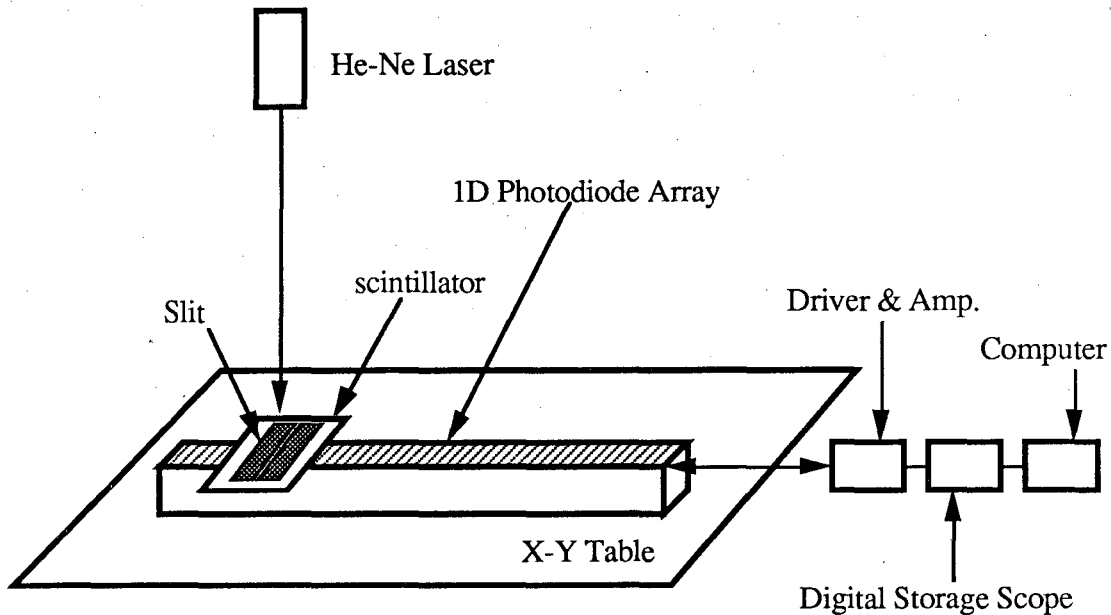


Fig.4-15 Experimental setup for the light spread measurement.

A facsimile head module made by SEIKO-EPSON (linear a-Si:H photodiode array with a TFT attached to each photodiode) had 400 dpi resolution [16]. The external circuit was the standard readout electronics designed for the facsimile application and it gave a sequence of voltage pulses, each pulse height corresponding to a charge produced by each photodiode element. This pulse sequence was digitized and stored by the Tektronix 2430 scope and then transferred to a computer for analysis.

First, the photodiode array was exposed to a parallel light beam from a He-Ne Laser through a narrow slit without a scintillator between the slit and the array. The slit opening was set to $50 \mu m$ so that only one pixel of the linear photodiode array gave a signal. The position of the array was adjusted such that the signal becomes maximum. Second, a

scintillator (either a 425 μm , 894 μm evaporated CsI(Tl) layer or one of the two $\text{Gd}_2\text{O}_2\text{S}$ compounds used before) was placed on the photodiode array and the Laser beam was used to expose the scintillator and the array. The intensity of the beam was adjusted by an optical attenuator sheet so that the signal was not saturated. An example of the output pulse sequence is shown in Fig.4-16.

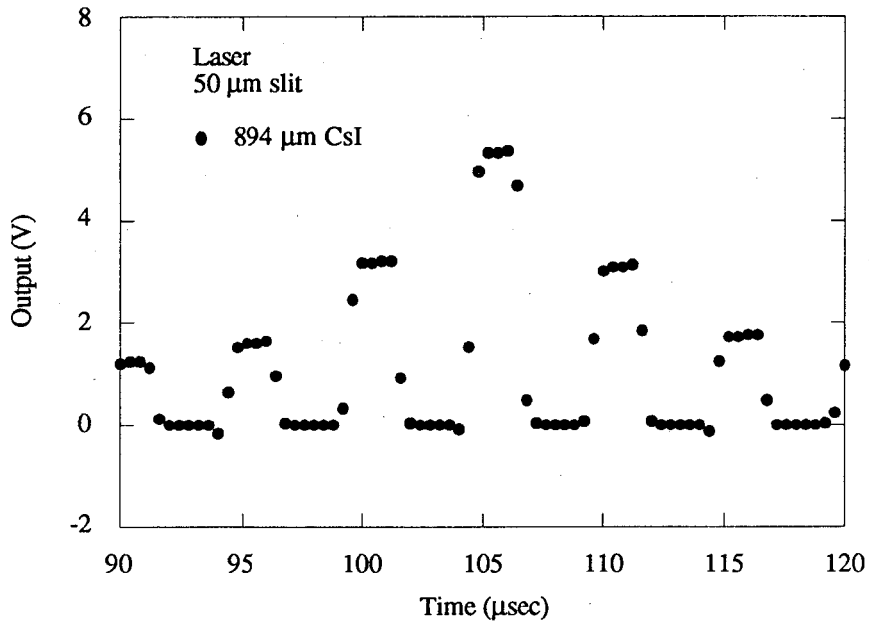


Fig.4-16 Digitized pulse sequence obtained by the setup shown in Fig.4-15 with a 894 μm thick CsI(Tl) layer.

The flat portions of the pulse sequence in Fig.4-16 corresponds to the time when a thin film transistor (TFT) attached to every photodiode element is turned on or off. Notice that the switching transient exists as can be seen as a small distortion from an ideal rectangular pulse. For the Laser light, this distortion is negligible compared to the signal charge. Therefore, a line spread function can be obtained by sampling one representative data point from each flat portion of the pulse sequence corresponding to the time when the TFT is turned on. The time was converted into position by using the known pitch of the photodiode array. A normalized line spread function was obtained this way for each scintillator and is shown in Fig.4-17 and Fig.4-18.

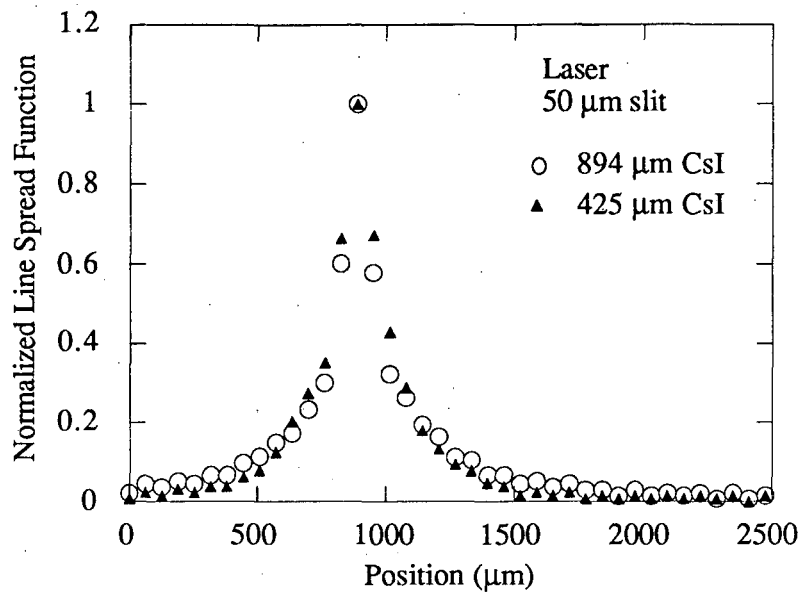


Fig.4-17 Line spread functions for CsI(Tl) layers obtained with the Laser light beam.

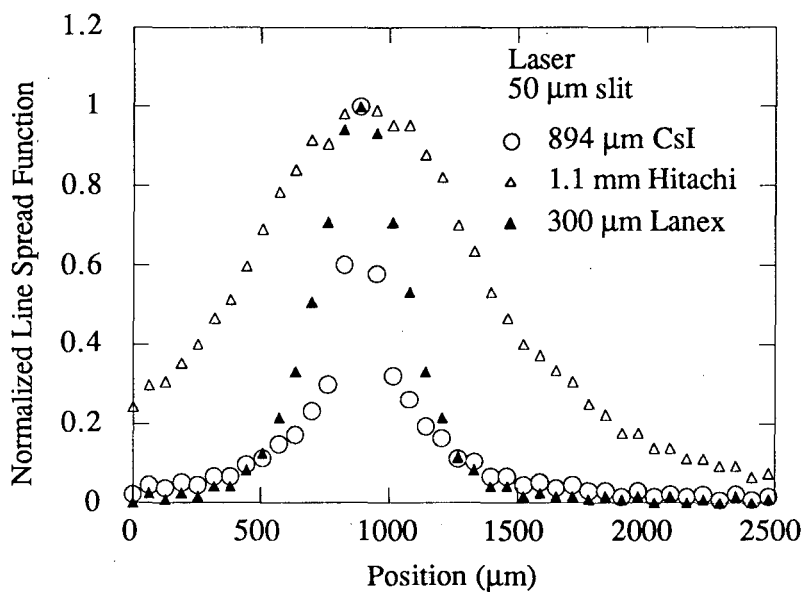


Fig.4-18 Line spread functions for the CsI(Tl) and the two Gd_2O_2S compounds obtained with the Laser light beam. The spatial resolution of the 894 μm thick evaporated CsI(Tl) layer is about 200 μm FWHM, whereas those of the Gd_2O_2S compounds, are 400 μm FWHM for the 300 μm thick Lanex screen and 1 mm FWHM for the 1.1 mm thick Hitachi scintillator.

Spatial resolution is often expressed in terms of a full width at half maximum (FWHM) of a line spread function. The thicknesses and the approximate resolutions of all the scintillators tested here are summarized in Table 4-6.

Table 4-6 Spatial resolution measured by Laser light

Scintillator	Thickness	spatial resolution
evaporated CsI(Tl)	425 μm	200 μm FWHM
evaporated CsI(Tl)	894 μm	200 μm FWHM
Hitachi ($\text{Gd}_2\text{O}_2\text{S}:\text{PrCeF}$)	1.1 mm	1 mm FWHM
Lanex ($\text{Gd}_2\text{O}_2\text{S}:\text{Tb}$)	300 μm	300 μm FWHM

This comparison of scintillators is not a clean quantitative one because the thickness of each scintillator is not the same, but it is clearly indicated that the light spread inside the evaporated CsI(Tl) layer is suppressed by its columnar structure.

A similar experiment was conducted with an X-ray beam instead of the Laser. The 425 μm thick CsI(Tl) layer coupled to the linear a-Si:H photodiode array was exposed to 50 kV X-ray (DC) through a 75 μm tungsten slit. Now the switching transient is larger than the photodiode signal size and the output pulse sequence looks very different as shown in Fig.4-19. The open circles show the pulse sequence when the X-ray beam was on and the solid circles show the switching transient only when the X-ray beam was off. Notice that the vertical scale of this figure is 1/1000 of Fig.4-16. Apparently there are two alternating patterns of transient in Fig.4-19. This is presumably due to the subtle difference in the two alternating readout lines which are implemented to increase the readout time [16]. Since these switching transient exist systematically independent of whether the X-ray beam is on or off, it can be subtracted from the observed X-ray signal to give a clearer picture of a line spread function. A line spread function shown in Fig.4-20 was obtained by averaging the difference between the two pulse sequences during the TFT ON periods and by converting time into position.

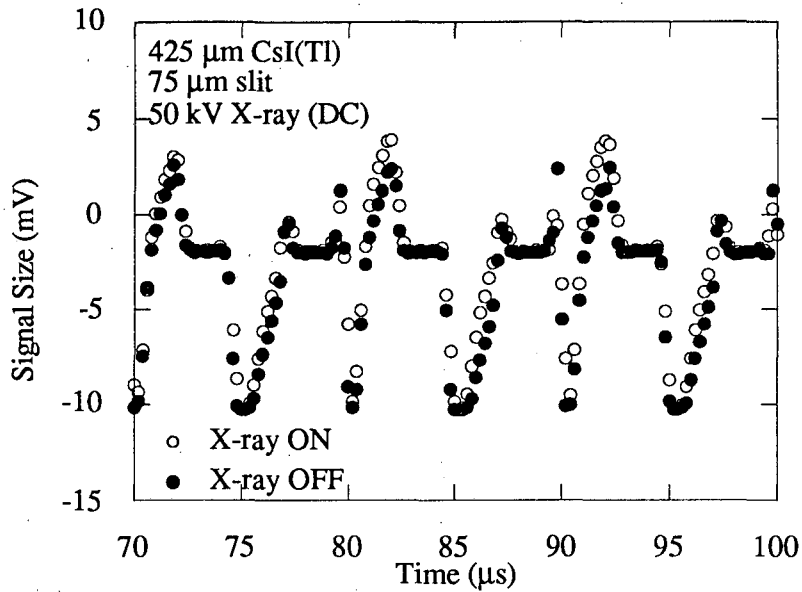


Fig.4-19 Digitized pulse sequence obtained with the X-ray beam on and off.

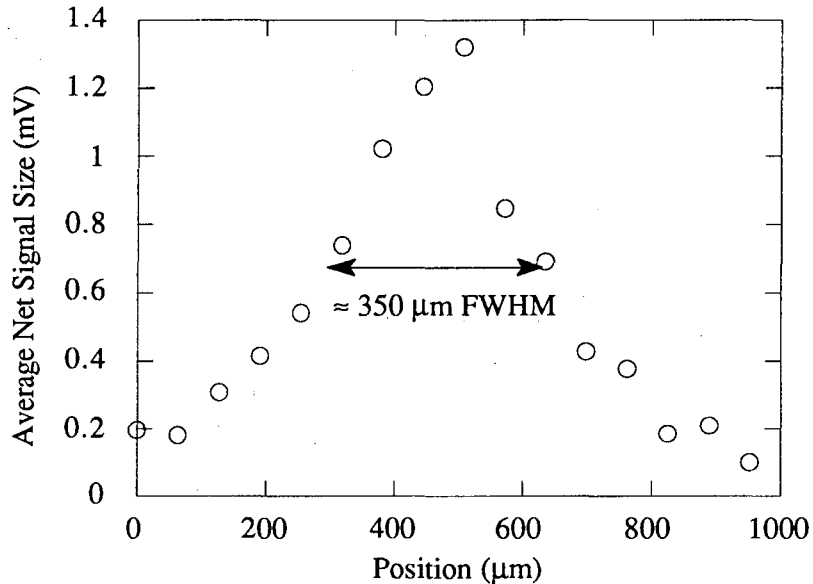


Fig.4-20 Line spread functions for the CsI(Tl) obtained by average difference analysis of Fig.4-19.

A discrepancy between the Laser measurements and the X-ray measurement comes from the fact that the scintillation light is emitted isotropically. Total reflection occurs on the columnar surface if the incident angle of the light is larger than the critical angle. This is

calculated to be 34° for the CsI and air boundary [6]. Some of the scintillation light is trapped inside the scintillator by internal reflection. Most of the light (83%) [6] come out from the CsI layer by successive reflection but the direction of this light at the end of the columns can make an angle as much as 34° with respect to the column direction of the CsI layer. On the other hand, the Laser light is incident on the scintillator normally and a large angle scattering on the columnar surface is unlikely, leading to a narrower light spread.

4-7. Radiation damage to an evaporated CsI(Tl)

A relatively thin evaporated CsI(Tl) (#50890B 125 μm thick) was exposed to γ rays from a strong Co-60 source at the irradiation facility at LBL. The nominal activity of the Co-60 source is 6000 Ci. The CsI(Tl) sample was held at 10-30 cm away from the source and left for various time periods required for each dose. The signal size was measured with and without a reflector after each exposure and plotted in Fig.4-21. Also shown in the figure are the data for 1 cm thick crystal CsI(Tl) taken from Ref.[17]. Each signal size is normalized against its zero dose level.

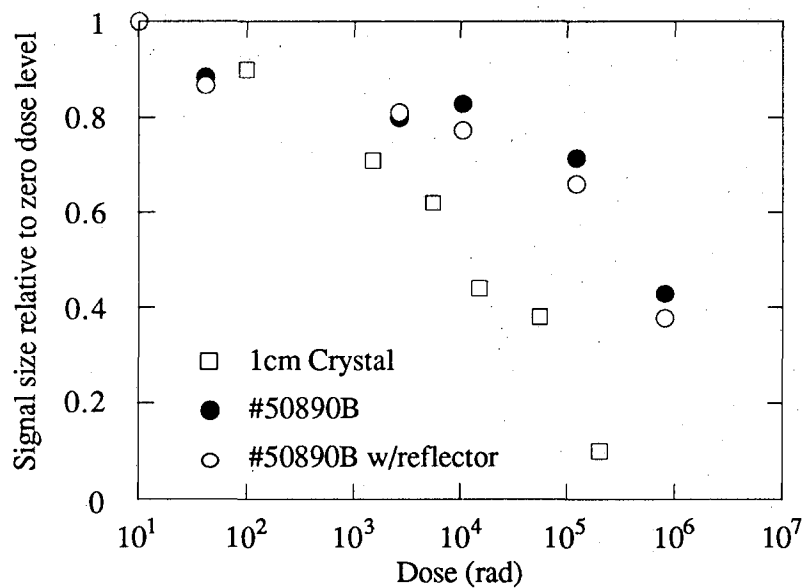


Fig.4-21 Light yield decrease due to radiation damage for a thin CsI(Tl) layer.

The dose required for 60 % signal loss is about 10^{+6} rad for the evaporated layer, which is about 50 times more than the dose required for the 1 cm crystal sample. The signal loss is systematically larger when the light collection is improved by the reflector. This suggests that the radiation damage changed transmission properties of the scintillator, in agreement with a more detailed study of the crystal CsI(Tl) [17].

4-8. X-ray and β particle detection with CsI coupled to a photodiode

4-8-1. X-ray detection

The direct and indirect detection of X-ray pulses was compared using a $10\ \mu\text{m}$ a-Si:H p-i-n diode and a $233\ \mu\text{m}$ thick evaporated CsI(Tl) layer. The structure of the diode was $0.8\ \text{mm}$ glass / SnO_2 / $30\ \text{nm-p+}$ / $10\ \mu\text{m-i}$ / $30\ \text{nm-n+}$ / Cr and its top Cr contact was $3 \times 3\ \text{mm}^2$ square. The open circles in Fig.4-22 show the pulse height spectrum for direct X-ray detection without a scintillator when this a-Si:H p-i-n diode was reverse-biased at 100 V and exposed to the collimated 50 kV X-ray beam directly. The average energy of X-ray photons was 30keV. The total energy contained in a single pulse was calibrated to be 22 MeV. Next the glass side of this diode was coupled to the glass substrate of a $233\ \mu\text{m}$ thick CsI(Tl) (#42490). The pulse height spectrum of the signal for the same X-ray

exposure to the scintillator is shown by solid circles in Fig.4-22.

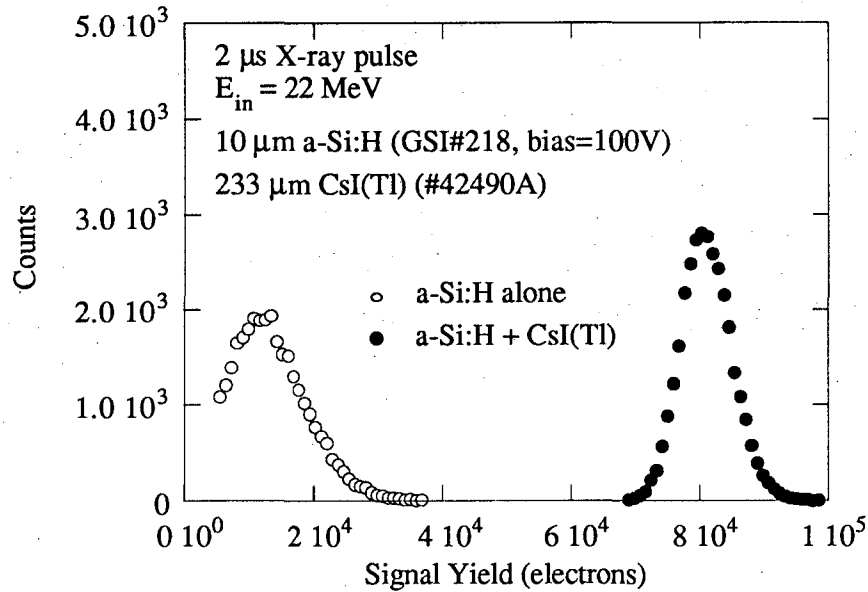


Fig.4-22 Detection of X-ray by a 10 μm thick a-Si:H diode with and without a 233 μm thick CsI(Tl).

The direct signal size of about 1.2×10^4 electrons is consistent with what we expect from W (the average energy to create an electron-hole pair) of 5 eV. As for the scintillation signal size of about 3600 electrons/MeV, it is about 20 % of the signal yield of the same CsI(Tl) layer coupled to the Hamamatsu photodiode. This loss may be due simply to inadequate light collection by the a-Si:H diode. Despite this loss, signal enhancement of a factor of 8 over direct detection is achieved by using this scintillator.

4-8-2. β particle detection

An evaporated CsI(Tl) layer 315 μm , 461 μm or 894 μm thick was coupled to the Hamamatsu photodiode S1723-04. The 894 μm layer was peeled off from the substrate and was coupled to the photodiode directly. Other layers were coupled through their roughened glass substrates. This photodiode was used, due to its small capacitance (50 pF), to reduce the noise. A thin Al reflector was put on the scintillator for better light

collection. This assembly was exposed to β particles from a Bi-207 source. The output of the photodiode was amplified by the charge-sensitive preamplifier (Tennelec TC170) and shaped by a quasi-Gaussian shaping amplifier (6.4 μ s peaking time). The resultant pulse height spectrum is shown in Fig.4-23. The solid and open circles and solid triangles in Fig.4-22 show the spectra obtained with 315 μ m, 461 μ m and 894 μ m CsI(Tl) layer respectively. The open triangles show the spectrum when the source was out.

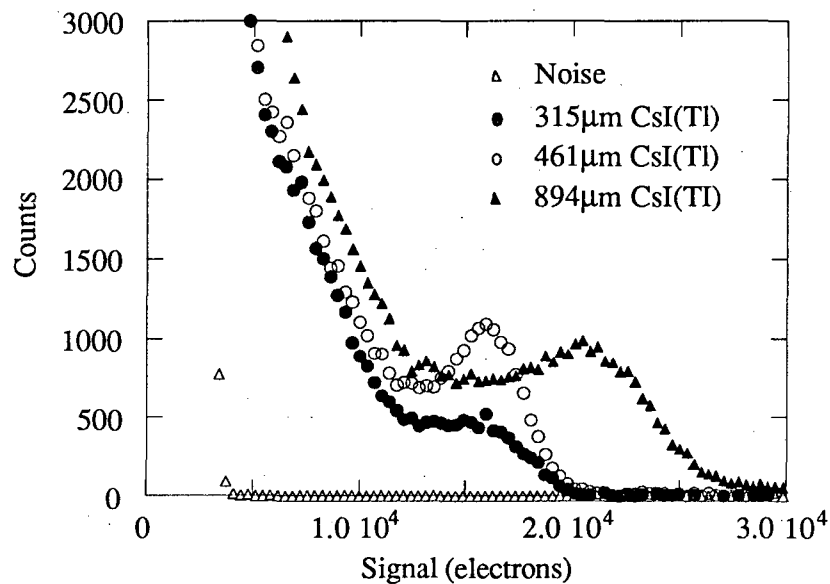


Fig.4-23 Detection of β particles from a Bi-207 source by an evaporated CsI(Tl) layer coupled to a photodiode.

A significant amount of low energy counts in Fig.4-23 for all of the three scintillators suggests that a large number of electrons escape from these layers. When the layer thickness is comparable to the electron range, some electrons deposit their full energy, creating a peak in the pulse height spectrum [18]. The pulse height spectrum shown by the open circles in Fig.4-24 was obtained by tilting the 461 μ m CsI(Tl) coupled with S1723-04 by 60° against the β particle incident direction, thus by increasing the effective thickness of CsI(Tl) by a factor of two. The fact that the peak appears at the same energy confirms

that this peak is the full energy peak. The 1 MeV monoenergetic β particles from the Bi-207 source create a peak only with the thicker CsI(Tl) layers. The 894 μm layer gave a larger signal yield than the other two layers probably because of better coupling to the photodiode.

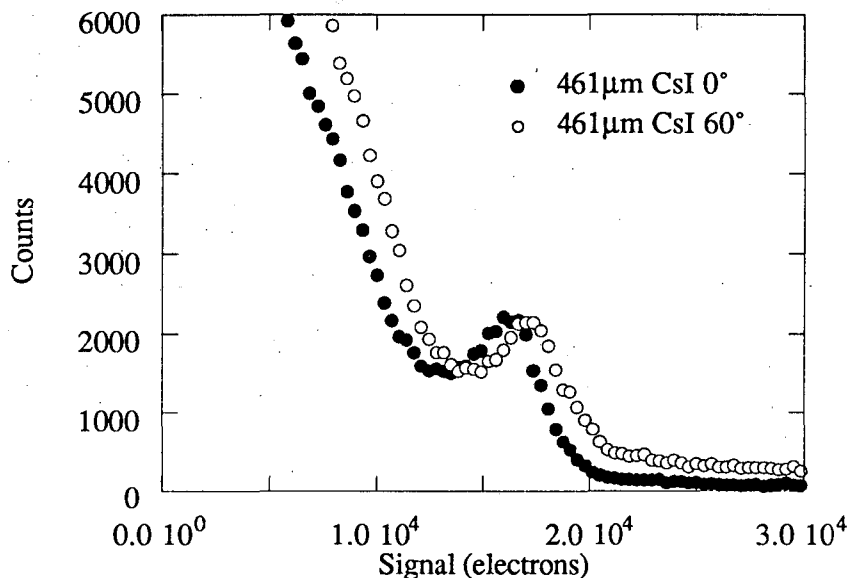


Fig.4-24 Detection of β particles from a Bi-207 source by an evaporated CsI(Tl) layer coupled to a photodiode for normal incidence and 60° incidence.

Finally, the Hitachi scintillator (1.1 mm thick $\text{GdO}_2\text{S}_2:\text{PrCeF}$) was compared with the 461 μm CsI(Tl) for detecting β particles from the Bi207 source. Almost the same pulse height spectra were obtained from the Hitachi scintillator as from the 461 μm thick CsI(Tl) layer shown in Fig.4-25. The previous analysis on the X-ray measurements showed that this Hitachi scintillator has almost the same signal yield with a slightly longer decay constant with CsI(Tl). The result of similar spectra for the two scintillators in Fig.4-25 is consistent with the X-ray analysis on the signal yields and also with the discussion about the nature of the peak.

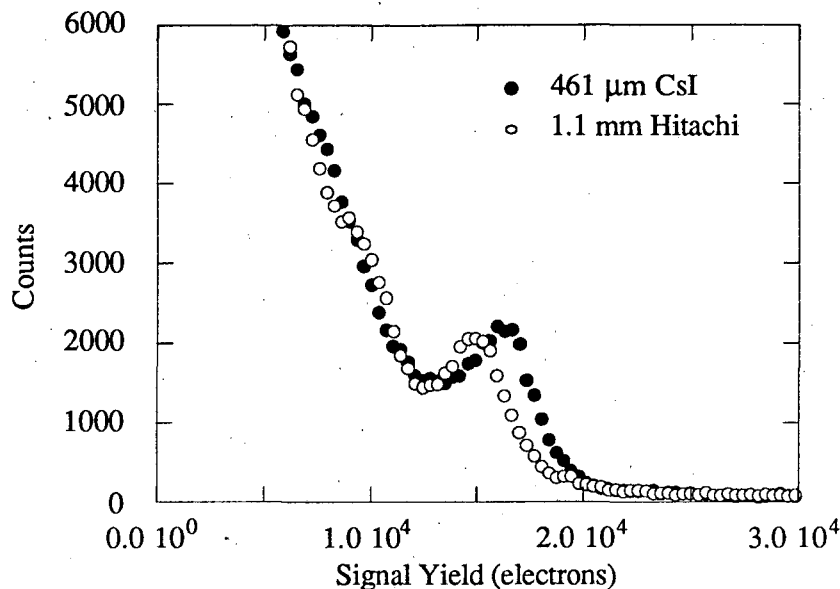


Fig.4-25 Detection of β particles from a Bi-207 source by an evaporated CsI(Tl) layer and the Hitachi scintillator coupled to a photodiode.

Based on detection of single 1 MeV β particles, we can extrapolate to project that high energy minimum ionizing particles will produce $\approx 10,000$ e-h pairs for a $500 \mu\text{m}$ thick CsI(Tl) layer coupled to a 70% quantum efficiency photodiode since they deposit about 500 keV in a $500 \mu\text{m}$ thick CsI(Tl) layer. The signal to noise ratio would be larger than 10 if noise can be reduced to less than 1,000 electrons. Noise is expected to be reduced by making a smaller area device since the dominant component of noise is the delta noise, which is proportional to the detector capacitance [ref.4 of Chapter 3]. The individual element of an a-Si:H photodiode array would have much smaller areas and therefore lower capacitance.

4-9. Monolithic X-ray sensor

The scintillator materials were coupled to a photodiode through a glass or a transparent plastic plate in all the cases discussed so far. The Hamamatsu photodiode was extensively used because of the calibrated quantum efficiency and the low noise characteristics and it

permitted independent testing of phosphors. In combining the CsI with an a-Si:H photodiode, it is advantageous to have the CsI evaporated directly on an a-Si:H photodiode for (a) a better optical coupling between the two [19] and (b) no lateral spread of scintillation light. The structure of the monolithic X-ray sensor is schematically shown in Fig.4-26.

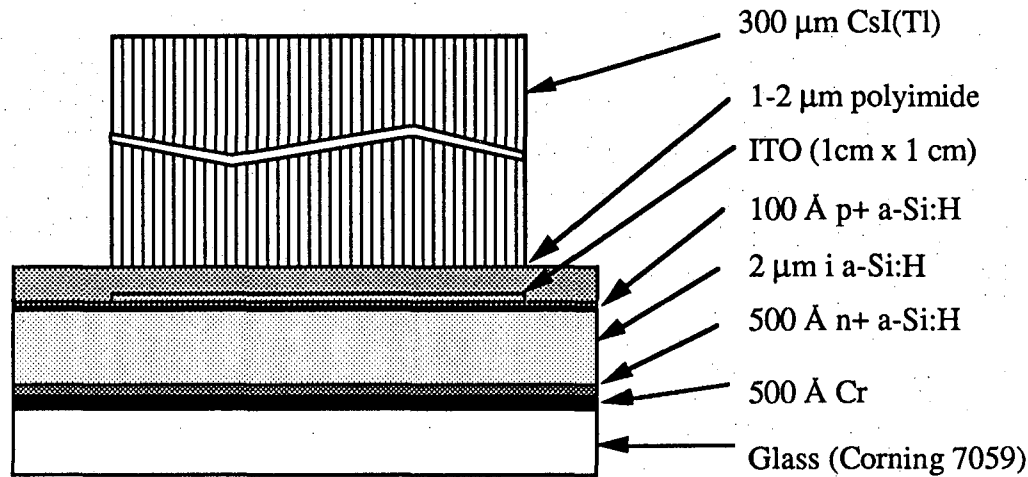


Fig.4-26 Monolithic X-ray sensor consisting of CsI(Tl) and a-Si:H photodiode.

The a-Si:H diode shown in Fig.4-26 was fabricated by the GD method. The top electronically conductive contact was made by sputtering about 1000 Å thick of indium tin oxide (ITO) on the top surface. This forms an optically transparent and electronically conductive layer [20]. We found that the CsI evaporated directly on the ITO coated a-Si:H did not adhere well. Some coating materials such as MgO, SiO₂, SiO were evaporated on the ITO in the hope of improving the CsI adhesion but these attempts were unsuccessful. Finally, we tried a high temperature plastic, polyimide. Polyimide layer has been used as a passivation layer for microelectronics applications. We found that it did improve adhesion of the CsI to the ITO. The polyimide PI-2555 (DuPont) was spin-coated (at 6000 rpm for 30 sec) on the ITO layer and it was baked at 200 °C for 30 min. Although the polyimide layer looked slightly yellow, the absorption of the CsI(Tl) scintillation was almost negligible. This was checked by comparing the signal yields when a polyimide layer

coated glass was inserted between the scintillator and the photodiode and when a similar glass without polyimide coating was inserted. Finally, CsI(Tl) 317 μm thick was evaporated with the optimized parameters.

This monolithic device was exposed to the 50 kV X-ray pulses. The pulse width was adjusted so that the energy contained in a single pulse was 10 MeV or 20 MeV. The pulse height spectra were obtained under these conditions and are shown in Fig.4-27. The threshold was set at the signal level of 1.0×10^5 electrons due to the large noise ($\approx 5 \times 10^4$ electrons FWHM) arising from the large capacitance of the a-Si:H diode (5000 pF). The signal yield of 1.2×10^4 electrons/MeV was obtained. This was about one half of the signal yield obtained by the other CsI(Tl) layer evaporated at the same time and coupled to the Hamamatsu photodiode. The loss can be explained by inferior quantum efficiency of this particular a-Si:H diode because of light absorption in the p^+ a-Si:H layer. Based on the results by Weisfield and Tsai [21], making the p^+ layer with a-SiC:H rather than plain a-Si:H will reduce the p-layer absorption of the scintillation light to less than 5%.

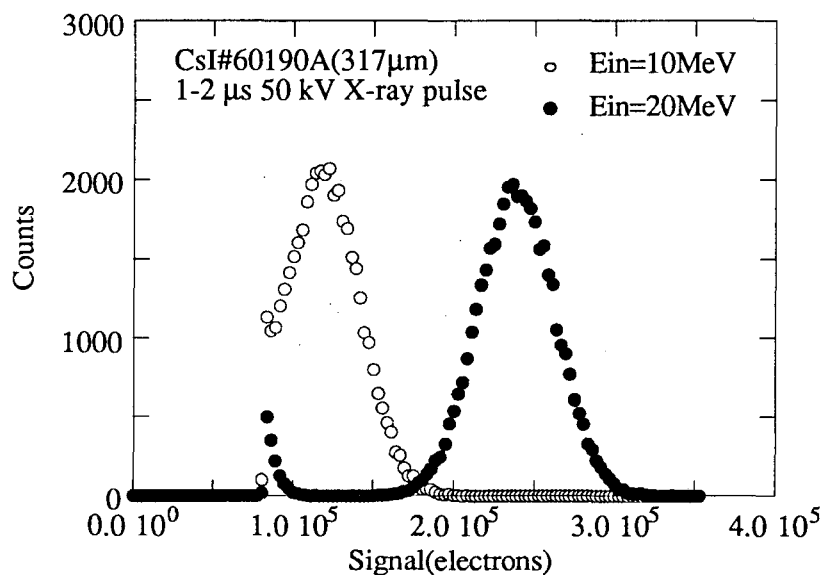


Fig.4-27 Monolithic X-ray sensor consisting of CsI(Tl) and a-Si:H photodiode.

4-10. Conclusion

CsI(Tl) layers 100-900 μm thick were evaporated from crystal CsI(Tl) chips on glass substrates as well as on a-Si:H layers. They were found to give light yields and speed comparable to crystal CsI(Tl). Concentration of Tl atoms in the evaporated layers was measured and correlated with the light yield. The light spread inside the scintillator was suppressed by the columnar structure of the evaporated CsI(Tl) layers. A prototype monolithic X-ray sensor consisting of CsI(Tl) and a-Si:H photodiode was fabricated. Further improvement in signal amplitude for this device is possible by optimizing the photodiode performance as well as the optical collection efficiency. Full implementation for most applications will require reducing the area of individual diode elements for low noise operation.

Appendix Summary of various evaporation runs

The table below summarizes the CsI(Tl) fabrication and measurement data. The first column (CSI#) shows identification numbers for our evaporated layers. The first three are crystal CsI(Tl) made by Bicron. The rest is our evaporated CsI(Tl) under various conditions. The ID # is also the date of evaporation run. Samples labeled as "A" were centered over the boat and those labeled "B" were off-centered except for the #21590" samples which were both off-centered. The thicknesses shown in the second column were calculated from the measured weight increase of the glass substrates, assuming the same density of CsI(Tl) (4.51 g/cm³). The physical thickness measured by micrometer was 10-20 % larger than these calculated values, indicating the density of evaporated layers is 10-20 % less than crystal CsI(Tl). The signal yield was measured using the 50 kV X-ray pulse and is expressed in terms of electrons per unit energy absorbed in the scintillators. A thin Al reflector was placed on top of the scintillator to measure the yields shown in the fourth column. The evaporation rate was obtained by dividing the thickness of the "A" sample by the evaporation time. The Tl concentration was obtained by the X-ray Reflection Fluorescence (XRF) analysis.

Table Summary of various evaporation runs

CSI#	Thickness (μm)	yield w/o Al (e/MeV)	yield w/Al (e/MeV)	Evp.rate ($\mu\text{m}/\text{min}$)	Tl (m/o)
AI-409	5000		20246		
DEMO	5000		9796		
2R.197	5000		7184		
#21590A	307		3953		
#21590B	307		4282		
#32390A	165		1760		
#32890	532		2027		
#40390A	372		1553		
#40390B	135		3486		
#40590A	335		2241		
#40590B	135		2490		

CSI#	Thickness (μm)	yield w/o Al (e/MeV)	yield w/Al (e/MeV)	Evp.rate ($\mu\text{m}/\text{min}$)	TI (m/o)
#40690A	257		4229		
#40690B	101		5472		
#41890A	312		3276	15.6	
#41890B	104		4762		
#42490A	233	12856	19686	6.7	
#42490B	100	14045	20734		
#42590A	319	5326	7101	9.1	
#42590B	137	8080	10774		
#50190A	302	5064	7958	8.6	
#50190B	132	7725	10484		
#50290A	461	4689	6630		
#50290B	191	7532	9304		
#50390A	301	5432	7605	7.52	
#50390B	126	9105	11950		
#50490A	275	17982	27723	4.583	
#50490B	132	17665	27050		0.032
#50790A	238	17908	29052	2.311	
#50790B	88	17652	29421		
#50890A	315	18550	29965	3.28	0.019
#50890B	125	20030	30332		
#52490A	461	16825	27180	3.5	
#52490B	194	16264	23297		
#60190A	317			3.52	
#60190B	122	12796	19776		
#60690A	318	15290	23469	3.35	0.010
#60690B	139	14415	20288		
#61190A	463	10668	15841	4.873	0.004
#61190B	179	11458	16042		
#61890A	308	16534	24801	3.28	
#61890B	124	16109	21863		
#72390A	425		27992	4.0	0.089
#72390B	177		28760		
#81790A	894	11449	18989	4.0	
#81790B	340	11151	15931		
#82290A	1001		20342	4.16	
#82290B	452		14556		
#90690A	1027		17828		
#90690B	362		16904		

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Chapter 5. Applications of a-Si:H radiation detectors

It has been shown that a-Si:H is capable of detecting radiation either directly in a thick diode or indirectly in a phosphor material coupled to a thin photodiode. Although this is sufficient as a radiation monitor, a wider range of application will require addition of position sensitivity to a-Si:H radiation detectors. Schemes to achieve position sensitivity and some expected application fields of radiation imaging devices are discussed in this chapter.

5-1. Position sensitivity

One dimensional (1D) or two dimensional (2D) position sensitivity may be achieved by dividing the electrode into narrow strips or small pixels and providing individual readout electronics for each strip or pixel. This is shown schematically in Fig.5-1. One-dimensional readout electronics for single-particle detection is available in the form of crystalline silicon integrated circuit chips such as the SVX chip designed at LBL for high energy physics experiments [1]. The top electrode of an a-Si:H diode is divided into narrow strips and each strip is wire-bonded to the separate amplifier channel of these IC chips to take advantage of the existing crystalline Si electronics. Single-particle-counting strip detectors can be built this way as shown in Fig.1-5 (a). A strip or pixel device with the front end electronics made from a-Si:H or poly-Si TFTs on each strip or pixel as shown in Fig.1-5 (b) and (c) has been proposed [2,3]. For fluence detection, the readout electronics could be simple switching devices to address strips sequentially as in the case of facsimile heads. For individual particle or single γ photon counting, the readout electronics must contain a high-gain charge-sensitive amplifier stage and a sequential addressing stage as is contained in the SVX chip [1]. As shown in Fig.5-1 (c), vertical integration of the electronics and the addressing bus lines would enable readout of each pixel signal without reducing the sensitive area to radiation. Also shown is a schematic of the electronics on each pixel. Again, fluence detection needs a simple switching device to

address X and Y coordinates while single particle counting requires a charge-sensitive amplifier stage in addition to the addressing stage.

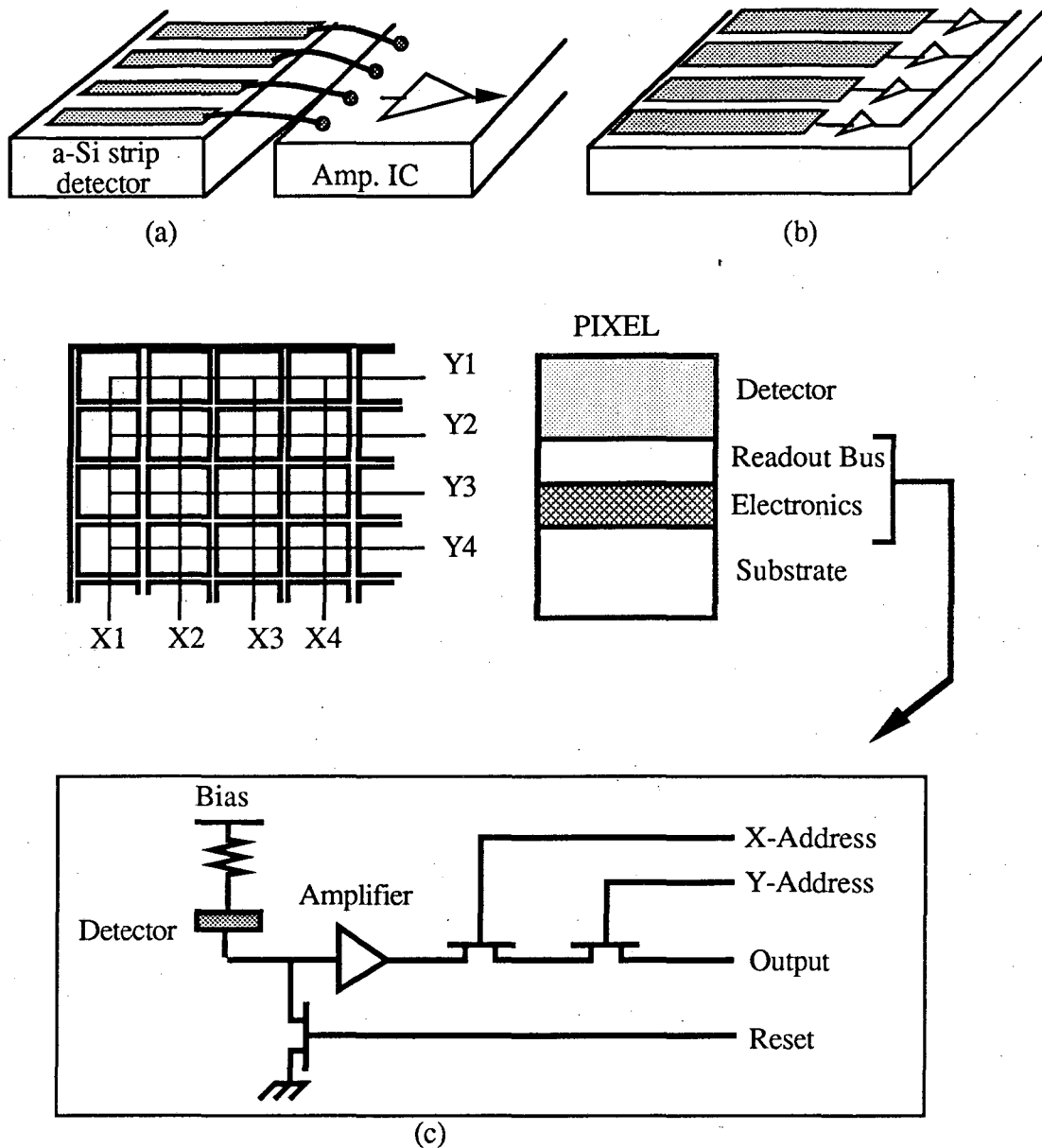


Fig.5-1 Schematics of position sensitive detectors (PSDs).
 (a) One dimensional PSD (strip detector).
 (b) One dimensional PSD with the built-in readout electronics made of a-Si:H or poly-Si thin film transistors (TFTs).
 (c) Two dimensional PSD (pixel detector) with the built-in readout electronics made of a-Si:H or poly-Si TFTs.

5-2. High energy physics applications

A number of secondary charged particles are emitted following a collision event of primary particles in high energy physics experiments. These particles can be detected by

position sensitive a-Si:H radiation detectors surrounding the collision site as shown schematically in Fig.5-2. Tracks of each particle can be identified by processing information from each detector. Thick a-Si:H layers formed into small pixels are essential to provide high signal to noise (S/N) ratio for direct detection of these particles. With small pixel detectors, noise contributions from the detector capacitance, leakage current and the carrier trapping and emission in a-Si:H layer are expected to be low enough to provide S/N ratio high enough for direct detection of the minimum ionization events. Due to the large area and high radiation resistivity required for this tracking device, a-Si:H radiation detectors may have advantages over the crystalline devices currently under development [4].

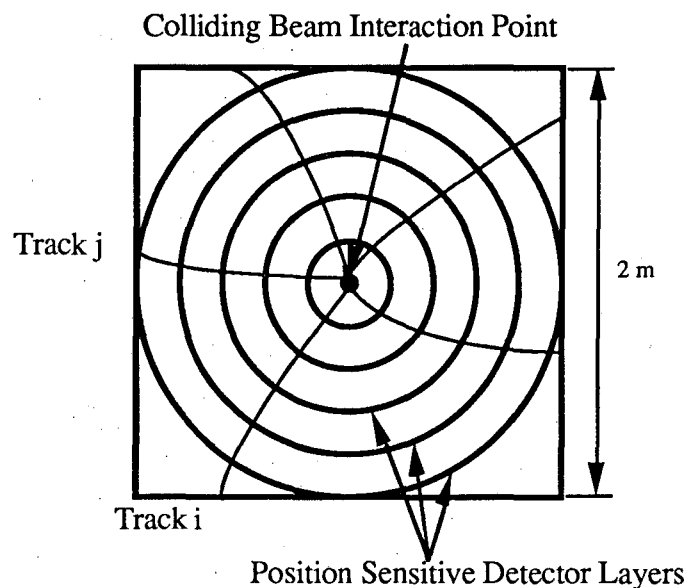


Fig.5-2 Tracking device. Particle tracks are identified by processing the information from the PSDs surrounding the colliding beam interaction point.

5-3. Medical imaging

There are many radiation detection devices used in medical imaging ranging from simple ones such as X-ray film, to elaborate systems such as a Positron Emission Tomograph (PET). We discuss the use of a-Si:H detectors in medical imaging by dividing these

existing devices into two groups i.e. flux imaging devices and single photon imaging devices.

5-3-1. Flux imaging devices

X-ray film has been widely used due to its excellent spatial resolution despite the limited dynamic range, non-linear response, lack of real-time capability, inability for repeated use and need for additional efforts to obtain digitized images. The imaging plate developed by Fuji [5] solves most of the deficiencies of film except for the real-time capability. It requires a separate readout process to "develop" an image. An X-ray image intensifier is a real-time device and is often used in X-ray digital imaging [6]. The detector system used for digital radiology consists of an image intensifier and a TV camera for image digitization. Since an image intensifier is a vacuum tube, it is bulky and mechanically delicate.

A pixel detector combined with a scintillation faceplate as shown in Fig.5-3 could be used in place of these devices. The TFT front-end electronics as shown schematically in Fig.5-4 read out charges stored in each pixel. A simple calculation below shows that the expected signal size is large enough to simplify the requirements for the readout electronics. Consider a typical X-ray exposure of 0.1 Roentgen as normally used for chest X-ray imaging. This corresponds to approximately 3×10^9 50 keV X-ray photons/cm². About 80 % of these X-ray photons would be absorbed by a 300 μm -thick CsI(Tl) layer. From the known light yield of 50,000 visible photons/1MeV absorbed for CsI(Tl), the 0.1R exposure produces 6×10^{12} photons/cm² in the CsI(Tl). As a conservative estimate, assume 50 % light collection and 60 % light to electron-hole pair conversion efficiency (quantum efficiency) of the a-Si:H photodiode. If we have 10^4 pixels/cm² (100 μm x 100 μm pixel size), each pixel produces 1.8×10^8 electron-hole pairs. The capacitance of a 1 μm thick 100 μm x 100 μm pixel is about 1 pF. Therefore, the average voltage signal on each pixel after 0.1R exposure is about 30 V. The spatial resolution is determined by the pixel size and the scintillation light spread in the phosphor. The output image is

inherently digitized and well-suited for sophisticated digital image processing techniques. The dynamic range required for these applications is about 1000. Whether these devices are capable of achieving this dynamic range depends on many design considerations and is beyond the intended scope of this chapter.

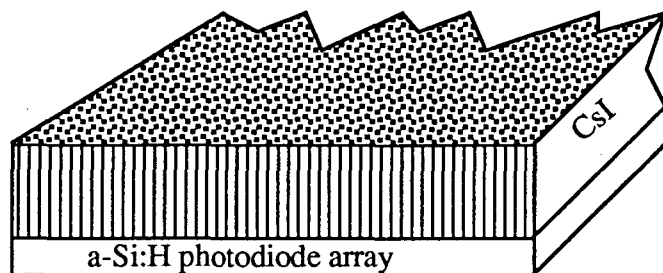


Fig.5-3 2D-PSD. A 2D a-Si:H photodiode array combined with either a 300 μm CsI(Tl) with fine columnar structure or a scintillation faceplate containing a rare-earth element can be used for recording X-ray transmission images.

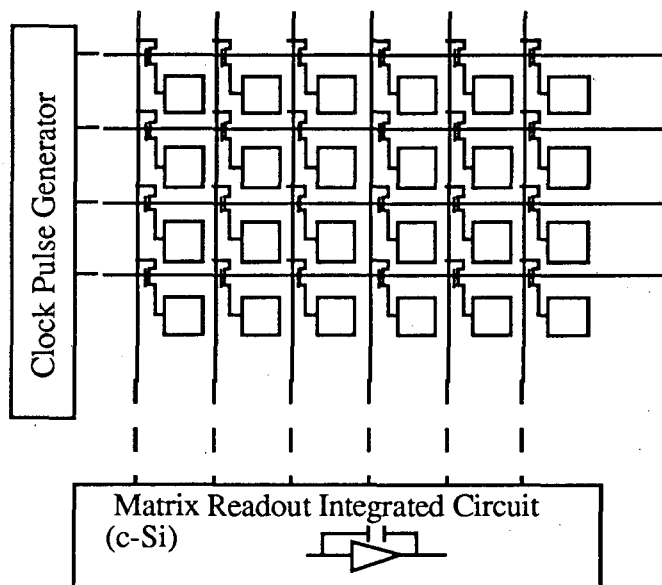


Fig.5-4 Readout electronics for the 2D array.

An X-ray Computed Tomograph (CT) is a more elaborate high flux imaging system [7], but the detector itself is not necessarily more sophisticated. A schematic drawing of an X-ray CT is shown in Fig.5-5. An X-ray-tube emitting a fan beam of X-rays is rotated around the body together with a one dimensional position sensitive X-ray detector (1D-

PSD). The transmission data through the patient's body known as projection profiles are stored in the computer for each X-ray-tube position and are used to reconstruct the transverse cross-sectional image of the X-ray attenuation coefficient distribution. An array of Xe-filled ionization chambers is normally used as the 1D-PSD. Due to the geometrical magnification, the spatial resolution of this detector can be as low as a few mm. A strip detector as shown in Fig.5-1(a),(b) combined with a phosphor should work as well as the Xe chamber. Again, no elaborate readout amplifier is needed since it is a flux detector.

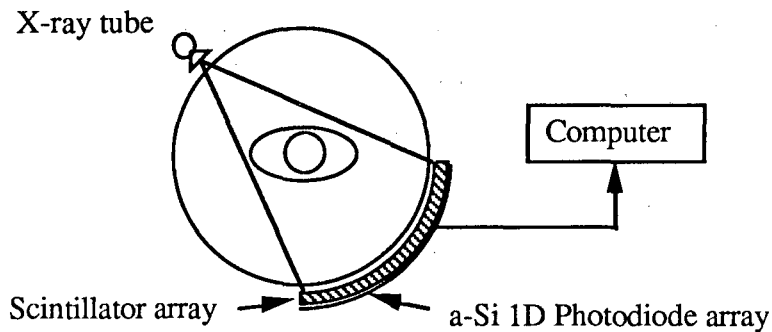


Fig.5-5 A 1D a-Si:H photodiode array combined with a similar 1D scintillator array can be used in place of the Xe-filled ionization chamber currently employed in an X-ray CT.

5-3-2. Single photon imaging devices

In nuclear medicine, radiopharmaceuticals are introduced into the body and γ rays emitted from the body are detected to give an image of the radionuclide distribution. Here, it is necessary to count individual γ rays, unlike the flux detectors where the charges created by a number of X-rays are accumulated during the duration of the X-ray pulse. The scintillation camera is an example of this type of imaging device. It consists of a slab of NaI(Tl) coupled to a number of photomultiplier tubes (PMTs). Centroid calculation of the PMT outputs gives the position information of the incident γ ray and the sum of all the PMT outputs gives the energy of the γ ray. A mechanical collimator is placed in front of the scintillator to accept only the desired γ rays to make an image of the radionuclide distribution in the body projected on the plane defined by the scintillator slab. A pixel

device combined with a high light yield scintillator faceplate as shown schematically in Fig.5-6 can replace this scintillation camera. Here, the scintillator must be carefully chosen and CsI(Tl) appears promising because of its high light yield, high atomic number and its built-in columnar structure. The thickness of CsI(Tl) should be more than 5 mm to ensure good detection efficiency for the most-commonly used 140 keV γ rays from the Tc-99m compounds. The intrinsic spatial resolution of conventional scintillation cameras is more than 3 mm. A pixel device shown in Fig.5-1 can have much higher spatial resolution if required. Since it doesn't have PMTs, it is compact, light weight, and would need less routine adjustments than the conventional PMT array.

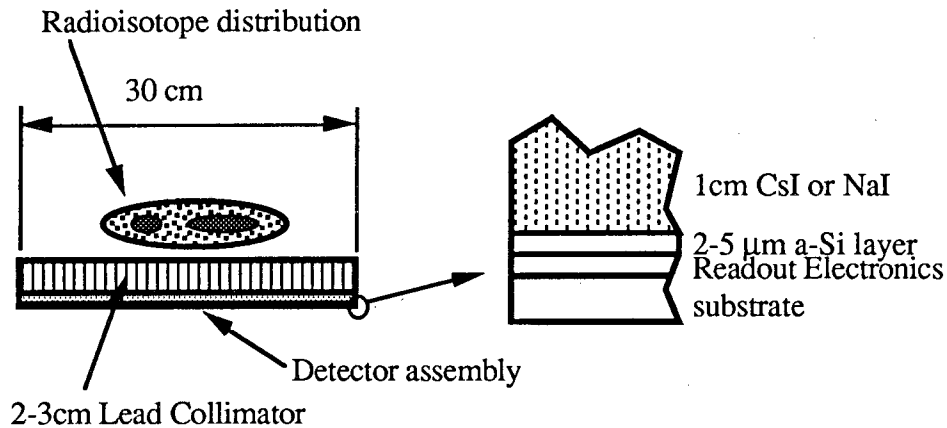


Fig.5-6 Scintillation camera

5-4. Material science and life science applications

Radiation detectors are used in material science for recording diffraction patterns in X-ray crystallography and X-ray or synchrotron radiation transmission images in non-destructive studies of materials. Bateman gives a review of the existing devices used for these studies [8]. As pointed out there, the principal restriction on the wide-spread use of semiconductor detectors is their limited area. A-Si:H may be a better choice for the detector material for such applications.

In X-ray crystallography, monoenergetic characteristic X-rays such as the Cu α -line (8 keV) are detected by curved or flat 1D-PSD such as film or multiwire proportional chambers(MWPC) [9,10]. A-Si:H alloys on flexible substrates can be used in place of film or MWPC detectors. Since the diffracted X-ray flux intensity is low for these studies, single photon detection is preferred over flux integration. The pulse mode operation (counting a single X-ray photon) of the indirect detection scheme is somewhat difficult because, for example, one 8 keV X-ray photon creates only about 400 photons in CsI(Tl). Direct detection of a single 8 keV X-ray photon needs careful design of the readout electronics because the expected number of electron-hole pairs is around 1300. Table 5-1 summarizes the probability of photoelectric effect of 8 keV and 17 keV X-ray photons in three kinds of a-Si:H alloys i.e.100 % Si, 90 % Si + 10 % Ge, 80 % Si + 20 % Ge. A-Si:Ge:H alloys with good quality can be made with the Ge content up to 20 %. Layer thicknesses of 30-50 μm could provide similar detection efficiency as the pressurized Xe-filled MWPCs.

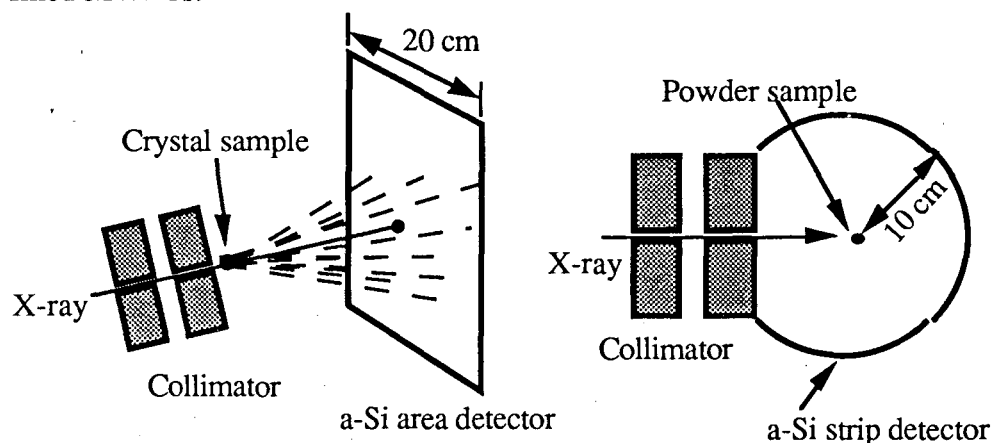


Fig.5-7 X-ray deflection experiment applications

Table 5-1. Detection efficiency of a-Si:Ge:H alloys

X-ray energy	100% Si		90% Si + 10% Ge		80% Si + 20% Ge	
	30 μm	50 μm	30 μm	50 μm	30 μm	50 μm
8 keV	0.37	0.53	0.45	0.63	0.53	0.72
17keV	0.051	0.083	0.18	0.28	0.31	0.46

In non-destructive studies of materials, where a flux of higher energy X-rays is employed, either direct or indirect detection of these X-rays is possible by the a-Si:H detectors operated by the integral mode (flux counting).

Even for X-ray microscopy [11] where the size advantage of a-Si:H is less significant, its high radiation resistivity is a desirable feature for these devices. When a large geometrical magnification is employed by making the ratio of the X-ray source-object distance to the object-detector distance small, a large detector area such as $10 \times 10 \text{ cm}^2$ becomes important again.

In biology, autoradiography is a powerful tool for obtaining information on protein distributions and DNA sequences from radioisotope-labelled plates of organic bodies and electrophoresis gels. Radioisotopes commonly used for these studies are H-3, C-14 and P-32. These are all β -emitters whose maximum energies are 18 keV, 155 keV and 1.7 MeV, respectively. Positions and intensity distributions of these β -emitters are found by detecting the β particles escaping from the plate or the gel with a 1D, or more recently 2D-PSD in close contact. X-ray film has been used widely. Multiwire proportional chambers (MWPC) have been applied for 1D or 2D radiochromatography [12]. To ensure sufficient sensitivity, the thickness of the gas-filled detector is about 1cm, causing spatial resolution degradation due to the parallax error. Single β particle detection for these radioisotopes can be done by a-Si:H radiation detectors either directly or indirectly with the exception of H-3. The detector thickness can be less than 1 mm and the sensitive area can be placed very close to the plates or gels. H-3 detection requires careful design considerations on the readout electronics due to the small signal size.

5-5. Conclusion

Some expected application areas of a-Si:H radiation detectors were discussed. Although the basic possibilities of detecting radiation with a-Si:H layers have been demonstrated in previous chapters, extensive further design and development considerations will be required for a prototype fabrication for each of these application field described in this chapter. Design considerations for the readout electronics for single particle detection and noise measurements of the a-Si:H and poly-Si TFTs are under way and some results have been published [2,3]. While this electronics development needs a fair amount of effort, such effort could be shared with industry which should find a large market for a-Si:H radiation detector applications such as described in this chapter.

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Chapter 6. Conclusion

Hydrogenated amorphous silicon (a-Si:H) has been investigated as a material for radiation detectors.

Charge collection and signal generation process are analyzed in terms of carrier mobilities, lifetimes and electric field. Charge collection in thick a-Si:H diodes is often limited by deep-level trapping of carriers during transit and a finite charge integration time required for single particle counting in some applications and sometimes by volume recombination of carriers for detecting heavily-ionizing particles such as α particles. The charge collection process is also strongly affected by the non-uniform electric field profiles in a-Si:H diodes caused by the fixed space charges inside the material under reverse-bias. Signal generation due to a weak light pulse irradiating each end of a thick diode is measured as a function of a reverse-bias and it gives a valuable information about the fixed space charges.

Field profiles in a-Si:H radiation detectors can be manipulated by either doping, electrode geometry, or a combination of both. The buried p-i-n structure improves the charge collection process by allowing one to apply higher bias while suppressing the near-surface breakdown phenomenon. It also gives a bias safety margin before breakdown for each element of a large area detector. Making the p layer of the conventional p-i-n diode thicker also enables high bias operation. Some other ideas to shape the field profile and their calculation results show that thicker a-Si:H layers may become fully-depleted with a non-uniform field profile. The microstrip electrode provides a similar charge collection efficiency to the parallel plate configuration with a smaller capacitance.

X-ray detection with a good sensitivity is demonstrated by an a-Si:H photodiode coupled to an evaporated CsI scintillator. The scintillation quality of evaporated CsI layers can be made almost identical to its single crystal counterpart.

These a-Si:H radiation detectors will find their applications in high energy physics experiments, medical imaging, material science and life science. Each application imposes different requirements on the radiation detector systems. Whether a-Si:H detectors can replace existing devices such as film, gas-filled detectors, scintillation detectors and crystalline semiconductor detectors strongly depends on each application and it must be judged case by case. However in general, the following advantages of the a-Si:H detectors over the existing devices are expected:

- (a) larger area, higher radiation resistance and lower cost than crystalline semiconductor detectors.
- (b) higher spatial resolution and detection efficiency than gas-filled detectors.
- (c) they are better suited for medical digital radiography than film or image intensifiers.

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